

UNIVERSITÀ DEGLI STUDI DI PADOVA DIPARTIMENTO DI INGEGNERIA INDUSTRIALE CORSO DI LAUREA IN INGEGNERIA MECCANICA

Tesi di Laurea Magistrale in Ingegneria Meccanica (classe LM-33, codice IN0518)

Acquisizione dei carichi aerodinamici della vettura Formula S.A.E. MG 13.18

Relatore: Prof. Giovanni Meneghetti

> Laureando: Daniele Tassetto 1153747

Anno accademico 2017-2018

Alla mia famiglia

Indice

1.	Introd	uzione	1				
1.	1	La competizione F.S.A.E.	1				
1.	2	L'evento					
1.3		Il Race Up team	4				
1.4		L'aerodinamica	5				
1.	2	La C.F.D. nel Race Up Team					
2.	Proget	tazione e realizzazione delle celle di carico	11				
2.	1	Individuazione dei siti di lavoro					
2.	2	Progettazione delle celle di carico					
2.	3	Produzione delle celle di carico	20				
2.	4	Preparazione del sistema di misura	22				
	2.4.1	Scelta della disposizione degli estensimetri	22				
	2.4.2	Preparazione delle superfici e tracciatura	24				
	2.4.3	Posizionamento ed incollaggio degli estensimetri	26				
	2.4.4	Cablaggio	30				
	2.4.5	Strumenti di acquisizione dei dati	33				
2.	5	Montaggio	36				
3.	Calibr	azione delle celle di carico	43				
3.1		Allestimento del banco per il fissaggio.					
3.	2	Applicazione dei carichi					
3.	3	Matrici di taratura					
4.	Config	razione del sistema di misura					
4.	1	Strumentazione per l'acquisizione dei carichi aerodinamici.	59				
4.	2	Strumentazione per l'acquisizione di segnali aggiuntivi	61				
4.3		Configurazione software del sistema di misura	65				
4.4		Verifica dei segnali di acquisizione					
5.	Test in	pista	71				
5.	1	Sito dei test	71				
5.	2	Prove in programma	72				
5.	3	Sessione di test	76				
6.	Analisi	i preliminare dei dati acquisiti	79				
6.	1	Analisi dei dati originali	79				
6.2		Filtraggio dei dati					
6.3		Post-processing dei dati					
7.	Analisi	i dei dati e presentazione dei risultati	85				
7.	1	Configurazioni aerodinamiche delle ali	85				
7.	2	Prove in rettilineo a velocità costante	88				
	7.2.1	Run in configurazione "Alto carico"	88				
	7.2.2	Run in configurazione "Medio carico"	95				

	7.2.3	Run in configurazione "Basso carico"	102
	7.2.4	Run in configurazione "Basso carico" all'ala anteriore e "Alto carico" all'ala posteriore.	109
	7.2.5	Run in configurazione "Medio carico" all'ala anteriore e "Alto carico" all'ala posteriore	
			116
7.3		Tracciato	123
7.4		Prove circolari a velocità costante	127
	7.4.1	Run sulla circonferenza di 6.125 metri	127
	7.4.2	Run sulla circonferenza di 9.125 metri	129
	7.4.3	Run sulla circonferenza di 12.125 metri	130
7.5		Discussione dei risultati ottenuti	131
8.	Conclus	sioni	133
Bib	liografia		137
App	endice A	: Centro di pressione	139
App	endice E	: Elaborazione dati taratura	141
App	endice C	: Codici Famos	145

Sommario

Obiettivo della tesi è acquisire dati sperimentali relativi ai carichi di downforce e drag dell'ala anteriore e dell'ala posteriore della vettura F.S.A.E. MG 13.18 del Race Up team dell'Università di Padova. Questi dati servono a convalidare i modelli C.F.D. che il team utilizza per progettare i componenti aerodinamici e per simulare il comportamento dinamico della vettura durante gli eventi a cui partecipa.

A tal fine sono state progettate delle celle di carico multicomponente che vanno a sostituire gli originali supporti delle ali. Ciascuna di queste celle di carico è stata studiata per avere un canale sensibile alla downforce e un canale sensibile al drag.

Dopo la progettazione, produzione, estensimetraggio e cablaggio e dopo il processo di taratura, le celle di carico sono state montate in vettura e utilizzate per una sessione di test nella quale sono stati acquisiti i dati che, dopo elaborazione numerica, hanno confermato i dati precedenti ottenuti con simulazioni C.F.D.

CAPITOLO 1: Introduzione

1.1 La competizione F.S.A.E.

La Formula S.A.E. è una competizione studentesca universitaria organizzata dalla Society of Automotive Engineers (S.A.E.) che prevede la progettazione e la produzione di un'auto da corsa, valutata durante una serie di prove in base alle sue qualità di design e di efficienza ingegneristica [1].

Istituita nel 1981, con lo scopo di dare agli studenti universitari la possibilità di confrontarsi in un evento che risulti quanto più appetibile e interessante, la competizione è ad oggi diffusa in tutto il mondo (Figura 1.1), con dieci eventi annuali, organizzati direttamente dalla SAE in collaborazione con le associazioni nazionali di ingegneri e tecnici dell'automobile.



Figura 1.1: Foto panoramica di tutti i team partecipanti alla Formula Student Germany 2009

L'idea posta alla base della Formula SAE è che un'azienda fittizia ingaggi il team affinché realizzi un prototipo di auto da corsa con determinate caratteristiche, che occupi il segmento di mercato dedicato a piloti non professionisti, definiti come "weekend autocross racers". Ogni team di studenti deve progettare, costruire, testare e promuovere il prototipo, il quale viene valutato in otto tipologie di prove, che spaziano dalle performance pure, al design, alla pubblicizzazione e alla presentazione del veicolo stesso.

Ad ogni evento è possibile totalizzare un massimo di 1000 punti distribuiti tra varie prove secondo il seguente schema:

- Presentazione piano aziendale: 75
- Presentazione design vettura: 150
- Analisi dei costi: 100
- Prova di accelerazione: 75
- Prova skid-pad: 50
- Prova autocross: 150
- Consumo carburante: 100
- Endurance: 300

Ogni evento FSAE si compone di quattro giorni di gare, dal giovedì alla domenica, in alcuni dei più famosi e importanti circuiti mondiali, come il Michigan International Speedway negli Stati Uniti, l'autodromo di Silverstone in Inghilterra o l'Hockenheimring in Germania. L'evento italiano si svolge presso l'Autodromo Riccardo Paletti di Varano de' Melegari solitamente verso la metà di luglio.

Gli eventi sono tutti accomunati dallo stesso regolamento, ma costituiscono ognuno una competizione indipendente, senza cioè contribuire ad assegnare punti per un campionato.

1.2 L'evento

Prima di poter partecipare alle prove guidate le vetture vengono sottoposte ad una serie di verifiche ed ispezioni, note comunemente come "techs", da parte dei giudici, che ne verificano la conformità al regolamento, soprattutto per quanto concerne le norme preposte a garantire l'incolumità del pilota.

A seguito del superamento dell'ispezione avviene il rifornimento della vettura, che viene successivamente posizionata sulla "tilt table", una piattaforma in grado di inclinare il prototipo su entrambi i lati fino ad un angolo di 60 gradi, al fine di certificare che non vi sia perdita di liquidi (Figura 1.2).



Figura 1.2: Tilt table

Eseguita la prova di tilt, la vettura viene sottoposta alla prova di "noise", nella quale i giudici accertano che il motore, girando a diversi regimi, non superi i 110 dB di rumore. L'ultima delle prove pre-gara è il test di frenata, in cui la macchina deve essere in grado, dopo una lunga accelerazione, di bloccare contemporaneamente tutte e quattro le ruote senza arrivare allo spegnimento del motore.

Superate le verifiche preliminari, si può accedere alle prove vere e proprie dell'evento, le quali iniziano con delle prove statiche:

- Cost analysis: L'obiettivo dell'analisi dei costi è quello di insegnare ai partecipanti quanto il costo in relazione al budget disponibile sia fattore imprescindibile, cui è fondamentale tenere conto in una progettazione ingegneristica. La prova è suddivisa in due parti: la compilazione di un report scritto (che deve essere inviato ai giudici prima della competizione) e una discussione durante l'evento stesso. Questo consente di valutare non soltanto il costo del prototipo, ma anche l'abilità del team di produrre una stima accurata dei costi di produzione e progettazione.
- Business plan presentation: Questa presentazione è finalizzata a valutare l'abilità del team nello sviluppare e consegnare un ampio ed esaustivo business case. L'evento è giudicato da persone provenienti dal mondo dell'automobile, pertanto il team deve ipotizzare di confrontarsi non soltanto con ingegneri, ma con un esecutivo rappresentate le varie aree di una società, inclusi manager di produzione, marketing e finanza. La valutazione è focalizzata al contenuto, all'organizzazione e all'illustrazione del progetto, nonché all'abilità nel rispondere alle domande dei giudici.
- Engineering Design: Il concetto alla base dell'evento è quello di valutare le scelte e gli sforzi progettuali e come essi incontrino le esigenze di mercato. Questi aspetti vengono valutati dai giudici in unione alla capacità del team di rispondere ai quesiti posti e all'ispezione della macchina, la quale deve essere presentata totalmente assemblata e pronta a gareggiare.

Finite le prove statiche, si procede con le prove dinamiche:

Acceleration: Durante il test di accelerazione la vettura deve accelerare per 75 metri lungo un percorso
rettilineo su superficie piana. La prova è divisa in due batterie, che devono essere corse da due
differenti piloti, ognuno dei quali ha a disposizione due tentativi. Il punteggio è determinato dalla

differenza tra il peggiore e il migliore tempo assoluti fatti registrare, tenendo presente che il tempo più alto preso in considerazione dai giudici non deve superare i 5.8 secondi, pari ad una velocità media di 46.55 km/h.

• Skid-pad: La prova valuta la capacità in curva della vettura. Il tracciato richiama la figura di un 8 con due cerchi di 15.25 metri, al cui centro è posta la linea di partenza/arrivo e delimitati da sedici piloti nel lato esterno e sedici lungo il lato interno di ciascun cerchio (Figura 1.3).



Figura 1.3: Tracciato della prova di Skid-pad

La vettura, una volta entrata nel percorso, deve compiere un giro del cerchio destro, per stabilire il senso di marcia, al termine del quale deve compierne un secondo, il quale viene cronometrato dai giudici. Terminato il secondo giro, la vettura deve spostarsi nel cerchio sinistro per effettuare due ulteriori giri, il secondo dei quali viene cronometrato. Ultimato il quarto giro, la macchina lascia il tracciato, nella stessa direzione da cui è entrata. La prova è divisa in due batterie, che devono essere corse da due differenti piloti, ognuno dei quali ha a disposizione due tentavi. Il punteggio è determinato sulla capacità di accelerazione laterale.

- Autocross: è una prova di sprint da eseguire su due giri di circuito per valutare la maneggevolezza della vettura. Il circuito è disegnato in modo da ottenere velocità medie comprese tra i 30 ed i 40 km/h e comprende brevi rettilinei (non più di 60 metri), curve a raggio costante (da 23 a 45 metri di diametro), tornanti (9 m di diametro esterno minimo), slaloms (coni a distanza compresa tra 25 e 40 m), chicanes e curve a raggio variabile. Per il punteggio vale il miglior tempo su due prove effettuate da piloti diversi
- Endurance and fuel economy: La prova di endurance è l'evento che chiude il week end di gare e mira a valutare le performance complessive del prototipo. Si svolge lungo un tracciato molto simili a quello in cui si corre la prova di autocross, per un totale di 22 km. Ai componenti del team non è permesso di intervenire sul veicolo durante lo svolgimento della prova, mentre è previsto un cambio di pilota a metà prova, durante un periodo di sosta di tre minuti. L'ordine di partenza viene stilato in base ai risultati dell'autocross, con il team più veloce primo a scendere in pista, seguito dal secondo e così via, scorrendo la classifica. Il tempo complessivo dell'endurance è dato dalla somma dei tempi di ciascun pilota, cui vanno a sommarsi le eventuali penalità, comparato con quello del team più rapido in pista. Nello stesso contesto dell'endurance viene stilata una classifica, e conseguentemente assegnati dei punti, per la fuel economy. Il risparmio di carburante rappresenta un aspetto fondamentale in molte forme di competizione motosportiva, oltre a mostrare quanto efficacemente sia stata preparata la vettura. Durante l'endurance non è consentito per questo alcun rifornimento di carburante. Il punteggio è basato sul conteggio della media di litri di carburante per chilometro durante la prova di endurance.

1.3 Il Race Up team

L'Università degli Studi di Padova partecipa agli eventi della Formula SAE dal 2006 con il nome di "Race Up team". Nel 2015 nasce la divisione elettrica dal nome "Race Up Electric" con il compito di progettare una monoposto elettrica, mentre la divisione che si occupa della vettura con motore tradizionale a combustione si chiama "Race Up Combustion" (Figura 1.4).



Figura 1.4: Vettura MG 13.18 Combustion impegnata nelle prove nel circuito Varano De' Melegari

Entrambe le divisioni contano circa 30 studenti di diverse facoltà e presenta una divisione interna in 7 reparti:

- Aerodinamica
- Frame and body
- Sospensioni
- Freni
- Elettronica
- Motore
- Business and marketing

Ciascun reparto ha il compito di progettare i diversi componenti della vettura seguendo le linee guida decise all'inizio della stagione.

La vettura della stagione 2017-2018 della divisione Combustion prende il nome di MG 13.18 ed ha preso parte agli eventi FSAE avvenuti in Italia e in Germania.

1.4 L'aerodinamica

L'aerodinamica è la branca della fluidodinamica che studia la dinamica dei gas, in particolare dell'aria, e la loro interazione con corpi solidi.

Lo studio dell'aerodinamica nelle competizioni automobilistiche ha inizio nei primi anni del '900. Inizialmente l'obiettivo principale era ridurre la resistenza all'avanzamento opposta dall'aria usando forme allungate che ritardavano la separazione del flusso dal profilo. La prima vettura progettata con un sistema di ali ideato per generare deportanza fu la "Chaparral 2E" del 1966 (Figura 1.5).



Figura 1.5: Chaparral 2E (1966)

Nel giro di pochi anni ogni vettura da competizione, persino in Formula 1, avrebbe montato alettoni su lunghi ammortizzatori, sebbene molti non fossero comparabili a quelli ideati dalla Chaparral; i loro cedimenti strutturali, infatti, causarono gravi incidenti che portarono le varie commissioni sportive organizzatrici a limitarne o addirittura vietarne l'uso.

Da allora ogni azienda si è focalizzata sullo studio dell'aerodinamica per incrementare le performance delle proprie vetture: non solo in termini di riduzione della resistenza all'avanzamento (drag) ma anche cercando di aumentare la deportanza (downforce).

Le forze aerodinamiche nascono dall'interazione del fluido con le superfici, generando sforzi normali e sforzi di taglio sulle superfici stesse. La risultante di queste forze può essere divisa in varie componenti, le direzioni più comunemente scelte sono definite classicamente da un sistema di riferimento fisso con il baricentro della vettura (Figura 1.6):

- Componente drag lungo l'asse x, la direzione di avanzamento della macchina,
- Componente lift lungo l'asse z, la direzione verticale,
- Componente laterale lungo l'asse y, a completare la terna.



Figura 1.6: Sistema di riferimento adottato

Queste tre componenti di forza sono definite nel seguente modo (Figura 1.7):

• Drag: la componente che si oppone all'avanzamento del veicolo (relazione 1.1).

$$D = \frac{1}{2} C_D A \rho V_{inf}^2 \tag{1.1}$$

Lift (o downforce): la componente perpendicolare alla direzione di movimento (relazione 1.2). Questa
forza deriva direttamente dalla Legge di Bernoulli, la quale stabilisce una correlazione tra velocità del
fluido e pressione lungo un ideale tubo di flusso. Considerando un semplice caso bidimensionale, i
profili aerodinamici sono studiati per indurre una velocità del fluido maggiore lungo la parte inferiore
del profilo, ciò corrisponde secondo Bernoulli a una pressione minore che induce l'effetto di
deportanza.

$$L = \frac{1}{2} C_L A \rho V_{inf}^2 \tag{1.2}$$

• Side force: la componente laterale (relazione 1.3).

$$Y = \frac{1}{2}C_Y A \rho V_{inf}^2 \tag{1.3}$$

Come si vede dalle relazioni sopra riportate, le forze aerodinamiche dipendono dalla densità dell'aria ρ [kg/m³], da una certa area di riferimento A [m²] (solitamente l'area frontale della vettura), dal quadrato della velocità indisturbata dell'aria che raggiunge il profilo V_{inf} [m/s] e da un coefficiente adimensionale C che dipende dalla geometria.

Il punto di applicazione della risultante delle forze aerodinamiche che nascono su una superficie è detto centro di pressione; ipotizzando di incernierare in quel punto il profilo, non sono necessari momenti per garantire l'equilibrio.



Figura 1.7: Forze aerodinamiche

Per la performance di una vettura le componenti che è importante controllare sono drag e lift, la componente laterale invece si può ritenere nulla nel caso di veicolo simmetrico ed assenza di vento laterale.

Per lo studio della dinamica del veicolo è di fondamentale importanza poter valutare tali forze. Una macchina tocca il suolo solo attraverso i quattro punti determinati dalla zona di contatto tra la strada e le gomme; attraverso queste aree tutte le forze, necessarie per il movimento della macchina, vengono trasmesse al suolo come forze d'attrito. Le forze di attrito sono proporzionali all'area e alla forza normale in superficie, quindi, aumentando il carico sulle ruote, l'auto è in grado di trasmettere più forza a terra. Attraverso le forze aerodinamiche di downforce è possibile aumentare il carico sulle ruote senza aumentare il peso della vettura: questo consente, ad esempio, di aumentare la velocità con cui è possibile affrontare una curva perché se il carico sulle ruote è superiore anche la forza di attrito delle gomme saranno più grandi e quindi l'aderenza della vettura sarà migliorata. D'altra parte, l'aumento di downforce è sempre accompagnato da un aumento di resistenza al movimento della vettura limitando le prestazioni soprattutto in termini di velocità massima. Ciò

implica la ricerca di un compromesso che dipende da diversi aspetti come il tipo di auto, la pista e le condizioni esterne.

Avere degli strumenti che permettano di valutare a priori tali forze e di fondamentale importanza per la progettazione e la valutazione della performance del veicolo.

1.5 La C.F.D. nel Race Up Team

La Computational Fluid Dynamics (C.F.D.) è l'analisi di sistemi che coinvolgono correnti fluide attraverso l'utilizzo di software di simulazione. Il principale vantaggio della C.F.D. è che permette di ottenere risultati in maniera veloce e relativamente poco costosa.

Il reparto aerodinamica del Team Race Up fa largo uso di simulazioni C.F.D. per stimare le forze di drag e downforce e conseguentemente progettare i profili alari della vettura. Tali risultati vengono poi forniti agli altri reparti, in particolare al reparto sospensioni e freni, per poter valutare la dinamica del veicolo e progettare i vari componenti della macchina nella maniera più opportuna.

Appare evidente come la C.F.D. sia uno strumento indispensabile per una corretta progettazione e proprio per questo è allo stesso modo importante avere strumenti di verifica della bontà dei risultati numerici di tale strumento. L'analisi numerica deve sempre andare di pari passo con le rilevazioni sperimentali.

I primi dati propri del team a riguardo sono associati al lavoro di tesi di Marco De Lazzari [2], il quale realizzò delle celle di carico ad anello che misuravano i carichi dei 4 puntoni e dei 2 tiranti con cui era montata l'ala posteriore della MG 10.15 (Figura 1.8).



Figura 1.8: Vettura MG 10.15 con celle di carico applicate all'ala posteriore [2]

Tali risultati sperimentali furono poi utilizzati da Riccardo Milan [3] come validazione del suo modello C.F.D. I risultati numerici di Milan risultano in accordo con quelli sperimentali di De Lazzari entro un errore massimo del 6%. Convalidato il modello sulla MG 10.15, Milan ha poi eseguito simulazioni aerodinamiche sulla vettura MG 13.18 considerando 3 diverse configurazioni dell'ala anteriore (Front wing F.W.) e 3 diverse configurazioni dell'ala posteriore (Rear wing R.W.). Sia l'ala anteriore sia quella posteriore infatti presentano dei flap regolabili che permettono di modificare il comportamento aerodinamico della vettura

In tabella 1.1 sono riportati i risultati ottenuti dalle simulazioni C.F.D. eseguite da Milan: le 3 configurazioni dell'ala anteriore combinate con le 3 configurazioni dell'ala posteriore danno 9 differenti configurazioni complessive per l'assetto della vettura. I valori numerici si riferiscono a una velocità di 50 km/h. Nella tabella si parla di "Alto", "Medio" e "Basso carico", per una spiegazione approfondita delle varie configurazioni delle ali si rimanda al paragrafo 7.1.

R.W. CARICO	F.W. CARICO	R.W. D.F. [N]	R.W. DRAG [N]	F.W. D.F. [N]	F.W. DRAG [N]	D.F. [N]	DRAG [N]	EFFICIENCY (D.F./DRAG)
Alto	Alto	87	45.08	105.14	19.72	187.56	127.92	1.466
Basso	Alto	71.48	29.9	106.62	20.2	167.04	109.22	1.529
Medio	Alto	82.72	38.7	105.68	19.94	177.4	118.82	1.493
Alto	Medio	89.54	46.8	101.28	17.46	183.24	125.74	1.457
Alto	Basso	89.48	45.1	90.36	12.74	170.6	118.48	1.440
Medio	Medio	86.6	37.88	97.96	16.7	173.4	110.82	1.565

Medio	Basso	84.5	38.76	89.6	12.56	158.74	109.58	1.449
Basso	Medio	71.16	29.38	99.72	16.46	161.86	101.32	1.597
Basso	Basso	71.78	29.42	86.88	12.26	143.7	101.22	1.420

Tabella 1.1: Risultati C.F.D. a una velocità di 50 km/h [3]

Obiettivo della presente tesi è acquisire per i via sperimentale gli effettivi carichi aerodinamici rilevati in prove in pista e verificare l'accordo di tali dati con le previsioni C.F.D. A tal fine si progettano delle celle di carico multicomponente che andranno a sostituire le interfacce di supporto dell'ala anteriore e dell'ala superiore.

Capitolo 2: Progettazione e realizzazione delle celle di carico

2.1 Individuazione componenti da riprogettare

Al fine di progettare le celle di carico che dovranno misurare le forze di downforce e drag dell'ala anteriore e posteriore, è necessario innanzitutto individuare i componenti da riprogettare al fine di renderli sensibili a tali forze nel modo più disaccoppiato possibile.

L'idea è stata riprogettare i supporti delle ali in quanto è proprio attraverso tali supporti che le forze che si generano alle ali vengono trasmesse alla vettura e infine a terra.

L'ala anteriore risulta montata in vettura tramite due triangoli (Figura 2.1) in fibra di carbonio con all'interno Rohacell.



Figura 2.1: Triangoli di supporto dell'ala anteriore

L'ala posteriore invece è collegata a telaio mediante due strutture reticolari (Figura 2.2) in Ergal.



Figura 2.2: Reticolari di supporto dell'ala posteriore

L'obiettivo è riprogettare tali componenti senza modificare la geometria della vettura, cioè mantenendo come vincoli le posizioni spaziali dei collegamenti bullonati che collegheranno queste interfacce al telaio e alle rispettive ali.

2.2 Progettazione delle celle di carico

Il principio della progettazione è stato pensare a una geometria che consentisse di disaccoppiare il più efficacemente possibile il contributo della downforce da quello del drag. L'idea più semplice ed efficace è stata concepire le celle di carico come l'insieme di due travi incastrate a sbalzo, l'una orizzontare sensibile alla downforce e l'altra verticale sensibile al drag. Tale concept doveva comunque rispettare gli ingombri e la geometria originaria della vettura, cioè consentire di montare le ali nelle stesse posizioni che occupano in configurazione da gara.

Concepita la geometria, il dimensionamento ha seguito le successive linee guida:

- Resistenza: coefficiente di sicurezza rispetto allo snervamento almeno di 3,
- Rigidezza: valori confrontabili con quelle dei supporti originari, per evitare spostamenti delle ali eccessivi sotto carico,

x=2362.08mm, y=-464.72mm

• Sensibilità alle forze: sensibilità a fondo scala di circa 0.5 mV/V.

Per quanto riguarda l'ala anteriore i dati di progetto che si sono considerati per il dimensionamento derivano da analisi C.F.D. del team e sono:

- Downforce su 1 supporto a 100 km/h in configurazione di alto carico: 220 N
 Drag su 1 supporto a 100 km/h in configurazione di alto carico: 40 N
- Posizione centro di pressione (Figura 2.3):

La posizione del centro di pressione è stata scelta arbitrariamente in quanto in un sistema piano, come il caso in esame, esiste una retta luogo dei punti dei centri di pressione (vedi Appendice A). Va inoltre precisato che il centro di pressione cambia sensibilmente in funzione della velocità e della configurazione delle ali, quello preso in considerazione è il centro di pressione relativo alla configurazione di alto carico a 100 km/h, ossia il caso più gravoso.



Figura 2.3: Posizione del centro di pressione dell'ala anteriore in configurazione di alto carico a 100 km/h

Partendo da questi dati si è eseguito il dimensionamento, inizialmente eseguito con conti carta-penna e infine con simulazioni F.E.M. tramite "Ansys Mechanical APDL 19.1". Come materiale si è scelto una lega di alluminio 7075-T6 (nome commerciale Ergal) di cui il team già possedeva una lastra dallo spessore nominale di 5 mm in magazzino. I dati considerati di tale materiale sono modulo elastico E = 71000 MPa, sigma di snervamento = 460 MPa, sigma di rottura = 540 MPa, durezza Brinell = 150, coefficiente di Poisson = 0.32 [4]. Per verificare i dati trovati in letteratura è stata eseguita una prova di durezza Brinell su uno spezzone di lamiera che ha confermato la durezza trovata in precedenza.

La geometria definitiva per quanto riguarda i supporti anteriori è mostrata in figura 2.4, in figura 2.5 è mostrata la messa in tavola con indicate le dimensioni principali.



Figura 2.4: CAD interfaccia anteriore



Figura 2.5: Messa in tavola interfaccia anteriore con quote principali

Si nota come sia per la trave superiore (quella sensibile alla downforce) che per quella verticale (quella sensibile al drag) è stata assunta una soluzione a "doppia trave": tale soluzione si è resa necessaria per mantenere segnali elevati e allo stesso tempo aumentare la rigidezza della struttura, infatti così facendo si è aumentato il momento statico mantenendo invariata la sezione netta. I 2 fori per il montaggio a telaio e i 2 per il montaggio dell'ala hanno diametro di 5 mm (come i supporti triangolari preesistenti), inoltre si nota la presenza di altri 3 fori dal diametro di 4 mm: questi ultimi fori sono stati pensati per permettere l'attacco di tiranti da un'interfaccia all'altra ed aumentare così la rigidezza laterale.

In figura 2.6 si mostra la struttura analizzata con un'analisi FEM. Sono stati usati elementi "plane 182" per l'interfaccia e "beam 188" per trasmettere le forze dal centro di pressione alla struttura.



Figura 2.6: FEM interfaccia anteriore

L'analisi ha dato come risultati una tensione massima equivalente secondo Von Mises di 59 MPa, che garantisce un coefficiente di sicurezza rispetto allo snervamento di 7.8. La sensibilità teorica a fondo scala risulta di 0.69 mV/V per il canale downforce e di 0.47 mV/V per il canale drag (per la spiegazione del posizionamento degli estensimetri si guardi paragrafo 2.4.1). Le celle di carico commerciali solitamente hanno sensibilità a fondo scala di 2 mV/V, mentre per quanto riguarda quelle customizzate da esperienza sono sufficienti anche valori più bassi intorno a 0.5 mV/V.

La rigidezza verticale dell'intera struttura, cioè considerando entrambe le interfacce di supporto, vale 267.4 N/mm e quella longitudinale 279.8 N/mm. Per quanto riguarda la rigidezza laterale essa risulta di soli 10.0 N/mm, eccessivamente bassa in confronto ai carichi laterali che si sono stimati considerando la massa dell'ala anteriore (4.3 kg) e le accelerazioni laterali massime fornite dal team di circa 2g (circa 19.6 m/s²). Quindi i carichi laterali massimi anteriori sono di circa 40 N per supporto.

Per aumentare la rigidezza laterale si è pensato di inserire dei tiranti montati a "X": questa soluzione permette di avere una rigidezza laterale di 79.2 N/mm, mantenendo inalterata la sensibilità alla downforce e al drag. Per valutare questa soluzione è stato implementato un modello tridimensionale in Ansys utilizzando elementi "solid 185", i tiranti invece sono stati modellati con elementi "link 180". Nella figura 2.7 si notano le due coppie di tiranti a "X" mentre la struttura inferiore rappresenta l'ala attraverso cui le forze vanno dal centro di pressione alle due interfacce.



Figura 2.7: FEM struttura di supporto ala anteriore

Di seguito si riportano sinteticamente i risultati teorici ottenuti già esposti in precedenza:

- Sigma equivalente massima di Von Mises:
- Coefficiente di sicurezza allo snervamento:
- Sensibilità a fondo scala downforce:
- Sensibilità a fondo scala drag:
- Rigidezza verticale:
- Rigidezza longitudinale:
- Rigidezza laterale:

59 MPa 7.8 0.69 mV/V 0.47 mV/V 267.4 N/mm 279.8 N/mm 79.2 N/mm Trattando dell'ala posteriore i dati di progetto che si sono considerati per il dimensionamento sono:

- Downforce su 1 supporto a 100 km/h in configurazione di alto carico: 180 N
 Drag su 1 supporto a 100 km/h in configurazione di alto carico: 90 N
- Posizione centro di pressione (Figura 2.8):

90 N x=326.69mm y=324.66mm



Figura 2.8: Posizione del centro di pressione dell'ala posteriore in configurazione di alto carico a 100 km/h

Per quanto riguarda il posteriore valgono le stesse considerazione fatte per l'anteriore e il materiale scelto per i supporti posteriori è sempre Ergal.

In figura 2.9 è mostrato il CAD dell'interfaccia posteriore mentre in figura 2.10 è possibile vedere la messa in tavola con le dimensioni principali.



Figura 2.9: CAD interfaccia posteriore



Figura 2.10: Messa in tavola interfaccia posteriore con quote principali

In figura 2.11 si può vedere l'interfaccia posteriore meshata per le analisi FEM, in figura 2.12 invece si vede l'intera struttura di supporto dell'ala posteriore dove è stata adottata, allo stesso modo dell'anteriore, una soluzione con tiranti a "X" per irrigidire lateralmente.



Figura 2.11: FEM interfaccia posteriore



Figura 2.12: FEM struttura di supporto ala posteriore

Per incrementare ulteriormente la rigidezza laterale si è poi pensato di aggiungere due lamine di metallo esternamente ai bracci sensibili al drag al fine di impedire lo "sbandamento" laterale di tali parti (Figura 2.13).



Figura 2.13: CAD interfaccia posteriore irrigidita lateralmente con lamine metalliche

Per concludere si riportano i risultati teorici ottenuti dei supporti posteriori:

- Sigma equivalente massima di Von Mises: •
- 44 MPa • Coefficiente di sicurezza allo snervamento: 10.5 Sensibilità a fondo scala downforce: 0.70 mV/V• 0.55 mV/VSensibilità a fondo scala drag: • Rigidezza verticale: 233.6 N/mm • Rigidezza longitudinale: 345.1 N/mm •
- Rigidezza laterale: •

Infine in figura 2.14 si mostra il concept della vettura con montate le celle di carico che sorreggono le ali.

48.2 N/mm



Figura 2.15: CAD del telaio con montate le ali su le celle di carico

2.3 Produzione delle celle di carico

Le celle di carico sono state prodotte partendo da una lastra di Ergal dalle dimensioni di 1000x1500 mm che il team aveva in magazzino. Al fine di minimizzare lo spreco di materiale si sono disposti i pezzi come in figura 2.16, è stato così sufficiente utilizzare uno spezzone di lastra dalle dimensioni di 460x650 mm.



Figura 2.16: Disposizione dei pezzi sulla lastra di Ergal da tagliare

La lavorazione eseguita è stata un taglio ad acqua presso l'azienda "Breda Racing S.R.L." con sede a Villafranca Padovana.

In figura 2.17 e 2.18 si vedono realizzati i pezzi che andranno a costituire rispettivamente la cella di carico anteriore e quella posteriore.



Figura 2.17: Supporto ala anteriore



Figura 2.18: Supporto ala posteriore

2.4 Preparazione del sistema di misura

2.4.1 Scelta della disposizione degli estensimetri

Ogni cella di carico deve presentare due canali, uno sensibile alla downforce e uno sensibile al drag. Visto che il centro di pressione non è un punto fisso, ma varia sensibilmente la sua posizione in funzione della velocità e della configurazione aerodinamica dell'ala, era necessario scegliere una disposizione degli estensimetri che permettesse di misurare la componente di carico di interesse indipendentemente dal suo punto di applicazione. A tal fine è stata scelta una disposizione del tipo mostrato in figura 2.19, in cui lo sbilanciamento del ponte è proporzionale al carico applicato e non alla sua posizione, infatti:

$$V_0 = V_S \frac{\kappa}{4} (\varepsilon_1 - \varepsilon_2 + \varepsilon_3 - \varepsilon_4) = V_S \frac{\kappa}{4} (\varepsilon_A + \varepsilon_A - \varepsilon_B - \varepsilon_B) = \frac{V_S \kappa L2}{2 E W_f} P \qquad (2.1)$$



Figura 2.19: Disposizione degli estensimetri adottata per avere misure di carico indipendenti dal punto di applicazione [5]

In figura 2.20 è mostrato il posizionamento degli estensimetri sulle celle di carico anteriori. Si nota come ogni cella di carico presenti 8 estensimetri: 4 per il ponte intero di Wheatstone sensibile alla downforce (in rosso nell'immagine) e 4 per il ponte sensibile al drag (in giallo).



Figura 2.20: Posizione degli estensimetri sulla cella di carico anteriore (in rosso gli estensimetri del ponte di Wheatstone sensibile alla downforce, in giallo quelli del drag)

In figura 2.21 invece è possibile vedere come sono stati posizionati gli estensimetri sulle celle di carico posteriori. Allo stesso modo dell'anteriore, gli estensimetri rappresentati in rosso formano il canale downforce mentre quelli in giallo il canale drag.



Figura 2.21: Posizione degli estensimetri sulla cella di carico posteiore (in rosso gli estensimetri del ponte di Wheatstone sensibile alla downforce, in giallo quelli del drag)

Complessivamente quindi si acquisiranno 8 canali, cioè un segnale di downforce e uno di drag per ciascuna delle 4 celle di carico realizzate.

A seguire viene spiegata la procedura operativa utilizzata per l'incollaggio degli estensimetri, per maggiori informazioni a riguardo è possibile consultare il manuale Kyowa [6].

2.4.2 Preparazione delle superfici e tracciatura

Al fine di un corretto incollaggio degli estensimetri è necessario innanzitutto preparare le superfici sulle quali vanno applicati. Infatti la superficie deve risultare il più liscia possibile, senza graffi o solchi che potrebbero comportare errate misurazioni delle deformazioni da parte degli estensimetri. A tal fine le superfici dei siti dove andranno applicati gli estensimetri sono state preparate con carta vetrata dalla granulosità crescente: si è partiti da una grana di 100 per poi passare a 240, 320, 500 e infine a 1000. I movimenti eseguiti con la carta vetrata sono stati circolari per evitare direzioni preferenziali.

Una volta preparata la superficie e pulita con alcool, si procede con la tracciatura. Tale operazione serve ad avere dei riferimenti ai quali sovrapporre la griglia estensimetrica per un corretto e preciso posizionamento. Per le tracce longitudinali si è utilizzato un apposito tracciatore (Figura 2.22) dotato di calibro integrato per individuare correttamente la metà dello spessore.



Figura 2.22: Tracciatura longitudinale
Per le tracce trasversali invece si è fatto uso di una squadra e un incisore (Figura 2.23). Queste tacche sono state fatte in corrispondenza dell'inizio della sezione netta minima, ossia dove finivano i raggi di raccordo degli intagli centrali (per maggior chiarezza si rimanda alle immagini 2.20 e 2.21).



Figura 2.23: Tracciatura trasversale

2.4.3 Posizionamento ed incollaggio degli estensimetri

I 32 estensimetri utilizzati sono estensimetri Kyowa modello "KFGS-3-120-C1-11 L3M3R" precablati con cavo di 3 metri a tre fili e presentano i seguenti parametri (Figura 2.24):

- Gauge factor: $K = 2.08 \pm 1\%$
- Resistenza: 119.6 Ω ± 0.4 %

		MADE IN JAPAN
Model KFGS-		Lot No. Y4706M Quantity Batch No. 014A 数量 10
Gage Factor (23°C、50	0%) 2.08 ±1.0%	Temperature Compensation for STEEL 主な適合材料
ケーシ率 Gage Length	3 mm	Adoptable Thermal Expansion 適合線膨張係数 11.7×10 ⁻⁶ /℃
ゲージ長 Gage Resistance (23°	(50%) 110.60 $\pm 0.4\%$	Applicable Adhesive 主な適用接着剤 CC-33A,EP-340
ゲージ抵抗 リート	Ratio (23℃、50%) (0.5 ±0.3)%	Temperature Coefficient of Gage Factor Refer to Graph ゲージ率の温度係数 グラフ参照
析感度比	(0.5 <u>10.0</u>) Refer to Graph ガラフ参照	共和品-1437B
Thermal Output 熱出力	COMA STAT	-GAGES CE

Figura 2.24: Data sheet degli estensimetri utilizzati

Dall'immagine osserviamo inoltre che sono stati utilizzati estensimetri compensati per acciaio, mentre le celle di carico sono in lega di alluminio. Ciò non appare un problema in quanto i test saranno eseguiti all'aperto e quindi a temperatura ambiente, non ci sono motivi per considerare variazioni di temperatura tali da rendere necessaria una compensazione.

Per il posizionamento si procede nel seguente modo: con l'aiuto di uno specchietto appoggiato sul tavolo si attacca l'estensimetro su un pezzo di scotch semi-trasparente (Figura 2.25), tale striscia di nastro permette di posizionare l'estensimetro correttamente allineando le tacche di centratura con gli indicatori presenti attorno alla griglia dell'estensimetro (Figura 2.26 e 2.27).



Figura 2.25: Estensimetro attaccato allo scotch



Figura 2.26: Posizionamento dell'estensimetro



Figura 2.27: Griglia estensimetrica con indicatori per il centraggio

Una volta posizionato l'estensimetro, facendo attenzione a far andare i cavi nella direzione in cui si troverà l'abitacolo della vettura, si procede all'incollaggio vero e proprio. Si alza lo scotch su cui è ancora attaccato l'estensimetro (Figura 2.28), si applica una goccia di colla (cianoacrilato CC-33A, figura 2.29) e rapidamente si abbassa lo scotch premendo per almeno 60 secondi con un foglio di teflon (Figura 2.30). Infine si rimuove lo scotch verificando che l'estensimetro resti incollato alla superficie del pezzo.



Figura 2.28: Applicazione della colla



Figura 2.29: Cianoacrilato CC-33A



Figura 2.30: Fase di reticolazione della colla

Terminata la fase di incollaggio, si procede all'applicazione di uno strato di silicone che ha il compito di proteggere l'estensimetro dagli agenti esterni e dalla sporcizia (Figura 2.31).



Figura 2.31: Strato protettivo di silicone sopra all'estensimetro incollato

2.4.4 Cablaggio

Per eseguire il collegamento degli estensimetri a ponte intero si utilizzano degli spinotti (Figura 2.32) su cui si vanno a collegare i terminali dei fili. Ciascuno spinotto consente di configurare due canali, quindi infine gli spinotti cablati risulteranno 4.



Figura 2.32: Spinotto della centralina IMC

Per riprodurre la disposizione del ponte intero di Wheatstone (Figura 2.33) è necessario eseguire il cablaggio mostrato in figura 2.34, come spiegato nel manuale allegato alla centralina IMC utilizzata. Nello schema la dicitura "VB" indica l'alimentazione mentre "in" indica l'output, cioè lo sbilanciamento. Con "sense" si fa riferimento al collegamento previsto nelle centraline IMC per compensare la lunghezza dei cavi, la quale comporta un'attenuazione del segnale di misura direttamente proporzionale alla lunghezza del cavo.



Figura 2.33: Configurazione ponte intero di Wheatstone



Figura 2.34: Schema elettrico di collegamento degli estensimetri nello spinotto IMC

Ogni estensimetro presenta 1 cavo rosso e 2 bianchi e il cablaggio è stato eseguito come mostrato in figura 2.35 (i cavi bianchi sono in nero nell'immagine).



Figura 2.35: Schema del cablaggio eseguito

In figura 2.36 viene mostrato un esempio di cablaggio di un canale eseguito su uno spinotto.



Figura 2.36: Spinotto con un canale cablato

Infine, in figura 2.37 vengono mostrate le interfacce estensimetrate e cablate pronte per l'utilizzo in vettura.



Figura 2.37: Celle di carico

2.4.5 Strumenti di acquisizione dei dati

Per l'acquisizione dei carichi sono state utilizzate due centraline, la centralina "IMC Cronos-PL2" e l'espansione "Cansas-L-UNI8", come sarà spiegato più in dettaglio nel paragrafo 4.1.

Queste centraline vanno inserite in vettura in modo da non modificare l'aerodinamica e allo stesso tempo non creare ingombro alla libera guida del pilota. A tal fine è stata realizzata una tavola di compensato opportunamente forata (Figura 2.38) che fa da supporto per l'alloggiamento delle centraline stesse. Si può notare inoltre come la tavola sia stata dotata di due piccole fasce di materiale spugnoso che serviranno ad aumentare l'isolamento delle centraline alle vibrazioni della vettura.



Figura 2.38: Tavola di compensato per il supporto delle centraline

Le centraline sono fissate a tale supporto con fascette da elettricista e sono posizionate in vettura sospese nello spazio libero sopra le gambe del pilota come si vede in figura 2.39. A sua volta, la tavola di compensato è legata al telaio attraverso fascette da elettricista (Figura 2.40).



Figura 2.39: Posizionamento delle centraline in vettura, sopra le gambe del pilota



Figura 2.40: Fissaggio della tavola porta-centraline a telaio

L'alimentazione delle centraline è garantita da due batterie da 12V (Figura 2.41), le quali sono state posizionate anteriormente sopra il parallelepipedo di Honeycomb (Figura 2.42).



Figura 2.41: Batteria da 12 V per l'alimentazione della centralina



Figura 2.42: Posizionamento delle batterie in vettura

In figura si vedono le batterie fissate unicamente con scotch, in fase di test per sicurezza sono state utilizzate anche delle fascette metalliche al fine di garantire un fissaggio più sicuro.

Completano il kit di acquisizione i cavi di alimentazione e il cavo viola di collegamento tra centralina Cronos ed espansione. Questi cavi sono stati posizionati in modo tale da non interferire con i pedali e i movimenti dei piedi del pilota così da garantire una guida sicura (Figura 2.43).



Figura 2.43: Posizionamento dei cavi delle centraline

2.5 Montaggio

Terminate le fasi di realizzazione e cablaggio delle celle di carico descritte in precedenza, si può procedere al montaggio delle stesse in vettura. Come descritto in precedenza, al fine di garantire anche una sufficiente rigidezza laterale alle strutture di supporto delle ali, sono stati tagliati e deformati plasticamente dei tubicini di alluminio (Figura 2.43) che vanno a costituire i tiranti a "X".



Figura 2.43: Taglio del tubicino di alluminio (a sinistra) e tubicino piegato (a destra)

In figura 2.44 si possono osservare le celle di carico anteriori montate sull'ala anteriore con installata la struttura tubolare di irrigidimento. In figura 2.45 invece si osserva l'intera struttura di supporto anteriore montata a telaio.



Figura 2.44: Struttura di supporto anteriore con montati i tubicini a "X"



Figura 2.45: Struttura di supporto anteriore montata

Per quanto riguarda il posteriore sono state effettuate scelte analoghe come si può osservare in figura 2.46. Infine, in figura 2.47 viene mostrata l'intera struttura di supporto dell'ala posteriore.



Figura 2.46: Struttura di supporto dell'ala posteriore con montati i tubicini a "X"



Figura 4.47: Struttura di supporto dell'ala posteriore montata

Come si nota dalle immagini precedenti, ci sono numerosi cavi che partono dagli estensimetri delle celle di carico e vanno in centralina attraverso gli spinotti. È quindi importante e necessario studiare accuratamente anche il percorso dei cavi al fine in primis di non danneggiarli e allo stesso tempo non recare disturbo alla guida del pilota o all'aerodinamica. A tal fine i cavi sono stati raggruppati e fatti correre lungo il telaio al quale sono stati fissati con fascette da elettricista. Si è cercato inoltre di allontanare il più possibile tutti i cavi dal motore e da altri punti dove si sarebbero potuti danneggiare.

Oltre ai cavi degli estensimetri, sono presenti anche altri cavi relativi ad altri segnali acquisiti (Si veda paragrafo 4.1) per i quali sono stati presi gli stessi provvedimenti descritti in precedenza.

I cavi sono stati fissati in più punti così da evitare che durante i test ci siano tratti in movimento, ciò potrebbe modificare l'aerodinamica ma ancora di più potrebbe essere di intralcio per il pilota.

Nelle seguenti figure è possibile osservare le scelte effettuate per quanto riguarda il percorso e il fissaggio dei cavi in abitacolo.

In particolare, in figura 4.48 e 4.49 si possono vedere i connettori attaccati alle centraline e i relativi cavi che sono stati fissati in modo da non interferire con i pedali e con i piedi del pilota.



Figura 4.48: Connettori cablati collegati alle centraline e percorso dei relativi cavi



Figura 4.49: Connettori cablati collegati alle centraline

In figura 4.50 e 4.51 si mostra il percorso dei cavi delle celle di carico posteriori.



Figura 4.50: Percorso dei cavi provenienti dalle celle di carico posteriori



Figura 4.51: Percorso dei cavi provenienti dalle celle di carico posteriori lungo il telaio

Per quanto riguarda le celle di carico anteriori invece le soluzioni assunte per il percorso cavi sono mostrate in figura 4.52.



Figura 4.52: Percorso dei cavi provenienti dalle celle di carico anteriori

Il trigger, ossia il "grilletto" che permette al pilota di iniziare o stoppare un'acquisizione, è stato posizionato esternamente all'abitacolo, come mostrato in figura 4.53, in una posizione facilmente raggiungibile dal pilota con le mani. Esso è stato fissato con nastro adesivo.



Figura 4.53: Posizionamento del trigger in vettura

Infine, va montato il muso anteriore che va a coprire completamente le centraline, le batterie e tutto il cablaggio. Come si nota in figura 4.54, la geometria delle celle di carico anteriori obbliga a montare il muso circa 10 cm più in avanti rispetto alla sua posizione "normale". Questa scelta di compromessa si è resa necessaria in quanto tagliare opportunamente il muso avrebbe provocato modifiche aerodinamiche più pesanti, invece questa soluzione non comporta sostanziali modifiche all'aerodinamica della monoposto.



Figura 4.54: Muso della vettura montato sopra il sistema di misura

CAPITOLO 3: Calibrazione delle celle di carico

3.1 Allestimento banco per il fissaggio

Per eseguire la taratura statica delle celle di carico, ossia applicare una serie di pesi dalla massa crescente, è necessario innanzitutto vincolarle nella maniera più simile possibile alle reali condizioni di utilizzo. In vettura le interfacce anteriori e posteriori saranno collegate a telaio attraverso collegamenti bullonati, che nelle analisi F.E.M. sono stati simulati con incastri al centro dei due fori interessati.

Per riprodurre tale vincolo in fase di calibrazione si è utilizzato un blocco di alluminio opportunamente forato, fissato a una morsa, che a sua volta era vincolata a un banco (Figura 3.1 e 3.2).



Figura 3.1: Morsa vincolata al banco



Figura 3.2: Blocco di alluminio fissato in morsa

Su tale supporto è possibile cambiare l'orientazione del pezzo semplicemente ruotando il blocco di alluminio, così da poter sollecitare a downforce (Figura 3.3) o a drag (Figura 3.4).



Figura 3.3: Componente sollecitato a downforce



Figura 3.4: Componente sollecitato a drag

3.2 Applicazione dei carichi

Il processo di taratura è necessario per correlare le grandezze in ingresso (forze agenti in N) con le grandezze in uscita dal sistema (deformazioni in microepsilon).

Durante la calibrazione è fondamentale ridurre al minimo le perturbazioni del sistema in modo da ottenere una relazione ingresso/uscita precisa e aderente alla realtà.

Il processo di taratura consiste nel sollecitare una cella di carico per volta, i pesi verranno applicati in maniera crescente fino al peso massimo e in seguito si effettuerà la fase di scarico. Quindi in uscita si avrà un segnale a rampa, prima crescente e poi decrescente. Per verificare la ripetibilità del processo di misura, ogni rampa è stata ripetuta due volte.

Iniziamo trattando la cella di carico anteriore destra (Anterior right A.R.) rispetto alla postazione di guida. Per quanto riguarda la calibrazione a downforce la cella è stata fissata come mostrato in figura 3.5 e sono stati applicati i pesi attraverso il bilanciere.



Figura 3.5: cella di carico A.R. sollecitata a downforce

Visto che da progetto il carico massimo di downforce dei supporti anteriori è di 220 N, si è deciso di applicare la seguente serie di pesi:

0,528 - 1,060 - 1,592 - 2,589 - 4,589 - 6,589 - 9,942 - 14,942 - 18,305 - 20,307 - 21,304 - 22,302 - 21,304 - 20,307 - 18,305 - 14,942 - 9,942 - 6,589 - 4,589 - 2,589 - 1,592 - 1,060 - 0,528 kg.

I segnali acquisiti durante l'applicazione di tali carichi sono riportati in figura 3.6, si nota come una pura sollecitazione a downforce comporti anche la presenza di un segnale di drag diverso da zero. Ciò è dovuto al fatto che i due canali non sono perfettamente disaccoppiati.



Figura 3.6: Rampa di taratura downforce (o lift) cella di carico A.R.

Per ogni "scalino" si è considerato un intervallo in cui il segnale era abbastanza costante (Figura 3.7) e si è calcolato il valor medio di downforce e di drag corrispondente a un certo carico applicato. Si è poi proceduto al calcolo della sensibilità come sarà spiegato nel prossimo paragrafo.



Figura 3.7: Esempio di intervallo considerato, il valore considerato è la media dei segnali misurati in quell'intervallo

Per quanto riguarda il drag, la cella è stata montata come mostrato in figura 3.8 e la serie di pesi applicata è la seguente, considerando che il carico massimo di progetto è di 40 N: 0,200 - 0,728 - 1,26 - 2,257 - 3,257 - 4,252 - 4,784 - 4,984 - 4,784 - 4,252 - 3,254 - 1,260 - 0,728 - 0,200 kg.



Figura 3.8: cella di carico A.R. sollecitata a drag

I segnali acquisiti sono mostrati nella seguente figura 3.9.



Per quanto riguarda il drag si nota che il segnale è quasi perfettamente disaccoppiato dal segnale di downforce, il quale rimane quasi identicamente nullo per tutta la prova.

La procedura di taratura della cella di carico anteriore sinistra (Anterior left A.L.) è del tutto analoga a quella effettuata per la cella A.R. Si riporta in seguito la rampa di taratura a downforce (Figura 3.10) e quella a drag (Figura 3.11).



Figura 3.10: Rampa di taratura downforce (o lift) cella di carico A.L.



Figura 3.11: Rampa di taratura drag cella di carico A.L.

La cella di carico posteriore destra (Posterior right P.R., figura 3.12) invece presenta un carico massimo di progetto a downforce di 180 N per cui si è deciso di applicare questa serie di pesi: 0,997 - 2,997 - 4,997 - 8,350 - 13,350 - 16,713 - 18,713 - 19,710 - 18,713 - 16,713 - 13,350 - 8,350 - 4,997 - 2,997 - 0,997 kg.



Figura 3.12: cella di carico P.R. sollecitata a downforce

In figura 3.13 si riportano i segnali acquisiti durante la procedura di taratura a downforce.



Infine, a drag i carichi massimi di progetto sono previsti in 90 N e si è deciso di applicare come serie di pesi: 0,528 - 1,525 - 2,522 - 4,522 - 6,522 - 8,524 - 9,522 - 10,054 - 9,522 - 8,524 - 6,522 - 4,522 - 2,522 - 1,525 -0,528 kg.

In figura 3.14 è riportata la rampa di taratura eseguita a drag.



L'applicazione dei carichi della cella di carico posteriore sinistra (Posterior left P.L.) è identica a quella eseguita per la P.R. In figura 3.15 viene riportata la rampa di taratura a downforce e in figura 3.16 la rampa a drag.



Figura 3.15: Rampa di taratura downforce (o lift) cella di carico P.L.



Figura 3.16: Rampa di taratura drag cella di carico P.L.

3.3 Matrici di taratura

Le celle di carico considerate si definiscono multicomponente in quanto un'unica struttura presenta due canali, uno studiato per misurare la downforce e uno per il drag. Ottenere segnali completamente disaccoppiati è impossibile e capita sempre che una pura sollecitazione a downforce comporti anche un segnale di drag e viceversa. È quindi necessario lavorare con matrici di sensibilità, che nel caso in esame saranno 2x2, definite nel seguente modo:

$$\begin{cases} segnale_lift \\ segnale_drag \end{cases} = \begin{bmatrix} S_{11} & S_{12} \\ S_{21} & S_{22} \end{bmatrix} \begin{pmatrix} L \\ D \end{pmatrix}$$
(3.1)

Il vettore segnale, in $\mu\epsilon$, è uguale al vettore dei carichi esterni, in N, pre-moltiplicato per la matrice di sensibilità [S]. Questa matrice si trova elaborando i dati acquisiti effettuando le rampe viste in precedenza: consideriamo innanzitutto il lift, riportando in un piano cartesiano in ascissa il valore del carico applicato e in ordinata i corrispondenti segnali misurati (media del segnale nell'intervallo considerato) si ottiene il grafico di due rette, una relativa alla downforce e una al drag. Se i canali sono ben disaccoppiati, una delle due rette, quella della componente non sollecitata, avrà pendenza quasi nulla.

Interpolando i punti sperimentali con delle rette di intercetta nulla si trovano i valori dei coefficienti della matrice di sensibilità S_{11} e S_{21} leggendo l'inclinazione di tali rette.

Eseguendo lo stesso procedimento con le rampe relative al drag, si ottengono altre due rette i cui coefficienti angolari sono i coefficienti S_{12} e S_{22} .

Nella fase di test però non saranno noti i carichi applicati ma si misureranno solamente i segnali. Sarà quindi necessario utilizzare la matrice di calibrazione o taratura [C], cioè quella matrice che moltiplicata per il segnale ci dà i carichi esterni applicati:

Appare evidente come la matrice di calibrazione [C] non sia altro che l'inversa della matrice di sensibilità [S], quindi una volta calcolata [S] è immediato ricavare [C].

Per verificare l'accuratezza con cui si è realizzata la cella di carico è utile confrontare la matrice di sensibilità ottenuta sperimentalmente con quella teorica ottenuta mediante analisi F.E.M.

Applicando un carico unitario di downforce e misurando le deformazioni nei punti in cui sono applicati gli estensimetri, è possibile risalire ai valori teorici dei coefficienti S_{11} e S_{21} , che risultano numericamente uguali proprio alla deformazione totale essendo il carico applicato unitario. Allo stesso modo applicando un carico unitario di drag si può risalire ai valori teorici di S_{12} e S_{22} .

Per quanto riguarda le celle di carico anteriori, la matrice di sensibilità teorica e quella di taratura teorica risultano:

$$[S_{A.t}] = \begin{bmatrix} 6.309 & 0.001 \\ -0.020 & 23.280 \end{bmatrix}$$
$$[C_{A.t}] = \begin{bmatrix} 158.50 & 0.00681 \\ 0.136 & 42.96 \end{bmatrix} \cdot 10^{-3}$$

Si vanno ora ad analizzare i dati sperimentali al fine di ottenere le reali matrici di calibrazione, partendo dalla cella di carico anteriore destra. In appendice B si trovano riportati in dettaglio i valori numerici ottenuti ed elaborati.



In figura 3.17 e 3.18 si riportano rispettivamente le rette di taratura ottenute sottoponendo la cella a carichi di downforce e di drag.

Figura 3.17: Rette di taratura della cella di carico A.R. sollecitata a downforce



Figura 3.18: Rette di taratura della cella di carico A.R. sollecitata a drag

Si nota come non vi siano effetti di isteresi in quanto i punti sperimentali nella fase di scarico sono sovrapposti a quelli nella fase di carico.

Come visto in precedenza, dai coefficienti angolari è possibile ricavare la matrice di sensibilità e, invertendola, la matrice di taratura:

$$[S_{A.R.}] = \begin{bmatrix} 6.333 & -0.0007 \\ -0.1735 & 24.851 \end{bmatrix}$$

$$[C_{A.R.}] = \begin{bmatrix} 157.90 & 0.00445\\ 1.102 & 40.24 \end{bmatrix} \cdot 10^{-3}$$

Si analizza ora la cella di carico anteriore sinistra, in figura 3.19 vengono mostrate le rette di taratura ottenute sollecitando a downforce e in figura 3.20 sollecitando a drag, successivamente vengono riportate le matrici di calibrazione.



Figura 3.19: Rette di taratura della cella di carico A.L. sollecitata a downforce



Figura 3.20: Rette di taratura della cella di carico A.L. sollecitata a drag

 $[S_{A.L.}] = \begin{bmatrix} 6.497 & -0.0335 \\ -0.039 & 26.113 \end{bmatrix}$

$$[C_{A.L.}] = \begin{bmatrix} 153.92 & 0.197 \\ 0.230 & 38.30 \end{bmatrix} \cdot 10^{-3}$$

Trattando le celle di carico posteriori, la matrice di sensibilità teorica e quella di taratura teorica risultano da analisi F.E.M.:

$$[S_{P.t}] = \begin{bmatrix} 7.739 & -0.001 \\ 0.010 & 12.240 \end{bmatrix}$$
$$[C_{P.t}] = \begin{bmatrix} 129.22 & 0.0105 \\ -0.106 & 81.70 \end{bmatrix} \cdot 10^{-3}$$

La cella di carico posteriore destra presenta le seguenti rette di taratura (Figura 3.21 e 3.22) e matrici.



Figura 3.21: Rette di taratura della cella di carico P.R. sollecitata a downforce



Figura 3.22: Rette di taratura della cella di carico P.R. sollecitata a drag

$$[S_{P.R.}] = \begin{bmatrix} 7.442 & -0.0448\\ 0.1531 & 13.629 \end{bmatrix}$$
$$[C_{P.R.}] = \begin{bmatrix} 134.36 & 0.442\\ -1.509 & 73.37 \end{bmatrix} \cdot 10^{-3}$$

Infine, si analizza la cella di carico posteriore sinistra di cui vengono riportate di seguito le rette di taratura (Figura 3.23 e 3.24) e le matrici di calibrazione.



Figura 3.23: Rette di taratura della cella di carico P.L. sollecitata a downforce



Figura 3.24: Rette di taratura della cella di carico P.L. sollecitata a drag

$$[S_{A.L.}] = \begin{bmatrix} 7.887 & -0.0724\\ 0.1383 & 13.064 \end{bmatrix}$$
$$[C_{A.L.}] = \begin{bmatrix} 126.78 & 0.703\\ -1.342 & 76.54 \end{bmatrix} \cdot 10^{-3}$$

Capitolo 4: Configurazione del sistema di misura

4.1 Strumentazione per l'acquisizione dei carichi aerodinamici

Si dispone di 4 celle di carico, cioè i due supporti dell'ala anteriore e i due supporti dell'ala posteriore. Ciascuna cella di carico è in grado di misurare la componente downforce e la componente drag ad essa applicata. Si ha quindi un totale di 8 canali acquisiti attraverso le celle di carico progettate. Questi 8 canali vengono acquisiti attraverso gli 8 ingressi disponibili nella centralina "IMC Cronos-PL2" (Figura 4.1, gli 8 ingressi utilizzati corrispondono ai 4 attacchi per gli spinotti più in basso nell'immagine).



Figura 4.1: Centralina IMC Cronos-PL2

In tabella 4.1 si riporta la numerazione scelta per indicare i vari canali acquisiti.

Centralina	Canale	Nome	Posizione	Sensore	Resistenza [Ω]
IMC Cronos-PL2	C001	PR_LIFT	Supporto	Kyowa KFGS-	119.6±0.4%
			destro ala	3-120-C1-11	
			posteriore	L3M3R	
	C002	PR_DRAG	Supporto	Kyowa KFGS-	119.6±0.4%
			destro ala	3-120-C1-11	
			posteriore	L3M3R	
	C003	PL_LIFT	Supporto	Kyowa KFGS-	119.6±0.4%
			sinistro ala	3-120-C1-11	
			posteriore	L3M3R	
	C004	PL_DRAG	Supporto	Kyowa KFGS-	119.6±0.4%
			sinistro ala	3-120-C1-11	
			posteriore	L3M3R	

IMC Cronos-PL2	C005	AR_LIFT	Supporto		Kyowa KFGS-	119.6±0.4%
			destro	ala	3-120-C1-11	
			anteriore		L3M3R	
	C006	AR_DRAG	Supporto		Kyowa KFGS-	119.6±0.4%
			destro	ala	3-120-C1-11	
			anteriore		L3M3R	
	C007	AL_LIFT	Supporto		Kyowa KFGS-	119.6±0.4%
			sinistro	ala	3-120-C1-11	
			anteriore		L3M3R	
	C008	AL_DRAG	Supporto		Kyowa KFGS-	119.6±0.4%
			sinistro	ala	3-120-C1-11	
			anteriore		L3M3R	

Tabella 4.1: Scelta dei canali nella centralina IMC
4.2 Strumentazione per l'acquisizione di segnali aggiuntivi

Oltre agli 8 segnali derivanti dai carichi aerodinamici, si è deciso di acquisire altri 8 segnali significativi al fine di correlare le variazioni di carico aerodinamico con i trasferimenti di carico in vettura.

Si è scelto di acquisire i segnali dei pull dei 4 gruppi sospensione della vettura (Figura 4.2 e 4.3). Tali pull estensimetrati (Figura 4.4) sono stati ereditati dal lavoro di tesi di Riccardo Chiarin e si rimanda al suo lavoro per ulteriori dettagli [7].



Figura 4.2: Rendering del gruppo sospensione della ruota posteriore sinistra della vettura MG 13.18 (Le sigle indicate significano: R.U.W.F. Rear Up Wishbone Front, ovvero il tubo frontale del triangolo posteriore alto, R.U.W.R. Rear Up Wishbone Rear, R.L.W.F. Rear Low Wishbone Front e R.L.W.R. Rear Low Wishbone Rear) [7]



Figura 4.3: Gruppo sospensione anteriore sinistro con montato il pull estensimetrato



Figura 4.4: Pull estensimetrati e cablati

Gli altri 4 segnali che si è stabilito di misurare sono quelli derivanti dai 4 potenziometri lineari (Figura 4.5) che misurano le corse delle sospensioni. Questi potenziometri sono il modello "DIA9,5-75" dell'azienda AvioRace S.R.L. e hanno una corsa di 75 mm.

Il team utilizza tali potenziometri, per acquisire le corse delle sospensioni durante gli eventi e i test, collegati alla propria centralina della vettura. Per i test oggetto dello studio invece è stato eseguito un nuovo cablaggio dei potenziometri per collegarli alla centralina IMC (più precisamente alla sua espansione come si vedrà a breve) e avere così già tutti i dati sincronizzati.

In figura 4.6 si può osservare il potenziometro anteriore sinistro montato in parallelo con la relativa sospensione di cui misura la corsa.



Figura 4.5: Potenziometro lineare AvioRace



Figura 4.6: Potenziometro anteriore sinistro montato in vettura

Per acquisire questi aggiuntivi 8 canali è necessario utilizzare l'espansione "Cansas-L-UNI8" (Figura 4.7), un modulo di espansione della centralina IMC che consente di avere a disposizione altri 8 canali.



Figura 4.7: Espansione Cansas-L-UNI8

In tabella 4.2 si riporta la numerazione scelta per indicare i vari canali acquisiti.

Centralina	Canale	Nome	Posizione	Sensore
	F001	PULL_PR	Sospensione	Estensimetri XY
IMC Cansas	F002	PULL_PL	Sospensione posteriore sinistra	Estensimetri XY
L-UNI8	F003	PULL_AR	Sospensione anteriore destra	Estensimetri XY
	F004	PULL_AL	Sospensione anteriore sinistra	Estensimetri XY
	F005	POT_PR	Sospensione posteriore destra	AvioRace pot. lineare DIA9,5-75
IMC Cansas	F006	POT_PL	Sospensione posteriore sinistra	AvioRace pot. lineare DIA9,5-75
L-UNI8	F007	POT_AR	Sospensione anteriore destra	AvioRace pot. lineare DIA9,5-75
	F008	POT_AL	Sospensione anteriore sinistra	AvioRace pot. lineare DIA9,5-75

Tabella 4.2: Scelta dei canali nell'espansione Cansas

4.3 Configurazione software del sistema di misura

Per sapere come realizzare il collegamento hardware delle centraline e come eseguire la loro configurazione si rimanda ai manuali di utilizzo [8] e [9]. Di seguito si riportano le scelte effettuate.

La configurazione dei canali va svolta una tantum, prima dei test. Al momento dei test, o della taratura o della verifica dei segnali, basterà aprire la directory creata precedentemente che contiene già salvati tutti i settaggi decisi. I dati verranno memorizzati nella memoria interna della centralina, si consigliano almeno 400 MB di memoria libera.

Per configurare gli 8 canali della centralina Cronos si utilizza il software "imc Devices V2.6". Andando nella schermata di configurazione si può associare ad ogni ingresso un canale estensimetrico, secondo lo schema già mostrato in precedenza. Si ricorda che in ogni spinotto sono presenti due collegamenti dei canali, rispettivamente uno in posizione 1 e un in posizione 2. Poi per ogni canale si va ad impostare la durata massima dell'acquisizione (undefined, perché stopperemo manualmente di volta in volta l'acquisizione con il trigger), l'intervallo di campionamento (2 ms, ovvero una frequenza di campionamento di 500 Hz). È inoltre possibile impostare un fattore Y che converte direttamente i segnali misurati in microepsilon per esempio in forze in N, però non era utilizzabile nel nostro caso in quanto si aveva a che fare con celle di carico multicomponente che richiedevano matrici di conversione. Nella sezione "Amplifier" si può selezionare il tipo di ponte configurato, che nel caso delle celle di carico dell'aerodinamica è "General strain gauge – full bridge" (Figura 4.8), la resistenza nominale degli estensimetri utilizzati (120 Ohm), il gauge factor (2.08); si può inoltre impostare l'input range (2000 microepsilon), si consiglia di scegliere come input range il valore disponibile immediatamente superiore alle deformazioni massime che si ipotizza di avere, così da avere una risoluzione ottimale. Si è infine scelto di usare un filtro passa basso di tipo Butterworth con frequenza di taglio a 50 Hz per attenuare i disturbi derivanti dalle vibrazioni causate dal motore e da altri fattori esterni (Figura 4.9).



Figura 4.8: Configurazione del ponte di Wheatstone scelta per le celle di carico dell'aerodinamica nel software "imc Devices V2.6"

10	$\overline{A} \mathrel{\scriptscriptstyle{\pm}} \overline{V}$	× ii ii = M	I = A	gevice: Centralina				
Rate D	topessing Ev	egts Ingger	Amplifier Balance	cing				
n 0	hannel name	1	Amplifier	1	14 C 14		Filter	
2 3 4 5 5 6 7 8	PR_DRAG PL_UFT PL_DRAG AR_UFT AR_DRAG AL_UFT AL_DRAG	General strain gauge General strain gauge General strain gauge General strain gauge General strain gauge General strain gauge General strain gauge	- Miknidge 120 - Miknidge 120 - Miknidge 120 - Miknidge 120 - Miknidge 120 - Miknidge 120 - Miknidge 120	*2000 μ eps *2000 μ eps *2000 μ eps *2000 μ eps *2000 μ eps *2000 μ eps *2000 μ eps	Low pass 50 Hz Low pass 50 Hz			
ridge, current a	nd voltage amplifie suge - fullbridg -	and temperature condition	oner "UNI-S" V N = 1	Overade	wher: Bulleworth	V		
idge, custent a ieneral strain g train	nd voltage amplifie suge - full-bridg - 5	r and temperature condition	oner "UNI-8" ▼ N = 1 Unit µ ept	Character Filter toos	suffer Butterworth	×.		
idge, current a ieneral strain g train suge factor:	nd voltage amplifie suge - fulkbridg - s 2.08	r and temperature conditions of temperature conditions of the second sec	oner "Uts &" Via Unit sin coeff: 0.3	Character Filter type Cut off	salic: Butterworth Low pass 50 Hz	M		
dge, current a eneral strain g tain xuge factor: sply:	nd voltage amplifie suge - full-bridge s 2.08 5.V v	r and temperature conditi 120 Ohm Transverse st Moduku ol	oner 1UR 8° V ≈ 1 Urat µ epp arcoeft: 0.0 felastoty: 100 GPa	Obseacter Files type Cut off Insequency	sate: Builterworth Low pass 50 Hz			
dge, current a eneral strain g rain wge foctor poly. wut range:	nd voltage amplifie suge - fußbidg suge 5V \$V	r and temperature condition 120 Otm Transverse itt Modulus of ± 2000 µr eps	oner "URLS" W N = 1 Urk µeps setscept Too GPa r	Obseacter Files type Cut off Insquency	solic: Buiteworth Low pass 50 Hz	× ×		

Figura 4.9: Schermata della scheda "Amplifier" delle celle di carico nel software "imc Devices V2.6"

La configurazione dei canali collegati all'espansione Cansas va eseguita con il software "imc CANSAS". Per quanto riguarda i pull si sono riprodotte le impostazioni utilizzate da Chiarin nella sua tesi: input range 1538.46 microepsilon (Figura 4.10), "Full bridge, Poisson gages in adjacent arms" (Figura 4.11), Gauge factor 2 (Figura 4.12).

ouped by messages Un	niversal amplifier input channel: PULL_PR	
Counced by resistance Towards of the resistance Counce of the resist	Versisa angeleer input charmet PAL PR Pouls Render could Scaling Message Megping Orout Info Terminul Name: PAL PR Comment: PAL PR Comment: PAL PR Comment: Product render Product: Product render Product ren	

Figura 4.10: Schermata della scheda "Inputs" dei pull nel software "imc CANSAS"

	X 188 5 M V & 1 M
Grouped by messages Transitor J ² AC, carchi wero mbb W (MB) Who in which CAP by interface CAP by	Universal amplifier rout, channel: PLUL_PR Trypts: Binding crout is Scaling Message Mapping Orcus. Info Product configuration: The fording - Processing agrees in adjustrant strain $f_{int} = \frac{K_{int}}{K_{int}}$ $f_{int} = \frac{K_{int}}{K_{int}}$ $f_{int} = \frac{K_{int}}{K_{int}}$ $f_{int} = \frac{K_{int}}{K_{int}}$ The definition of the adjustrant balances: The vertice bodied in adjustrant for for balances for the balance for th

Figura 4.11: Schermata della scheda "Bridge circuit" dei pull nel software "imc CANSAS"

	Universal a	mplifier input chann	el: PULL_PR		
Tarterio 1544, carch, error, areo and	Unversa la Trouts I U	Indian rout dann Indian creat. Sealt Enternada totan - Stan gases pro- Gauge Factor: - Material paramet Poisons Ratio: - Literic Modular Notes:	k FULL_PR Prostage Mopping Circuit Info protes 2 Tripical range 1.94.7 0.3 7 Tripical range 1.94.7 an C.3 Tripical range 1.94.7 Tripical range 1.94.	Constant of the second	

Figura 4.12: Schermata della scheda "Scaling" dei pull nel software "imc CANSAS"

Per i segnali derivanti dai potenziometri invece si sono configurati gli ultimi 4 canali restanti disponibili nell'espansione. Per essi è sufficiente impostare il voltaggio di alimentazione (5 V) (Figura 4.13), inoltre è possibile inserire direttamente una retta di taratura (Figura 4.14) così da avere immediatamente il segnale in mm (tale taratura è stata poi verificata applicando corse note ai potenziometri e rilevando i segnali misurati).

By Ext Yew Body Extra 1940 Image Dy Massage Transfor 7542, cricht angliter rout downet. POT FR Image Dy Massage Name Image Name Im	🖙 ime CANSAS		
Organizity in the stage is a stage if is in the durate IPOT_FR Organizity in the stage is a stage if is in the durate IPOT_FR Organization of Sec. (a) Message Magang Organization in the stage is a stage in the interval in the stage is a stage in the interval in the stage is a stage in the interval in the stage is a stage in the interval interval in the stage is a stage in the interval interval in the stage is a stage in the interval interv	Ble Edit Wew Module Extras Help		
Grouped by messages	00H91+001m2m2		
Image: Instruction year, and year of the provide and the provid	Grouped by messages	Universal amplifier input channel: POT_PR	
lasty	and up to yom house ■ Structure JAL, cardid, service and ■ Structure JAL, cardid, service ① Devends applier ■ Conversion applier ■ Conversion ■ Conve	Typeds Bridge mould Scaling Message Mapping Caruli Turini Typeds Terminal Dis. 6 + 195 - 195 (DSUB Pin 2 10) Name: POT_PR Commerk: Potage Oberacteristic Watage Oberacteristic Bridde Dip. K range: ± 75 mm (Dir. ± 5 V) Sampling intervalit 2 ms	
	leady		

Figura 4.13: Schermata della scheda "Inputs" dei potenziometri nel software "imc CANSAS"

uped by messages
Ale ur monique Ale ur monique URS (5%) Cardi, sero.mb) URS (5%) Cardi, sero.mb) Curvess anythin Curvess anythin Curves a

Figura 4.14: Schermata della scheda "Scaling" dei potenziometri nel software "imc CANSAS"

Come intervallo di campionamento anche per i segnali acquisiti con l'espansione Cansas si è impostato 2 ms. Una volta configurati anche i canali dell'espansione, le impostazioni vanno importate nel software "imc Devices" come spiegato nei manuali già citati. Ciò va fatto una sola volta, nei successivi utilizzi basterà aprire la directory e si troveranno già le configurazioni impostate anche dei canali della Cansas (Figura 4.15).

| Tem. Charrel name Mode Samp region Tingger Tingger A holdy region (a) HRQ (DRA) Analysis

 | Tem. Dannel name Mode Specifier Dustion -/- Alt project Trapper -db. India project Classical india projecrobit Classical indindia project <t< th=""><th>Tem. Characteriance Mode Sangture Duration Characteriance Tragger Astrogradue Astrogradue Columna Columa Columa Columna</th><th>Tem. Denominante Mode Singuine Dustrion Figure Trigger d.a. Induity reput Ambigraphic Ambigraphic Ambigraphic Column underhead Colum underhead Column underhead</th><th>Tem. Charmel name Mode Sample route Other set of the set of the</th><th>Tem. Tem. Drawnel name Mode Sample rock All support Tigger Lab Andrey rock Collamon Collamon</th><th>Tem. Channel name Mode Sample Tele Current Tigger -db. Instant region Control Current <t< th=""><th>Tem. Charmel name Mode Sample (2000) 1-/1 Range Togget LB Analyzerov 20001 Coll #20100 Coll #20100 20001 <</th><th>L Tem. Description Mode Service (no.) Particle Togget Lab. Analysis Analys</th><th>Tem. Dramel name Mode Sample number Local page Tagget La Analyzerus 2010 001 #28/L121 24/L221 2010 undefined 2000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 2000 #200 2000 #200 2000 #200 1000 #200 #200 1000 #200 #200 1000 #200 #200 1000 #200 #200 #200 #200 #200 #20<</th><th>L Tem. Depredmentation Mode Service To Duration Togget La Indian product Cold Institution Ammentation Cold Institution Product Cold Institution</th><th>L Tem. Depared name Mode Service Te Dustrie Togget Jandary recut. Jandary recut. Jandary recut. Togget Togget Jandary recut. Jandary recut. Jandary recut. Togget Togget Jandary recut. Jandary recut. Jandary recut. Jandary recut. Jandary recut. Jandary recut. Jandary recut. Ja</th><th>L Tem. Durand name Mode Sample number Durande number Tragger La India, India, Parka American et al. American et al. American et al. Tragger Tragger La India, India, Parka American et al. American et al. Tragger Tragger Tragger La India. La India. American et al. Tragger Tragger Tragger La India. La India. La India. American et al. Tragger Tragger La India. La India. La India. American et al. 20 ms. undefined 2000 u.e.go.7 La India. La India. La India. American et al. 20 ms. undefined 2000 u.go.7 La India. La India. La India. American et al. 20 ms. undefined 2000 u.go.7 La India. La India. La India. Single endid. 20 ms. undefined 2000 u.go.7 La India. La India. Single endid. 20 ms. undefined 7 La India. La India.</th><th>Tem. Dannel name Mode Samplere Duration Particle Trogget Jandary rec. <t< th=""><th>Tem. Dannel name Mode Singlerin Dualities Trigger Jandbyrgel Jandbyrgel Jandbyrgel Jandbyrgel Jandbyrgel Trigger Jandbyrgel Jandbyrgel</th><th>Tem. Denominante Mode Singline Duration F. Barge Trigget db. Andrographic Andrographic Andrographic Control or Contro or Control or Contro or Control or Control or Con</th><th>Tem. Channel name Mode Samptime Duttion (-/, Barget Togget Ab Stratumer Calment Calm</th><th>Tem. Charmel name Mode Sample number Log Product Tagget -Ab. Analog routing 201011 010111 01011 010111</th><th>Tem. Dramel name Mode Sample number Dustries Tragget La Ansky pruck 2010 000000000 10000000000 100000000000 1000000000000000000000000000000000000</th><th>Tem. Charrel name Mode Sample (During)
 Tigger La Mode yords Anticle 20001 (Calification) 0 La Mode yords Anticle 20001 (Calification) 0 La Mode yords Anticle 20001 (Calification) 0 La Mode yords 20011 (Calification) 0 0 La Mode (La Mode) 20011 (Calification) 0 0 La Mode 1001 ALUFT Antification 2000111100 0 La Mode 1001 ALUFT Antification 2000111000 7 La Mode 101 FULL PR Single rodd 20001110000 7 La Mode 101 FULL PR Single rodd 200011100000 7 La Mode 101</th><th>Tem. Charmel name Mode Sample Topic Topper A holdy cruck Analyse of the constraint o</th><th>Tem. Charmel name Mode Sample route Other and state Data of a state A holdy crude Column and state Column and state Column and state Togget A holdy crude Column and state Column and state</th><th>Tem. Depred name Mode Sample rob Public Particle Togget An Andrey rob. Andrey rob. Andrey rob. Andrey rob. France Togget Andrey rob. Andrey rob. Andrey rob. Andrey rob. France Togget Andrey rob. Andrey rob. Andrey rob. Andrey rob. 200 ms 4000 ms ope 7 Andrey rob. In 604 Ioi PLUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. In 604 Ioi PLUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 200 ms ope 7 Andrey rob. Ioi ALUTT Single ended 20 ms undefined 7 Andrey rob. Ioi ALUTT<</th><th>Tem. Dannel name Mode Singlerin Dualities Trigger Jandbyrgold (a) Ballitätie Jandbyrgold Singlerin Dualities Dualiti</th><th>Tem. Denominante Mode Singline Duration F. Barge Trigget db Andrographic Andrographic Andrographic Control or Contro or Control or Contro or Control or Control or Cont</th><th>Tem. Diamedinative Mode Service in [0.04] Duration -// Range Togget -db. India C1 20101 C1 20101 India 20001 India 20001 India 20001 India 20001 India 20001 India 20001<!--</th--><th>Tem. Charnel name Mode Sample (Direct) All Races Tigger La Analysing (Direct) All Races 20011 01 100 10000</th><th>Tem. Dannel name Mode Samptime Dualization -// Parage Trigger d. Infoll (1) #241Link Zamptime 201m undefined 2000 prot 7 d. Infoll (1) #241Link Zamptime 20 mm undefined 2000 prot 7 d. Infoll (1) #241Link Zamptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Single ended 20 mm undefined 7 d. (4) PULL PL Single ended 20 mm undefined 7</th></th></t<></th></t<></th></t<> | Tem. Characteriance Mode Sangture Duration Characteriance Tragger Astrogradue Astrogradue Columna Columa Columa Columna

 | Tem. Denominante Mode Singuine Dustrion Figure Trigger d.a. Induity reput Ambigraphic Ambigraphic Ambigraphic Column underhead Colum underhead Column underhead

 | Tem. Charmel name Mode Sample route Other set of the

 | Tem. Tem. Drawnel name Mode Sample rock All support Tigger Lab Andrey rock Collamon

 | Tem. Channel name Mode Sample Tele Current Tigger -db. Instant region Control Current Current <t< th=""><th>Tem. Charmel name Mode Sample (2000) 1-/1 Range Togget LB Analyzerov 20001 Coll #20100 Coll #20100 20001 20001 20001 20001 20001
 20001 <</th><th>L Tem. Description Mode Service (no.) Particle Togget Lab. Analysis Analys</th><th>Tem. Dramel name Mode Sample number Local page Tagget La Analyzerus 2010 001 #28/L121 24/L221 2010 undefined 2000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 2000 #200 2000 #200 2000 #200 1000 #200 #200 1000 #200 #200 1000 #200 #200 1000 #200 #200 #200 #200 #200 #20<</th><th>L Tem. Depredmentation Mode Service To Duration Togget La Indian product Cold Institution Ammentation Cold Institution Product Cold Institution</th><th>L Tem. Depared name Mode Service Te Dustrie Togget Jandary recut. Jandary recut. Jandary recut. Togget Togget Jandary recut. Jandary recut. Jandary recut. Togget Togget Jandary recut. Jandary recut. Jandary recut. Jandary recut. Jandary recut. Jandary recut. Jandary recut. Ja</th><th>L Tem. Durand name Mode Sample number Durande number Tragger La India, India, Parka American et al. American et al. American et al. Tragger Tragger La India, India, Parka American et al. American et al. Tragger Tragger Tragger La India. La India. American et al. Tragger Tragger Tragger La India. La India. La India. American et al. Tragger Tragger La India. La India. La India. American et al. 20 ms. undefined 2000 u.e.go.7 La India. La India. La India. American et al. 20 ms. undefined 2000 u.go.7 La India. La India. La India. American et al. 20 ms. undefined 2000 u.go.7 La India. La India. La India. Single endid. 20 ms. undefined 2000 u.go.7 La India. La India. Single endid. 20 ms. undefined 7 La India. La India.</th><th>Tem. Dannel name Mode Samplere Duration Particle Trogget Jandary rec. <t< th=""><th>Tem. Dannel name Mode Singlerin Dualities Trigger Jandbyrgel Jandbyrgel Jandbyrgel Jandbyrgel Jandbyrgel Trigger Jandbyrgel Jandbyrgel</th><th>Tem. Denominante Mode Singline Duration F. Barge Trigget db. Andrographic Andrographic Andrographic Control or Contro or Control or Contro or Control or Control or Con</th><th>Tem. Channel name Mode Samptime Duttion (-/, Barget Togget Ab Stratumer Calment Calm</th><th>Tem. Charmel name Mode Sample number Log Product Tagget -Ab. Analog routing 201011 010111 01011 010111</th><th>Tem. Dramel name Mode Sample number Dustries Tragget La Ansky pruck 2010 000000000 10000000000 100000000000 1000000000000000000000000000000000000</th><th>Tem. Charrel name Mode Sample (During) Tigger La Mode yords Anticle 20001 (Calification) 0 La Mode yords Anticle 20001 (Calification) 0 La Mode yords Anticle 20001 (Calification) 0 La Mode yords 20011 (Calification) 0 0 La Mode (La Mode) 20011 (Calification) 0 0 La Mode 1001 ALUFT Antification 2000111100 0 La Mode 1001 ALUFT Antification 2000111000 7 La Mode 101 FULL PR Single rodd 20001110000 7 La Mode 101 FULL PR Single rodd 200011100000 7 La Mode 101</th><th>Tem. Charmel name Mode Sample Topic Topper A holdy cruck Analyse of the constraint o</th><th>Tem. Charmel name Mode Sample route Other and state Data of a state A holdy crude Column and state Column and state Column and state Togget A holdy crude Column and state Column and state</th><th>Tem. Depred name Mode Sample rob Public Particle Togget An Andrey rob. Andrey rob. Andrey rob. Andrey rob. France Togget Andrey rob. Andrey rob. Andrey rob. Andrey rob. France Togget Andrey rob. Andrey rob. Andrey rob. Andrey rob. 200 ms 4000 ms ope 7 Andrey rob. In 604 Ioi PLUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. In 604 Ioi PLUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 200 ms ope 7 Andrey rob. Ioi
ALUTT Single ended 20 ms undefined 7 Andrey rob. Ioi ALUTT<</th><th>Tem. Dannel name Mode Singlerin Dualities Trigger Jandbyrgold (a) Ballitätie Jandbyrgold Singlerin Dualities Dualiti</th><th>Tem. Denominante Mode Singline Duration F. Barge Trigget db Andrographic Andrographic Andrographic Control or Contro or Control or Contro or Control or Control or Cont</th><th>Tem. Diamedinative Mode Service in [0.04] Duration -// Range Togget -db. India C1 20101 C1 20101 India 20001 India 20001 India 20001 India 20001 India 20001 India 20001<!--</th--><th>Tem. Charnel name Mode Sample (Direct) All Races Tigger La Analysing (Direct) All Races 20011 01 100 10000</th><th>Tem. Dannel name Mode Samptime Dualization -// Parage Trigger d. Infoll (1) #241Link Zamptime 201m undefined 2000 prot 7 d. Infoll (1) #241Link Zamptime 20 mm undefined 2000 prot 7 d. Infoll (1) #241Link Zamptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Single ended 20 mm undefined 7 d. (4) PULL PL Single ended 20 mm undefined 7</th></th></t<></th></t<> | Tem. Charmel name Mode Sample (2000) 1-/1 Range Togget LB Analyzerov 20001 Coll #20100 Coll #20100 20001 <

 | L Tem. Description Mode Service (no.) Particle Togget Lab. Analysis Analys

 | Tem. Dramel name Mode Sample number Local page Tagget La Analyzerus 2010 001 #28/L121 24/L221 2010 undefined 2000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 20 1000 #200 2000 #200 2000 #200 2000 #200 1000 #200 #200 1000 #200 #200 1000 #200 #200 1000 #200 #200 #200 #200 #200 #20<

 | L Tem. Depredmentation Mode Service To Duration Togget La Indian product Cold Institution Ammentation Cold Institution Product Cold Institution

 | L Tem. Depared name Mode Service Te Dustrie Togget Jandary recut. Jandary recut. Jandary recut. Togget Togget Jandary recut. Jandary recut. Jandary recut. Togget Togget Jandary recut. Jandary recut. Jandary recut. Jandary recut. Jandary recut. Jandary recut. Jandary recut. Ja

 | L Tem. Durand name Mode Sample number Durande number Tragger La India, India, Parka American et al. American et al. American et al. Tragger Tragger La India, India, Parka American et al. American et al. Tragger Tragger Tragger La India. La India. American et al. Tragger Tragger Tragger La India. La India. La India. American et al. Tragger Tragger La India. La India. La India. American et al. 20 ms. undefined 2000 u.e.go.7 La India. La India. La India. American et al. 20 ms. undefined 2000 u.go.7 La India. La India. La India. American et al. 20 ms. undefined 2000 u.go.7 La India. La India. La India. Single endid. 20 ms. undefined 2000 u.go.7 La India. La India. Single endid. 20 ms. undefined 7 La India. La India.

 | Tem. Dannel name Mode Samplere Duration Particle Trogget Jandary rec. Jandary rec. <t< th=""><th>Tem. Dannel name Mode Singlerin Dualities Trigger Jandbyrgel Jandbyrgel Jandbyrgel Jandbyrgel Jandbyrgel Trigger Jandbyrgel Jandbyrgel</th><th>Tem. Denominante Mode Singline Duration F. Barge Trigget db. Andrographic Andrographic Andrographic Control or Contro or Control or Contro or Control or Control or Con</th><th>Tem. Channel name Mode Samptime Duttion (-/, Barget Togget Ab Stratumer Calment Calm</th><th>Tem. Charmel name Mode Sample number Log Product Tagget -Ab. Analog routing 201011 010111 01011 010111</th><th>Tem. Dramel name Mode Sample number Dustries Tragget La Ansky pruck 2010 000000000 10000000000 100000000000 1000000000000000000000000000000000000</th><th>Tem. Charrel name Mode Sample (During) Tigger La Mode yords Anticle 20001 (Calification) 0 La Mode yords Anticle 20001 (Calification) 0 La Mode yords Anticle 20001 (Calification) 0 La Mode yords 20011 (Calification) 0 0 La Mode (La Mode) 20011 (Calification) 0 0 La Mode 1001 ALUFT Antification 2000111100 0 La Mode 1001 ALUFT Antification 2000111000 7 La Mode 101 FULL PR Single rodd 20001110000 7 La Mode 101 FULL PR Single rodd 200011100000 7 La Mode 101</th><th>Tem. Charmel name Mode Sample Topic Topper A holdy cruck Analyse of the constraint o</th><th>Tem. Charmel name Mode Sample route Other and state Data of a state A holdy crude Column and state Column and state Column and state Togget A holdy crude Column and state Column and state</th><th>Tem. Depred name Mode Sample rob Public Particle Togget An Andrey rob. Andrey rob. Andrey rob. Andrey rob. France Togget Andrey rob. Andrey rob. Andrey rob. Andrey rob. France Togget Andrey rob. Andrey rob. Andrey rob. Andrey rob. 200 ms 4000 ms ope 7 Andrey rob. In 604 Ioi PLUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. In 604 Ioi PLUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 200 ms ope 7 Andrey rob. Ioi ALUTT Single ended 20 ms undefined 7 Andrey rob. Ioi ALUTT<</th><th>Tem. Dannel name Mode Singlerin Dualities Trigger Jandbyrgold (a) Ballitätie Jandbyrgold Singlerin Dualities Dualiti</th><th>Tem. Denominante Mode Singline Duration F. Barge Trigget db Andrographic Andrographic Andrographic Control or Contro or Control or Contro or Control or Control or Cont</th><th>Tem. Diamedinative Mode Service in [0.04] Duration -// Range Togget -db. India C1 20101 C1 20101 India 20001 India 20001 India 20001 India 20001 India 20001 India 20001<!--</th--><th>Tem. Charnel name Mode Sample (Direct) All Races Tigger La Analysing (Direct) All Races 20011 01 100 1000
 1000 10000</th><th>Tem. Dannel name Mode Samptime Dualization -// Parage Trigger d. Infoll (1) #241Link Zamptime 201m undefined 2000 prot 7 d. Infoll (1) #241Link Zamptime 20 mm undefined 2000 prot 7 d. Infoll (1) #241Link Zamptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Single ended 20 mm undefined 7 d. (4) PULL PL Single ended 20 mm undefined 7</th></th></t<> | Tem. Dannel name Mode Singlerin Dualities Trigger Jandbyrgel Jandbyrgel Jandbyrgel Jandbyrgel Jandbyrgel Trigger Jandbyrgel
 | Tem. Denominante Mode Singline Duration F. Barge Trigget db. Andrographic Andrographic Andrographic Control or
Contro or Control or Contro or Control or Control or Con
 | Tem. Channel name Mode Samptime Duttion (-/, Barget Togget Ab Stratumer Calment Calm
 | Tem. Charmel name Mode Sample number Log Product Tagget -Ab. Analog routing 201011 010111 01011 010111

 | Tem. Dramel name Mode Sample number Dustries Tragget La Ansky pruck 2010 000000000 10000000000 100000000000 1000000000000000000000000000000000000
 | Tem. Charrel name Mode Sample (During) Tigger La Mode yords Anticle 20001 (Calification) 0 La Mode yords Anticle 20001 (Calification) 0 La Mode yords Anticle 20001 (Calification) 0 La Mode yords 20011 (Calification) 0 0 La Mode (La Mode) 20011 (Calification) 0 0 La Mode 1001 ALUFT Antification 2000111100 0 La Mode 1001 ALUFT Antification 2000111000 7 La Mode 101 FULL PR Single rodd 20001110000 7 La Mode 101 FULL PR Single rodd 200011100000 7 La Mode 101
 | Tem. Charmel name Mode Sample Topic Topper A holdy cruck Analyse of the constraint o
 | Tem. Charmel name Mode Sample route Other and state Data of a state A holdy crude Column and state Column and state Column and state Togget A holdy crude Column and state
 | Tem. Depred name Mode Sample rob Public Particle Togget An Andrey rob. Andrey rob. Andrey rob. Andrey rob. France Togget Andrey rob. Andrey rob. Andrey rob. Andrey rob. France Togget Andrey rob. Andrey rob. Andrey rob. Andrey rob. 200 ms 4000 ms ope 7 Andrey rob. In 604 Ioi PLUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. In 604 Ioi PLUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 2000 ms ope 7 Andrey rob. Ioi ALUTT Andrey rob. 20 ms undefined 200 ms ope 7 Andrey rob. Ioi ALUTT Single ended 20 ms undefined 7 Andrey rob. Ioi ALUTT<
 | Tem. Dannel name Mode Singlerin Dualities Trigger Jandbyrgold (a) Ballitätie Jandbyrgold Singlerin Dualities Dualiti | Tem. Denominante Mode Singline Duration F. Barge Trigget db Andrographic Andrographic Andrographic Control or Contro or Control or Contro or Control or Control or Cont
 | Tem. Diamedinative Mode Service in [0.04] Duration -// Range Togget -db. India C1 20101 C1 20101 India 20001 India 20001 India 20001 India 20001 India 20001 India 20001 </th <th>Tem. Charnel name Mode Sample (Direct) All Races Tigger La Analysing (Direct) All Races 20011 01 100 10000</th> <th>Tem. Dannel name Mode Samptime Dualization -// Parage Trigger d. Infoll (1) #241Link Zamptime 201m undefined 2000 prot 7 d. Infoll (1) #241Link Zamptime 20 mm undefined 2000 prot 7 d. Infoll (1) #241Link Zamptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Single ended 20 mm undefined 7 d. (4) PULL PL Single ended 20 mm undefined 7</th> | Tem. Charnel name Mode Sample (Direct) All Races Tigger La Analysing (Direct) All Races 20011 01 100 10000 | Tem. Dannel name Mode Samptime Dualization -// Parage Trigger d. Infoll (1) #241Link Zamptime 201m undefined 2000 prot 7 d. Infoll (1) #241Link Zamptime 20 mm undefined 2000 prot 7 d. Infoll (1) #241Link Zamptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Amptime 20 mm undefined 2000 prot 7 d. Infoll (4) #41Link Single ended 20 mm undefined 7 d. (4) PULL PL Single ended 20 mm undefined 7 |

--

--
--
--

--
--

--
--
--
--
--
--
--
--
--
--
--
--
--

--
--

--
--
--
--
--

--
--

--
--

--
--
--
--
--

--
--
--

--

--
--
--
--
--

--
---|--

--	---
American Other Statution American 200 mm Value	

 | a. B. de Stratue Statue Autority Point Statue Point Statue -d.B. Mold 189 ULE Autority 20 min Statue 2000 prep 7 -d.B. Mold 189 ULE Autority 20 min undefined 2000 prep 7 -d.B. Mold 189 ULE Angelier 20 min undefined 2000 prep 7 -d.B. Mold 189 ULE Angelier 20 min undefined 2000 prep 7 -d.B. Mold 189 ULE Angelier 20 min undefined 2000 prep 7 -d.B. Mold 184 ULE Angelier 20 min undefined 2000 prep 7 -d.B. Mold MultipR Strade mold 20 min undefined 2000 prep 7 -d.B. Mold MultipR Strade mold 20 min undefined 2000 prep 7 -d.B. 164 ULE Strade mold 20 min undefined <td< th=""><th>An above Oils (FS) BUILTI Amplien 20 mm underland 0.000 mm 0.000 mm</th><th>A. End of the SPL ULE in the structure of the struc</th><th>All Ansolution Ansolution Ansolution All Ansolution Coll PR, DRAG Ansolution Coll PR, DRAG -All In/C3 (a) PR, UFT Ansolution Coll PR, DRAG -All In/C3 (a) PR, UFT Ansolution 20 ms undefined 2000 µ opp 7 -All In/C3 (a) PL, UFT Ansolution 20 ms undefined 2000 µ opp 7 -All In/C5 (a) AL, UFT Ansolution 20 ms undefined 2000 µ opp 7 -All In/C7 (a) AL, UFT Ansolution 20 ms undefined 2000 µ opp 7 -All In/C7 (a) AL, UFT Ansolution 20 ms undefined 2000 µ opp 7 -All In/C7 (a) PULL_PR Style ended 20 ms undefined 7 -All In PULL_PR Style ended 20 ms undefined 7 -All<</th><th>All: All: <td< th=""><th>All Instruction (a) 531 bit in Anglier (b) 500 with states in the state in the stat</th><th>B. A. 2010/2010 OIL FOLLOW Anstrong Column <thcolumn< th=""></thcolumn<></th><th>B. M. Difference Constraint Constant Constraint Con</th><th>Bit All Difference Differenc Differenc<!--</th--><th>Description Constraint <thconstraint< th=""> Constraint Constra</thconstraint<></th><th>Description Constraint <thconstraint< th=""> Constraint Constra</thconstraint<></th><th>Description Constraint <thconstraint< th=""> Constraint Constra</thconstraint<></th><th>B. B. Difference Constrained <thconstrained< th=""></thconstrained<></th><th>B. Martinology Control (M) Solution Solution<th>A. Endback (a) FSU(L) Annolive Annolive<</th><th>And End/20 (a) III ESE USING Answirth 200 ms undefined 200 ms undefined 200 ms 200 ms<!--</th--><th>A. Difference OIL FERUE American OUT Difference <thdifference< th=""> Difference <t< th=""><th>Description Other Status Other Status<!--</th--><th>All All (Mark Constraint All (Mark All</th><th>A. Difference Old Field Unit Anstrong Cold price P A. India India</th><th>All Amount Constraint Constraint</th><th>B. B. Differ Control Contro Control <thcontrol< th=""></thcontrol<></th><th>B. Martinology Control (M) Solution Solution</th></th></t<><th>A. Endback (a) FSU(L) Annolive Annolive<</th><th>Description Constraint Constr</th><th>Display Display Ansilons Ansilons Colored Colored</th><th>A Bit Disc. Cold Bit Disc. <thcold bit="" disc.<="" th=""> Cold Bit Disc.</thcold></th></thdifference<></th></th></th></th></td<></th></td<> | An above Oils (FS) BUILTI Amplien 20 mm underland 0.000 mm

 | A. End of the SPL ULE in the structure of the struc

 | All Ansolution Ansolution Ansolution All Ansolution Coll PR, DRAG Ansolution Coll PR, DRAG -All In/C3 (a) PR, UFT Ansolution Coll PR, DRAG -All In/C3 (a) PR, UFT Ansolution 20 ms undefined 2000 µ opp 7 -All In/C3 (a) PL, UFT Ansolution 20 ms undefined 2000 µ opp 7 -All In/C5 (a) AL, UFT Ansolution 20 ms undefined 2000 µ opp 7 -All In/C7 (a) AL, UFT
Ansolution 20 ms undefined 2000 µ opp 7 -All In/C7 (a) AL, UFT Ansolution 20 ms undefined 2000 µ opp 7 -All In/C7 (a) PULL_PR Style ended 20 ms undefined 7 -All In PULL_PR Style ended 20 ms undefined 7 -All<

 | All: All: <td< th=""><th>All Instruction (a) 531 bit in Anglier (b) 500 with states in the state in the stat</th><th>B. A. 2010/2010 OIL FOLLOW Anstrong Column <thcolumn< th=""></thcolumn<></th><th>B. M. Difference Constraint Constant Constraint Con</th><th>Bit All Difference Differenc Differenc<!--</th--><th>Description Constraint <thconstraint< th=""> Constraint Constra</thconstraint<></th><th>Description Constraint <thconstraint< th=""> Constraint Constra</thconstraint<></th><th>Description Constraint <thconstraint< th=""> Constraint Constra</thconstraint<></th><th>B. B. Difference Constrained <thconstrained< th=""></thconstrained<></th><th>B. Martinology Control (M) Solution Solution<th>A. Endback (a) FSU(L) Annolive Annolive<</th><th>And End/20 (a) III ESE USING Answirth 200 ms undefined 200 ms undefined 200 ms
 200 ms<!--</th--><th>A. Difference OIL FERUE American OUT Difference <thdifference< th=""> Difference <t< th=""><th>Description Other Status Other Status<!--</th--><th>All All (Mark Constraint All (Mark All</th><th>A. Difference Old Field Unit Anstrong Cold price P A. India India</th><th>All Amount Constraint Constraint</th><th>B. B. Differ Control Contro Control <thcontrol< th=""></thcontrol<></th><th>B. Martinology Control (M) Solution Solution</th></th></t<><th>A. Endback (a) FSU(L) Annolive Annolive<</th><th>Description Constraint Constr</th><th>Display Display Ansilons Ansilons Colored Colored</th><th>A Bit Disc. Cold Bit Disc. <thcold bit="" disc.<="" th=""> Cold Bit Disc.</thcold></th></thdifference<></th></th></th></th></td<> | All Instruction (a) 531 bit in Anglier (b) 500 with states in the state in the stat

 | B. A. 2010/2010 OIL FOLLOW Anstrong Column Column <thcolumn< th=""></thcolumn<>

 | B. M. Difference Constraint Constant Constraint Con

 | Bit All Difference Differenc Differenc </th <th>Description Constraint <thconstraint< th=""> Constraint Constra</thconstraint<></th> <th>Description Constraint <thconstraint< th=""> Constraint Constra</thconstraint<></th> <th>Description Constraint <thconstraint< th=""> Constraint Constra</thconstraint<></th> <th>B. B. Difference Constrained <thconstrained< th=""></thconstrained<></th> <th>B. Martinology Control (M) Solution Solution<th>A. Endback (a) FSU(L) Annolive Annolive<</th><th>And End/20 (a) III ESE USING Answirth 200 ms undefined 200 ms undefined 200 ms 200 ms<!--</th--><th>A. Difference OIL FERUE American OUT Difference <thdifference< th=""> Difference <t< th=""><th>Description Other Status Other Status<!--</th--><th>All All (Mark Constraint All (Mark
 All</th><th>A. Difference Old Field Unit Anstrong Cold price P A. India India</th><th>All Amount Constraint Constraint</th><th>B. B. Differ Control Contro Control <thcontrol< th=""></thcontrol<></th><th>B. Martinology Control (M) Solution Solution</th></th></t<><th>A. Endback (a) FSU(L) Annolive Annolive<</th><th>Description Constraint Constr</th><th>Display Display Ansilons Ansilons Colored Colored</th><th>A Bit Disc. Cold Bit Disc. <thcold bit="" disc.<="" th=""> Cold Bit Disc.</thcold></th></thdifference<></th></th></th> | Description Constraint Constraint <thconstraint< th=""> Constraint Constra</thconstraint<>

 | Description Constraint Constraint <thconstraint< th=""> Constraint Constra</thconstraint<>

 | Description Constraint Constraint <thconstraint< th=""> Constraint Constra</thconstraint<>

 | B. B. Difference Constrained Constrained <thconstrained< th=""></thconstrained<>
 | B. Martinology Control (M) Solution Solution <th>A. Endback (a) FSU(L) Annolive Annolive<</th> <th>And End/20 (a) III ESE USING Answirth 200 ms undefined 200 ms undefined 200 ms 200 ms<!--</th--><th>A. Difference OIL FERUE American OUT Difference <thdifference< th=""> Difference <t< th=""><th>Description Other Status Other Status<!--</th--><th>All All (Mark Constraint All (Mark All</th><th>A. Difference Old Field Unit Anstrong Cold price P A. India India</th><th>All Amount
 Constraint Constraint</th><th>B. B. Differ Control Contro Control <thcontrol< th=""></thcontrol<></th><th>B. Martinology Control (M) Solution Solution</th></th></t<><th>A. Endback (a) FSU(L) Annolive Annolive<</th><th>Description Constraint Constr</th><th>Display Display Ansilons Ansilons Colored Colored</th><th>A Bit Disc. Cold Bit Disc. <thcold bit="" disc.<="" th=""> Cold Bit Disc.</thcold></th></thdifference<></th></th> | A. Endback (a) FSU(L) Annolive Annolive<
 | And End/20 (a) III ESE USING Answirth 200 ms undefined 200 ms undefined 200 ms 200 ms </th <th>A. Difference OIL FERUE American OUT Difference <thdifference< th=""> Difference <t< th=""><th>Description Other Status Other Status<!--</th--><th>All All (Mark Constraint All (Mark All</th><th>A. Difference Old Field Unit Anstrong Cold price P A. India India</th><th>All Amount Constraint Constraint</th><th>B. B. Differ Control Contro Control <thcontrol< th=""></thcontrol<></th><th>B. Martinology Control (M) Solution Solution</th></th></t<><th>A. Endback (a) FSU(L) Annolive Annolive<</th><th>Description Constraint Constr</th><th>Display Display Ansilons Ansilons Colored Colored</th><th>A Bit Disc. Cold Bit Disc. <thcold bit="" disc.<="" th=""> Cold Bit Disc.</thcold></th></thdifference<></th> | A. Difference OIL FERUE American OUT Difference Difference <thdifference< th=""> Difference <t< th=""><th>Description Other Status Other Status<!--</th--><th>All All (Mark Constraint All (Mark All</th><th>A. Difference Old Field Unit Anstrong Cold price P A. India India</th><th>All Amount Constraint Constraint</th><th>B. B. Differ Control Contro Control <thcontrol< th=""></thcontrol<></th><th>B. Martinology Control (M) Solution Solution</th></th></t<><th>A. Endback (a) FSU(L) Annolive Annolive<</th><th>Description Constraint Constr</th><th>Display Display Ansilons Ansilons Colored Colored</th><th>A Bit Disc. Cold Bit Disc. <thcold bit="" disc.<="" th=""> Cold Bit Disc.</thcold></th></thdifference<> | Description Other Status Other Status </th <th>All All (Mark Constraint All (Mark All</th> <th>A. Difference Old Field Unit Anstrong Cold price P A. India India</th> <th>All Amount Constraint Constraint</th> <th>B. B. Differ Control Contro Control <thcontrol< th=""></thcontrol<></th> <th>B. Martinology Control (M) Solution Solution</th>
 | All All (Mark Constraint All (Mark All
 | A. Difference Old Field Unit Anstrong Cold price P A. India
 | All Amount Constraint
 | B. B. Differ Control Contro Control <thcontrol< th=""></thcontrol<> | B. Martinology Control (M) Solution
 | A. Endback (a) FSU(L) Annolive Annolive< | Description Constraint Constr | Display Display Ansilons Ansilons Colored | A Bit Disc.
Cold Bit Disc. <thcold bit="" disc.<="" th=""> Cold Bit Disc.</thcold> |
| Ab Ind2 (a) PR_DPAG Amplifier 20 me undefined 2000 µ eps 7 Ab Ind3 (a) PL_DPAG Amplifier 20 me undefined 2000 µ eps 7 Ab Ind3 (a) PL_DPAG Amplifier 20 me undefined 2000 µ eps 7 Ab Ind3 (a) AD_DPAG Amplifier 20 me undefined 2000 µ eps 7 Ab Ind3 (a) AD_DPAG Amplifier 20 me undefined 2000 µ eps 7 Ab Ind3 (a) AD_DPAG Amplifier 20 me undefined 2000 µ eps 7 Ab Ind3 (a) PLL_PR Single endid 20 me undefined 7 Ab (a) POT_LPR Single endid 20 me undefined 7 Ab (a) POT_LR Single endid 20 me undefined 7 Ab (a) POT_LR

 | -AB HV2 AI PRD (PAG6 Amplifier 20 mr undefined 2000 a res ? -AB HV3 AI LPLT Amplifier 20 mr undefined 2000 a res ? -AB HV3 AI LPLT Amplifier 20 mr undefined 2000 a res ? -AB HV3 GI AL Amplifier 20 mr undefined 2000 a res ? -AB HV3 GI AL Amplifier 20 mr undefined 2000 a res ? -AB HV3 GI AL Amplifier 20 mr undefined 2000 a res ? -AB HV3 Amplifier 20 mr undefined 2000 a res ? -AB HV1 Amplifier 20 mr undefined 2000 a res ? -AB GI HUL,PR Single mode 20 mr undefined ? -AB GI PUL,AN Single mode 20 mr

 | Am Ind2 (a) (A) <td>B. In02 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ em 7 B. In03 (a)
 PL_UTT Amplifier 2.0 ms undefined 2000 µ em 7 B. In05 (a) AL_UTT Amplifier 2.0 ms undefined 2000 µ em 7 B. In05 (a) AL_UTT Amplifier 2.0 ms undefined 2000 µ em 7 B. In05 (a) AL_UTT Amplifier 2.0 ms undefined 2000 µ em 7 B. In05 (a) AL_UTT Amplifier 2.0 ms undefined 2000 µ em 7 B. In00 (b) (b) PLLL_PR Single ended 2.0 ms undefined 7 B. (a) PULL_PR Single ended 2.0 ms undefined 7 B. (a) PUL_PR Single ended 2.0 ms undefined 7 B. (a)<!--</td--><td>Ind2 Ind2 <thind2< th=""> Ind2 Ind2 <thi< td=""><td></td><td>Jab Ind2 (a) PR_DRAG Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind3 (a) PL_DT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind3 (a) PL_DT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2.0 m undefined 2.0 m undefined 2.0 m undefined 7 Jab Ind6 Ind1 PUL_PR Single ended 2.0 m undefined 7 Jab Jab 7 Jab Jab <t< td=""><td>Image Info2 <td< td=""><td>Tab. MO2 0 PHD PAGE Anglifer 2000 spen Tab. MO2 0 PL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 7 7 Tab. 61 PULL_PR Single moidd 20 ms underlined 7 7 7 7</td><td>Jack MO2 (a) PHC PAGS Anglifer 200 m underned 2000 m reps 7 Jack MO3 IR PLUFT Anglifer 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Find Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Single model 20 m underned 2000 m reps 7 Jack IR PALL_FR Single model 20 m underned 7 7 4 Aug 7 1 Aug 7 1 1 1 1</td><td>Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLUFF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLULPR Single ended 2 0 ms undefined 7 Jack MO3 MULPR Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined<!--</td--><td>Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2 000 (r pm 7) Jack MO3 PALL_FR Single ended 2 0 ms undefined 7 Jack MO1_LPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1</td><td>Tab. MO2 (a) PR(D) PAGE Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUL_PR Single ended 2 0 ms undefined 7 Tab. (a) PULL_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined</td></td></td<></td></t<><td>Jack MO2 01 PRD_RAG Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 PLUTT Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 PLUTT Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG4 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG4 01 Amplifier 20 ms undefined 7 undefined 7 Jack MG4 PULL_PL Single ended 20 ms undefined 7 undefined 7 Jack 9 7 Jack <t< td=""><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B.
 H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUL_AR<!--</td--><td>db Inf2 (a) PR_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) PL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db (a) PLU_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m<</td><td>Image: Index (a) Info2 (a)</td><td>Jack InfO <th< td=""><td></td><td></td><td>Image Info2 <td< td=""><td>Jack MO2 (a) PRQ PAGE Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) PLUTT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack (a) PLUL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PUT_PR Single ended 2 0 ms undefined 7</td><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUT_AR<!--</td--><td>Jack MC2 (a) PR(D) PAGE Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) PLULPL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_AL Single end</td><td>1 102 101 PE, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 106 01 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AM PLL, PR Stripte ended 2.0 ms undefined 7 1 AM IPUL, PR Stripte ended 2.0 ms undefined 7</td><td>B. In02 (a) PE_DRAG Amplifier 20 ms undefined 2000 uppn ? B. In03 (a) PL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APLUTF Amplifier 20 ms undefined ? B. (a) PULL_PL Single ended 20 ms undefined ? B. (a) POT_PL Single ended 20 ms undefined ?
undefined</td></td></td<></td></th<></td></td></t<></td></td></thi<></thind2<></td></td> | B. In02 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ em 7 B. In03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ em 7 B. In05 (a) AL_UTT Amplifier 2.0 ms undefined 2000 µ em 7 B. In05 (a) AL_UTT Amplifier 2.0 ms undefined 2000 µ em 7 B. In05 (a) AL_UTT Amplifier 2.0 ms undefined 2000 µ em 7 B. In05 (a) AL_UTT Amplifier 2.0 ms undefined 2000 µ em 7 B. In00 (b) (b) PLLL_PR Single ended 2.0 ms undefined 7 B. (a) PULL_PR Single ended 2.0 ms undefined 7 B. (a) PUL_PR Single ended 2.0 ms undefined 7 B. (a) </td <td>Ind2 Ind2 <thind2< th=""> Ind2 Ind2 <thi< td=""><td></td><td>Jab Ind2 (a) PR_DRAG Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind3 (a) PL_DT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind3 (a) PL_DT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2.0 m undefined 2.0 m undefined 2.0 m undefined 7 Jab Ind6 Ind1 PUL_PR Single ended 2.0 m undefined 7 Jab Jab 7 Jab Jab <t< td=""><td>Image Info2 <td< td=""><td>Tab. MO2 0 PHD PAGE Anglifer 2000 spen Tab. MO2 0 PL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 7 7 Tab. 61 PULL_PR Single moidd 20 ms underlined 7 7 7 7</td><td>Jack MO2 (a) PHC PAGS Anglifer 200 m underned 2000 m reps 7 Jack MO3 IR PLUFT Anglifer 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Find Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Single model 20 m underned 2000 m reps 7 Jack IR PALL_FR Single model 20 m underned 7 7 4 Aug 7 1 Aug 7 1 1 1 1</td><td>Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLUFF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLULPR Single ended 2 0 ms undefined 7 Jack MO3 MULPR Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined<!--</td--><td>Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2 000 (r pm 7) Jack MO3 PALL_FR Single ended 2 0 ms undefined 7 Jack MO1_LPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1</td><td>Tab. MO2 (a) PR(D) PAGE Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUL_PR Single ended 2 0 ms undefined 7 Tab. (a) PULL_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined</td></td></td<></td></t<><td>Jack MO2 01 PRD_RAG Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 PLUTT Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 PLUTT Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG4 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG4 01 Amplifier 20 ms undefined 7 undefined 7 Jack MG4 PULL_PL Single ended 20 ms undefined 7 undefined 7 Jack 9 7 Jack <t< td=""><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B.
 H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUL_AR<!--</td--><td>db Inf2 (a) PR_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) PL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db (a) PLU_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m<</td><td>Image: Index (a) Info2 (a)</td><td>Jack InfO <th< td=""><td></td><td></td><td>Image Info2 <td< td=""><td>Jack MO2 (a) PRQ PAGE Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) PLUTT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack (a) PLUL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PUT_PR Single ended 2 0 ms undefined 7</td><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUT_AR<!--</td--><td>Jack MC2 (a) PR(D) PAGE Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) PLULPL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_AL Single end</td><td>1 102 101 PE, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 106 01 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AM PLL, PR Stripte ended 2.0 ms undefined 7 1 AM IPUL, PR Stripte ended 2.0 ms undefined 7</td><td>B. In02 (a) PE_DRAG Amplifier 20 ms undefined 2000 uppn ? B. In03 (a) PL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APLUTF Amplifier 20 ms
 undefined ? B. (a) PULL_PL Single ended 20 ms undefined ? B. (a) POT_PL Single ended 20 ms undefined ? undefined</td></td></td<></td></th<></td></td></t<></td></td></thi<></thind2<></td> | Ind2 Ind2 <thind2< th=""> Ind2 Ind2 <thi< td=""><td></td><td>Jab Ind2 (a) PR_DRAG Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind3 (a) PL_DT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind3 (a) PL_DT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2.0 m undefined 2.0 m undefined 2.0 m undefined 7 Jab Ind6 Ind1 PUL_PR Single ended 2.0 m undefined 7 Jab Jab 7 Jab Jab <t< td=""><td>Image Info2 <td< td=""><td>Tab. MO2 0 PHD PAGE Anglifer 2000 spen Tab. MO2 0 PL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 7 7 Tab. 61 PULL_PR Single moidd 20 ms underlined 7 7 7 7</td><td>Jack MO2 (a) PHC PAGS Anglifer 200 m underned 2000 m reps 7 Jack MO3 IR PLUFT Anglifer 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Find Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Single model 20 m underned 2000 m reps 7 Jack IR PALL_FR Single model 20 m underned 7 7 4 Aug 7 1 Aug 7 1 1 1 1</td><td>Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLUFF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLULPR Single ended 2 0 ms undefined 7 Jack MO3 MULPR Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined<!--</td--><td>Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2 000 (r pm 7) Jack MO3 PALL_FR Single ended 2 0 ms undefined 7 Jack MO1_LPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1</td><td>Tab. MO2 (a) PR(D) PAGE Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUL_PR Single
ended 2 0 ms undefined 7 Tab. (a) PULL_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined</td></td></td<></td></t<><td>Jack MO2 01 PRD_RAG Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 PLUTT Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 PLUTT Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG4 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG4 01 Amplifier 20 ms undefined 7 undefined 7 Jack MG4 PULL_PL Single ended 20 ms undefined 7 undefined 7 Jack 9 7 Jack <t< td=""><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUL_AR<!--</td--><td>db Inf2 (a) PR_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) PL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db (a) PLU_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m<</td><td>Image: Index (a) Info2 (a)</td><td>Jack InfO <th< td=""><td></td><td></td><td>Image Info2 <td< td=""><td>Jack MO2 (a) PRQ PAGE Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) PLUTT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack (a) PLUL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PUT_PR Single ended 2 0 ms undefined 7</td><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUT_AR<!--</td--><td>Jack MC2 (a) PR(D) PAGE Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) PLULPL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_AL Single end</td><td>1 102 101 PE, DPAG Amplifier 2.0 me
undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 106 01 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AM PLL, PR Stripte ended 2.0 ms undefined 7 1 AM IPUL, PR Stripte ended 2.0 ms undefined 7</td><td>B. In02 (a) PE_DRAG Amplifier 20 ms undefined 2000 uppn ? B. In03 (a) PL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APLUTF Amplifier 20 ms undefined ? B. (a) PULL_PL Single ended 20 ms undefined ? B. (a) POT_PL Single ended 20 ms undefined ? undefined</td></td></td<></td></th<></td></td></t<></td></td></thi<></thind2<> |

 | Jab Ind2 (a) PR_DRAG Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind3 (a) PL_DT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind3 (a) PL_DT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2000 µ eps 7 Jab Ind5 (a) APLDT Anglifier 2.0 m undefined 2.0 m undefined 2.0 m undefined 2.0 m undefined 7 Jab Ind6 Ind1 PUL_PR Single ended 2.0 m undefined 7 Jab Jab 7 Jab Jab <t< td=""><td>Image Info2 <td< td=""><td>Tab. MO2 0 PHD PAGE Anglifer 2000 spen Tab. MO2 0 PL_UFT
Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 7 7 Tab. 61 PULL_PR Single moidd 20 ms underlined 7 7 7 7</td><td>Jack MO2 (a) PHC PAGS Anglifer 200 m underned 2000 m reps 7 Jack MO3 IR PLUFT Anglifer 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Find Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Single model 20 m underned 2000 m reps 7 Jack IR PALL_FR Single model 20 m underned 7 7 4 Aug 7 1 Aug 7 1 1 1 1</td><td>Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLUFF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLULPR Single ended 2 0 ms undefined 7 Jack MO3 MULPR Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined<!--</td--><td>Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2 000 (r pm 7) Jack MO3 PALL_FR Single ended 2 0 ms undefined 7 Jack MO1_LPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1</td><td>Tab. MO2 (a) PR(D) PAGE Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUL_PR Single ended 2 0 ms undefined 7 Tab. (a) PULL_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined</td></td></td<></td></t<> <td>Jack MO2 01 PRD_RAG Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 PLUTT Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 PLUTT Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG4 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG4 01 Amplifier 20 ms undefined 7 undefined 7 Jack MG4 PULL_PL Single ended 20 ms undefined 7 undefined 7 Jack 9 7 Jack <t< td=""><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUL_AR<!--</td--><td>db Inf2 (a) PR_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) PL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ?
 db (a) PLU_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m<</td><td>Image: Index (a) Info2 (a)</td><td>Jack InfO <th< td=""><td></td><td></td><td>Image Info2 <td< td=""><td>Jack MO2 (a) PRQ PAGE Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) PLUTT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack (a) PLUL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PUT_PR Single ended 2 0 ms undefined 7</td><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUT_AR<!--</td--><td>Jack MC2 (a) PR(D) PAGE Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) PLULPL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_AL Single end</td><td>1 102 101 PE, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 106 01 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AM PLL, PR Stripte ended 2.0 ms undefined 7 1 AM IPUL, PR Stripte ended 2.0 ms undefined 7</td><td>B. In02 (a) PE_DRAG Amplifier 20 ms undefined 2000 uppn ? B. In03 (a) PL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APLUTF Amplifier 20 ms undefined ? B. (a) PULL_PL Single ended 20 ms undefined ? B. (a) POT_PL Single ended 20 ms undefined ? undefined</td></td></td<></td></th<></td></td></t<></td>
 | Image Info2 Info2 <td< td=""><td>Tab. MO2 0 PHD PAGE Anglifer 2000 spen Tab. MO2 0 PL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 7 7 Tab. 61 PULL_PR Single moidd 20 ms underlined 7 7 7 7</td><td>Jack MO2 (a) PHC PAGS Anglifer 200 m underned 2000 m reps 7 Jack MO3 IR PLUFT Anglifer 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Find Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Single model 20 m underned 2000 m reps 7 Jack IR PALL_FR Single model 20 m underned 7 7 4 Aug 7 1 Aug 7 1 1 1 1</td><td>Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLUFF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLULPR Single ended 2 0 ms undefined 7 Jack MO3 MULPR Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined<!--</td--><td>Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2 000 (r pm 7) Jack MO3 PALL_FR Single ended 2 0 ms undefined 7 Jack MO1_LPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined
 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1</td><td>Tab. MO2 (a) PR(D) PAGE Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUL_PR Single ended 2 0 ms undefined 7 Tab. (a) PULL_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined</td></td></td<>

 | Tab. MO2 0 PHD PAGE Anglifer 2000 spen Tab. MO2 0 PL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 2000 spen 7 Tab. MO3 0 AL_UFT Anglifer 20 ms underlined 7 7 Tab. 61 PULL_PR Single moidd 20 ms underlined 7 7 7 7

 | Jack MO2 (a) PHC PAGS Anglifer 200 m underned 2000 m reps 7 Jack MO3 IR PLUFT Anglifer 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Find Aug 20 m underned 2000 m reps 7 Jack MO3 IR Aug Single model 20 m underned 2000 m reps 7 Jack IR PALL_FR Single model 20 m underned 7 7 4 Aug 7 1 Aug 7 1 1 1 1

 | Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLUFF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLULPR Single ended 2 0 ms undefined 7 Jack MO3 MULPR Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined 7 Jack MO3 PO1_LP Single ended 2 0 ms undefined </td <td>Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2 000 (r pm 7) Jack MO3 PALL_FR Single ended 2 0 ms undefined 7 Jack MO1_LPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1</td> <td>Tab. MO2 (a) PR(D) PAGE Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUL_PR Single ended 2 0 ms undefined 7 Tab. (a) PULL_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined</td>

 | Jack MO2 (a) PR(D) PAGE Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) PLUTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2000 (r pm 7) Jack MO3 (a) ALLTF Anglifier 2 0 ms undefined 2 000 (r pm 7) Jack MO3 PALL_FR Single ended 2 0 ms undefined 7 Jack MO1_LPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1_TPR Single ended 2 0 ms undefined 7 Jack MO1

 | Tab. MO2 (a) PR(D) PAGE Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUTF Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) ALTT Anglier 2 0 ms undefined 2000 (r pm 7) Tab. In O3 (a) PLUL_PR Single ended 2 0 ms undefined 7 Tab. (a) PULL_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined

 | Jack MO2 01 PRD_RAG Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 PLUTT Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 PLUTT Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG3 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG4 01 Amplifier 20 ms undefined 2000 µ pm 7 Jack MG4 01 Amplifier 20 ms undefined 7 undefined 7 Jack MG4 PULL_PL Single ended 20 ms undefined 7 undefined 7 Jack 9 7 Jack <t< td=""><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUL_AR<!--</td--><td>db Inf2 (a) PR_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) PL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db (a) PLU_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m<</td><td>Image: Index (a) Info2 (a)</td><td>Jack InfO <th< td=""><td></td><td></td><td>Image Info2 <td< td=""><td>Jack MO2 (a) PRQ PAGE Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) PLUTT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack (a) PLUL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PUT_PR Single ended 2 0 ms undefined 7</td><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms
 undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUT_AR<!--</td--><td>Jack MC2 (a) PR(D) PAGE Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) PLULPL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_AL Single end</td><td>1 102 101 PE, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 106 01 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AM PLL, PR Stripte ended 2.0 ms undefined 7 1 AM IPUL, PR Stripte ended 2.0 ms undefined 7</td><td>B. In02 (a) PE_DRAG Amplifier 20 ms undefined 2000 uppn ? B. In03 (a) PL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APLUTF Amplifier 20 ms undefined ? B. (a) PULL_PL Single ended 20 ms undefined ? B. (a) POT_PL Single ended 20 ms undefined ? undefined</td></td></td<></td></th<></td></td></t<>
 | B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<
 | B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms
 undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUL_AR </td <td>db Inf2 (a) PR_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) PL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db (a) PLU_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m<</td> <td>Image: Index (a) Info2 (a)</td> <td>Jack InfO <th< td=""><td></td><td></td><td>Image Info2 <td< td=""><td>Jack MO2 (a) PRQ PAGE Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) PLUTT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack (a) PLUL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PUT_PR Single ended 2 0 ms undefined 7</td><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUT_AR<!--</td--><td>Jack MC2 (a) PR(D) PAGE Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) PLULPL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_AL Single end</td><td>1 102 101 PE, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 106 01 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AM PLL, PR Stripte ended 2.0 ms undefined 7 1 AM IPUL, PR Stripte ended 2.0 ms undefined 7</td><td>B. In02 (a) PE_DRAG Amplifier 20 ms undefined 2000 uppn ? B. In03 (a) PL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APLUTF Amplifier 20 ms undefined ? B. (a) PULL_PL Single ended 20 ms undefined ? B. (a) POT_PL Single ended 20 ms undefined ? undefined</td></td></td<></td></th<></td>
 | db Inf2 (a) PR_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf2 (a) PL_DBAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) PL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db Inf5 (a) AL_DDAG Anglifer 20 m underlined 2000 pres ? db (a) PLU_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m underlined ? db (a) PUL_PR Single ended 20 m<
 | Image: Index (a) Info2 (a)

 | Jack InfO InfO <th< td=""><td></td><td></td><td>Image Info2 <td< td=""><td>Jack MO2 (a) PRQ PAGE Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) PLUTT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack (a) PLUL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PUT_PR Single ended 2 0 ms undefined 7</td><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUT_AR<!--</td--><td>Jack MC2 (a) PR(D) PAGE Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) PLULPL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_AL Single end</td><td>1 102 101 PE, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 106 01 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AM PLL, PR Stripte ended 2.0 ms undefined 7 1 AM IPUL, PR Stripte ended 2.0 ms undefined 7</td><td>B. In02 (a) PE_DRAG Amplifier 20 ms undefined 2000 uppn ? B. In03 (a) PL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APLUTF Amplifier 20 ms undefined ? B. (a) PULL_PL Single ended 20 ms undefined ? B. (a) POT_PL Single ended 20 ms undefined ? undefined</td></td></td<></td></th<>
 |
 |
 | Image Info2 Info2 <td< td=""><td>Jack MO2 (a) PRQ PAGE Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) PLUTT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack (a) PLUL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PUT_PR Single ended 2 0 ms undefined 7</td><td>B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined<</td><td>B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUT_AR<!--</td--><td>Jack MC2 (a) PR(D) PAGE Amplifier 20 ms undefined
 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) PLULPL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_AL Single end</td><td>1 102 101 PE, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 106 01 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AM PLL, PR Stripte ended 2.0 ms undefined 7 1 AM IPUL, PR Stripte ended 2.0 ms undefined 7</td><td>B. In02 (a) PE_DRAG Amplifier 20 ms undefined 2000 uppn ? B. In03 (a) PL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APLUTF Amplifier 20 ms undefined ? B. (a) PULL_PL Single ended 20 ms undefined ? B. (a) POT_PL Single ended 20 ms undefined ? undefined</td></td></td<> | Jack MO2 (a) PRQ PAGE Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) PLUTT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack In63 (a) ALL/TT Amplifier 2 0 ms undefined 2000 (r pm 7) Jack (a) PLUL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PULL_PR Single ended 2 0 ms undefined 7 Jack (a) PUT_PR Single ended 2 0 ms undefined 7
 | B. H02 (a) PE_UPAG Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H03 (a) PL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H05 (a) APL_UTT Amplifier 2.0 ms undefined 2000 (r pm 7) B. H04 (a) APL_UTS Amplifier 2.0 ms undefined 2000 (r pm 7) B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined 7 B. (a) PULL_PE Single ended 2.0 ms undefined< | B Ind2 (a) PR_DRAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind3 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) PL_UTT Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Amplifier 2.0 ms undefined 2000 µ cm 7 B Ind5 (a) APL_DTAG Single ended 2.0 ms undefined 7 B (a) PULL_PR Single ended 2.0 ms undefined 7 B (a) PUL_AR Single ended 2.0 ms undefined 7 B (a) PUT_AR </td <td>Jack MC2 (a) PR(D) PAGE Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) PLULPL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_AL Single end</td> <td>1 102 101 PE, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 106 01 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier
 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AM PLL, PR Stripte ended 2.0 ms undefined 7 1 AM IPUL, PR Stripte ended 2.0 ms undefined 7</td> <td>B. In02 (a) PE_DRAG Amplifier 20 ms undefined 2000 uppn ? B. In03 (a) PL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APLUTF Amplifier 20 ms undefined ? B. (a) PULL_PL Single ended 20 ms undefined ? B. (a) POT_PL Single ended 20 ms undefined ? undefined</td> | Jack MC2 (a) PR(D) PAGE Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) PL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack In 63 (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) AL_UTF Amplifier 20 ms undefined 2000 area 7 Jack (a) PLULPL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 Jack (a) PULL_AL Single end | 1 102 101 PE, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 104 101 PL, DPAG Amplifier 2.0 me undefined 2000 µ exp 7 1 106 01 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AL, DPAG Amplifier 2.0 ms undefined 2000 µ exp 7 1 AM PLL, PR Stripte ended 2.0 ms undefined 7 1 AM IPUL, PR Stripte ended 2.0 ms undefined 7 | B. In02 (a) PE_DRAG Amplifier 20 ms undefined 2000 uppn ? B. In03 (a) PL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APL_UTF Amplifier 20 ms undefined 2000 uppn ? B. In05 (a) APLUTF Amplifier 20 ms undefined ? B. (a) PULL_PL Single ended 20 ms undefined ? B. (a) POT_PL Single ended 20 ms undefined ? undefined
 |
| Ab h03 (a) PL_LPT Amplifie 20 me undefined 2000 µ sep 7 Ab h03 (a) PL_LPT Amplifie 20 me undefined 2000 µ sep 7 Ab h03 (a) AD_LPT Amplifie 20 me undefined 2000 µ sep 7 Ab h03 (a) AD_LPT Amplifie 20 me undefined 2000 µ sep 7 Ab h03 (a) AD_LPT Amplifie 20 me undefined 2000 µ sep 7 Ab field AU_LPT Amplifie 20 me undefined 2000 µ sep 7 Ab field AU_LPT Single ended 20 me undefined 7 Ab (a) PUL_PT Single ended 20 me undefined 7 Ab (a) PUL_PT Single ended 20 me undefined 7 Ab (a) PUL_PT Single ended 20 me undefined 7 Ab (a) PUT_PT Single ended 20 me </td <td>-Ab Ind3 (a) PL_DFT Amplifier 2.0 m undefined 2000 a res 7 -Ab Ind3 (a) PL_DFT Amplifier 2.0 m undefined 2000 a res 7 -Ab Ind3 (a) PL_DFT Amplifier 2.0 m undefined 2000 a res 7 -Ab Ind3 (a) PL_DFT Amplifier 2.0 m undefined 2000 a res 7 -Ab Ind3 (a) PL_DFT Amplifier 2.0 m undefined 2000 pre 7 -Ab (a) PLL_FT Single model 2.0 m undefined 2000 pre 7 -Ab (a) PULL_FN Single model 2.0 m undefined 7 -Ab (a) PULL_FN Single model 2.0 m undefined 7 -Ab (a) POT_FN Single model 2.0 m undefined 7 -Ab (a) POT_FN Single model 2.0 m</td> <td>Ab Ind3 (a) PL_[FF] Anglifier 2.0 ms underlined 2000 s cm 7 Ab Ind5 (a) PL_[DNAG Anglifier 2.0 ms underlined 2000 s cm 7 Ab Ind5 (a) APL_[FF] Anglifier 2.0 ms underlined 2000 s cm 7 Ab Ind5 (a) APL_[FF] Anglifier 2.0 ms underlined 2000 s cm 7 Ab Ind5 (a) APL_[FF] Anglifier 2.0 ms underlined 2000 s cm 7 Ab (a) APL_[FF] Single ended 2.0 ms underlined 2000 p cm 7 Ab (a) PULL_FR Single ended 2.0 ms underlined 7 Ab (a) PUL_FR Single ended 2.0 ms underlined 7 Ab (a) POT_FR Single ended 2.0 ms underlined 7 Ab (a) POT_FR Single ended</td> <td>The field of the second seco</td> <td>Image Profile <thprofile< th=""> <thprofile< th=""> <thpro< td=""><td></td><td>-db. Ir03 (a) PL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir05 (a) PL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir05 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir05 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir06 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir08 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir08 (a) PUL_PR Stript ended 2.0 ms undefined 7 -db. (a) PUL_PR Stript ended 2.0 ms undefined 7 -db. (a) POT_PR Stript ended 2.0 ms undefined 7 -db. (a) POT_PR<!--</td--><td>Image Introl Int Applie 2.0 ms undefined 2000 proci 7 Image PL_DIMG Anglier 2.0 ms undefined 2000 proci 7 Image PL_DIMG Anglier 2.0 ms undefined 2000 proci 7 Image Anglier 2.0 ms undefined 200 proci 7 Image Anglier 2.0 ms undefined 7 7 <tr< td=""><td>Tab. In-03 Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 200 µ sep 7 Tab. In-01 IN PLL_PR Single endd 20 ms undefined 7 Tab. Gi PULL_PR Single endd 20 ms undefined 7 Tab. Gi PULL_AL Single endd 20 ms undefined 7 Tab. Gi PUL_AL</td><td>Image Ind3 Iai PL_UFT Anglifier 20 ms undefined 2000 µ sop 7 Image IP_DIAG Anglifier 20 ms undefined 2000 µ sop 7 Image IP_DIAG Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 200 µ sop 7 Image Image Image Single ended 20 ms undefined 70 Image Image Image Single ended 20 ms undefined 7 Image Image Image Single ended 20 ms undefined 7 Image Image Single ended</td><td>Tab. In-03 Iai PL_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G PL_DTAG Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. G PULL_PR Single endd 20 ms undefined 7 Tab. G PULL_PR Single endd 20 ms undefined 7 Tab. G PULL_AR Single endd 20 ms undefined 7 Tab. G PULL_AR Single endd 20</td><td>Tab. In-03 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai AL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-07 Iai AL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. Iai PULL_PR Single ended 20 ms undefined 200 µ eps 7 Tab. Iai PULL_PR Single ended 20 ms undefined 7 Tab. Iai PULL_PR Single ended 20 ms undefined 7 Tab. Iai PULL_AR Single ended 20 ms undefined 7 Tab. Iai PULL_AR Single ended <td< td=""><td>Tab. In-03
 In-03 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-05 In-04 In-04 In-06 In-04 In-06 In-06</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_PR Single ended 20 ms undefined 70 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefin</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jam In O3 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O4 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUTPK Single ended 2.0 m underlend 7 Jam (a) PULL_PR Single ended 2.0 m underlend 7 Jam (a) PUL_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m</td></td<><td>Ind (a) (b) (a) (b) (c) (c)<td>Image Ind3 Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic PL PL Single ended 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 7 7 7 7 7 7 7 7 7 7 7 <td< td=""><td>-Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PLL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended</td><td>Image Image Part Amelier 2.0 ms undefined 2000 profi 200</td><td>Ind: (a) (b) (a) (b) (c) (c)< (c) (c)< (c)<</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_FR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefine</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) AL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) AL_UFRAG Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined 7 Jack (a) POT_PR
 Single endd 20 ms undefined</td><td>Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Main Phylicity 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended<td>Base Ind3 (a) PL_UFT Anglier 20 ms und4med 2000 µ sep 7 Base Ind5 (a) PL_UFT Anglier 20 ms undefmed 2000 µ sep 7 Base Ind5 (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 200 µ sep 7 Base (a) PULL_PR Single ended 20 ms undefmed 70 7 Base (a) PULL_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7</td></td></td<></td></td></td></tr<></td></td></thpro<></thprofile<></thprofile<></td> | -Ab Ind3 (a) PL_DFT Amplifier 2.0 m undefined 2000 a res 7 -Ab Ind3 (a) PL_DFT Amplifier 2.0 m undefined 2000 a res 7 -Ab Ind3 (a) PL_DFT Amplifier 2.0 m undefined 2000 a res 7 -Ab Ind3 (a) PL_DFT Amplifier 2.0 m undefined 2000 a res 7 -Ab Ind3 (a) PL_DFT Amplifier 2.0 m undefined 2000 pre 7 -Ab (a) PLL_FT Single model 2.0 m undefined 2000 pre 7 -Ab (a) PULL_FN Single model 2.0 m undefined 7 -Ab (a) PULL_FN Single model 2.0 m undefined 7 -Ab (a) POT_FN Single model 2.0 m undefined 7 -Ab (a) POT_FN Single model 2.0 m

 | Ab Ind3 (a) PL_[FF] Anglifier 2.0 ms underlined 2000 s cm 7 Ab Ind5 (a) PL_[DNAG Anglifier 2.0 ms underlined 2000 s cm 7 Ab Ind5 (a) APL_[FF] Anglifier 2.0 ms underlined 2000 s cm 7 Ab Ind5 (a) APL_[FF] Anglifier 2.0 ms underlined 2000 s cm 7 Ab Ind5 (a) APL_[FF] Anglifier 2.0 ms underlined 2000 s cm 7 Ab (a) APL_[FF] Single ended 2.0 ms underlined 2000 p cm 7 Ab (a) PULL_FR Single ended 2.0 ms underlined 7 Ab (a) PUL_FR Single ended 2.0 ms underlined 7 Ab (a) POT_FR Single ended 2.0 ms underlined 7 Ab (a) POT_FR Single ended

 | The field of the second seco

 | Image Profile Profile <thprofile< th=""> <thprofile< th=""> <thpro< td=""><td></td><td>-db. Ir03 (a) PL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir05 (a) PL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir05 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir05 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir06 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir08 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir08 (a) PUL_PR Stript ended 2.0 ms undefined 7 -db. (a) PUL_PR Stript ended 2.0 ms undefined 7 -db. (a) POT_PR Stript ended 2.0 ms undefined 7 -db. (a) POT_PR<!--</td--><td>Image Introl Int Applie 2.0 ms undefined 2000 proci 7 Image PL_DIMG Anglier 2.0 ms undefined 2000 proci 7 Image PL_DIMG Anglier 2.0 ms undefined 2000 proci 7 Image Anglier 2.0 ms undefined 200 proci 7 Image Anglier 2.0 ms undefined 7 7 <tr< td=""><td>Tab. In-03 Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 200 µ sep 7 Tab. In-01 IN PLL_PR Single endd 20 ms undefined 7 Tab. Gi PULL_PR Single endd 20 ms undefined 7 Tab. Gi PULL_AL Single endd 20 ms undefined 7 Tab. Gi PUL_AL</td><td>Image Ind3 Iai PL_UFT Anglifier 20 ms undefined 2000 µ sop 7 Image IP_DIAG Anglifier 20 ms undefined 2000 µ sop 7 Image IP_DIAG Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 200 µ sop 7 Image Image
 Image Single ended 20 ms undefined 70 Image Image Image Single ended 20 ms undefined 7 Image Image Image Single ended 20 ms undefined 7 Image Image Single ended</td><td>Tab. In-03 Iai PL_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G PL_DTAG Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. G PULL_PR Single endd 20 ms undefined 7 Tab. G PULL_PR Single endd 20 ms undefined 7 Tab. G PULL_AR Single endd 20 ms undefined 7 Tab. G PULL_AR Single endd 20</td><td>Tab. In-03 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai AL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-07 Iai AL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. Iai PULL_PR Single ended 20 ms undefined 200 µ eps 7 Tab. Iai PULL_PR Single ended 20 ms undefined 7 Tab. Iai PULL_PR Single ended 20 ms undefined 7 Tab. Iai PULL_AR Single ended 20 ms undefined 7 Tab. Iai PULL_AR Single ended <td< td=""><td>Tab. In-03 In-03 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-05 In-04 In-04 In-06 In-04 In-06 In-06</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_PR Single ended 20 ms undefined 70 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefin</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jam In O3 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O4 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUTPK Single ended 2.0 m underlend 7 Jam (a) PULL_PR Single ended 2.0 m underlend 7 Jam (a) PUL_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m</td></td<><td>Ind (a) (b) (a) (b) (c) (c)<td>Image Ind3 Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic PL PL Single ended 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 7 7 7 7 7 7 7 7 7 7 7 <td< td=""><td>-Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PLL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended</td><td>Image Image Part Amelier 2.0 ms undefined 2000 profi 200</td><td>Ind: (a) (b) (a) (b) (c) (c)< (c) (c)< (c)<</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_FR Single ended 20 ms undefined
7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefine</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) AL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) AL_UFRAG Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined</td><td>Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Main Phylicity 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended<td>Base Ind3 (a) PL_UFT Anglier 20 ms und4med 2000 µ sep 7 Base Ind5 (a) PL_UFT Anglier 20 ms undefmed 2000 µ sep 7 Base Ind5 (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 200 µ sep 7 Base (a) PULL_PR Single ended 20 ms undefmed 70 7 Base (a) PULL_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7</td></td></td<></td></td></td></tr<></td></td></thpro<></thprofile<></thprofile<> |

 | -db. Ir03 (a) PL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir05 (a) PL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir05 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir05 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir06 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir08 (a) AL_UPT Amplifie 2.0 ms undefined 2000 y-sps 7 -db. Ir08 (a) PUL_PR Stript ended 2.0 ms undefined 7 -db. (a) PUL_PR Stript ended 2.0 ms undefined 7 -db. (a) POT_PR Stript ended 2.0 ms undefined 7 -db. (a) POT_PR </td <td>Image Introl Int Applie 2.0 ms undefined 2000 proci 7 Image PL_DIMG Anglier 2.0 ms undefined 2000 proci 7 Image PL_DIMG Anglier 2.0 ms undefined 2000 proci 7 Image Anglier 2.0 ms undefined 200 proci 7 Image Anglier 2.0 ms undefined 7 7 <tr< td=""><td>Tab. In-03 Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 200 µ sep 7 Tab. In-01 IN PLL_PR Single endd 20 ms undefined 7 Tab. Gi PULL_PR Single endd 20 ms undefined 7 Tab. Gi PULL_AL Single endd 20 ms undefined 7 Tab. Gi PUL_AL</td><td>Image Ind3 Iai PL_UFT Anglifier 20 ms undefined 2000 µ sop 7 Image IP_DIAG Anglifier 20 ms undefined 2000 µ sop 7 Image IP_DIAG Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 200 µ sop 7 Image Image Image Single ended 20 ms undefined 70 Image Image Image Single ended 20 ms undefined 7 Image Image Image Single ended 20 ms undefined 7 Image Image Single ended</td><td>Tab. In-03 Iai PL_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G PL_DTAG Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. G PULL_PR Single endd 20 ms undefined 7 Tab. G PULL_PR Single endd 20 ms undefined 7 Tab. G PULL_AR Single endd 20 ms undefined 7 Tab. G PULL_AR Single endd 20</td><td>Tab. In-03 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai AL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-07 Iai AL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. Iai PULL_PR Single ended 20 ms undefined 200 µ eps 7 Tab. Iai PULL_PR Single ended 20 ms undefined 7 Tab. Iai PULL_PR Single ended 20 ms undefined 7 Tab. Iai PULL_AR Single ended 20 ms undefined 7 Tab. Iai PULL_AR Single ended <td< td=""><td>Tab. In-03 In-03 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-05 In-04 In-04 In-06 In-04 In-06 In-06</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_PR Single ended 20 ms undefined 70 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefin</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior
 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jam In O3 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O4 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUTPK Single ended 2.0 m underlend 7 Jam (a) PULL_PR Single ended 2.0 m underlend 7 Jam (a) PUL_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m</td></td<><td>Ind (a) (b) (a) (b) (c) (c)<td>Image Ind3 Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic PL PL Single ended 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 7 7 7 7 7 7 7 7 7 7 7 <td< td=""><td>-Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PLL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended</td><td>Image Image Part Amelier 2.0 ms undefined 2000 profi 200</td><td>Ind: (a) (b) (a) (b) (c) (c)< (c) (c)< (c)<</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_FR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefine</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) AL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) AL_UFRAG Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined</td><td>Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Main Phylicity 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended<td>Base Ind3 (a) PL_UFT Anglier 20 ms und4med 2000 µ sep 7 Base Ind5 (a) PL_UFT Anglier 20 ms undefmed 2000 µ sep 7 Base Ind5 (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 200 µ sep 7 Base (a) PULL_PR Single ended 20 ms undefmed 70 7 Base
 (a) PULL_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7</td></td></td<></td></td></td></tr<></td> | Image Introl Int Applie 2.0 ms undefined 2000 proci 7 Image PL_DIMG Anglier 2.0 ms undefined 2000 proci 7 Image PL_DIMG Anglier 2.0 ms undefined 2000 proci 7 Image Anglier 2.0 ms undefined 200 proci 7 Image Anglier 2.0 ms undefined 7 7 <tr< td=""><td>Tab. In-03 Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 200 µ sep 7 Tab. In-01 IN PLL_PR Single endd 20 ms undefined 7 Tab. Gi PULL_PR Single endd 20 ms undefined 7 Tab. Gi PULL_AL Single endd 20 ms undefined 7 Tab. Gi PUL_AL</td><td>Image Ind3 Iai PL_UFT Anglifier
 20 ms undefined 2000 µ sop 7 Image IP_DIAG Anglifier 20 ms undefined 2000 µ sop 7 Image IP_DIAG Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 200 µ sop 7 Image Image Image Single ended 20 ms undefined 70 Image Image Image Single ended 20 ms undefined 7 Image Image Image Single ended 20 ms undefined 7 Image Image Single ended</td><td>Tab. In-03 Iai PL_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G PL_DTAG Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. G PULL_PR Single endd 20 ms undefined 7 Tab. G PULL_PR Single endd 20 ms undefined 7 Tab. G PULL_AR Single endd 20 ms undefined 7 Tab. G PULL_AR Single endd 20</td><td>Tab. In-03 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai AL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-07 Iai AL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. Iai PULL_PR Single ended 20 ms undefined 200 µ eps 7 Tab. Iai PULL_PR Single ended 20 ms undefined 7 Tab. Iai PULL_PR Single ended 20 ms undefined 7 Tab. Iai PULL_AR Single ended 20 ms undefined 7 Tab. Iai PULL_AR Single ended <td< td=""><td>Tab. In-03 In-03 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-05 In-04 In-04 In-06 In-04 In-06 In-06</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_PR Single ended 20 ms undefined 70 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefin</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jam In O3 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O4 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUTPK Single ended 2.0 m underlend 7 Jam (a) PULL_PR Single ended 2.0 m underlend 7 Jam (a) PUL_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m</td></td<><td>Ind (a) (b) (a) (b) (c) (c)<td>Image Ind3 Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic PL PL Single ended 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 7 7 7 7 7 7 7 7 7 7 7 <td< td=""><td>-Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PLL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended</td><td>Image Image Part Amelier 2.0 ms undefined 2000 profi 200</td><td>Ind: (a) (b) (a) (b) (c) (c)< (c) (c)<
(c)<</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_FR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefine</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) AL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) AL_UFRAG Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined</td><td>Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Main Phylicity 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended<td>Base Ind3 (a) PL_UFT Anglier 20 ms und4med 2000 µ sep 7 Base Ind5 (a) PL_UFT Anglier 20 ms undefmed 2000 µ sep 7 Base Ind5 (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 200 µ sep 7 Base (a) PULL_PR Single ended 20 ms undefmed 70 7 Base (a) PULL_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7</td></td></td<></td></td></td></tr<>
 | Tab. In-03 Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Tab. In-05 Gi AL_UFT Amplifier 20 ms undefined 200 µ sep 7 Tab. In-01 IN PLL_PR Single endd 20 ms undefined 7 Tab. Gi PULL_PR Single endd 20 ms undefined 7 Tab. Gi PULL_AL Single endd 20 ms undefined 7 Tab. Gi PUL_AL

 | Image Ind3 Iai PL_UFT Anglifier 20 ms undefined 2000 µ sop 7 Image IP_DIAG Anglifier 20 ms undefined 2000 µ sop 7 Image IP_DIAG Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 2000 µ sop 7 Image Image Anglifier 20 ms undefined 200 µ sop 7 Image Image Image Single ended 20 ms undefined 70 Image Image Image Single ended 20 ms undefined 7 Image Image Image Single ended 20 ms undefined 7 Image Image Single ended

 | Tab. In-03 Iai PL_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G PL_DTAG Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT
Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. In-05 G AR_UFT Anglier 20 ms undefined 2000 p eps 7 Tab. G PULL_PR Single endd 20 ms undefined 7 Tab. G PULL_PR Single endd 20 ms undefined 7 Tab. G PULL_AR Single endd 20 ms undefined 7 Tab. G PULL_AR Single endd 20

 | Tab. In-03 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai PL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-05 Iai AL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. In-07 Iai AL_UFT Anglier 20 ms undefined 2000 µ eps 7 Tab. Iai PULL_PR Single ended 20 ms undefined 200 µ eps 7 Tab. Iai PULL_PR Single ended 20 ms undefined 7 Tab. Iai PULL_PR Single ended 20 ms undefined 7 Tab. Iai PULL_AR Single ended 20 ms undefined 7 Tab. Iai PULL_AR Single ended <td< td=""><td>Tab. In-03 In-03 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-05 In-04 In-04 In-06 In-04 In-06 In-06</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_PR Single ended 20 ms undefined 70 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefin</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jam In O3 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O4 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUTPK Single ended 2.0 m underlend 7 Jam (a) PULL_PR Single ended 2.0 m underlend 7 Jam (a) PUL_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m</td></td<> <td>Ind (a) (b) (a) (b) (c) (c)<td>Image Ind3 Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic PL PL Single ended 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 7 7 7 7 7 7 7 7 7 7 7 <td< td=""><td>-Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PLL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended</td><td>Image Image Part Amelier 2.0 ms undefined 2000 profi 200</td><td>Ind: (a) (b) (a) (b) (c) (c)< (c) (c)< (c)<</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_FR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefine</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms
 undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) AL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) AL_UFRAG Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined</td><td>Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Main Phylicity 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended<td>Base Ind3 (a) PL_UFT Anglier 20 ms und4med 2000 µ sep 7 Base Ind5 (a) PL_UFT Anglier 20 ms undefmed 2000 µ sep 7 Base Ind5 (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 200 µ sep 7 Base (a) PULL_PR Single ended 20 ms undefmed 70 7 Base (a) PULL_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7</td></td></td<></td></td> | Tab. In-03 In-03 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-03 In-04 In-05 In-04 In-04 In-06 In-04 In-06

 | Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_PR Single ended 20 ms undefined 70 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_PR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefin

 | Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende

 |

 | Jam In O3 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O4 (a) PL_UFT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In O5 (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUT Anglifier 2.0 m underlend 2000 grosp 7 Jam In OF (a) APLUTPK Single ended 2.0 m underlend 7 Jam (a) PULL_PR Single ended 2.0 m underlend 7 Jam (a) PUL_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m underlend 7 Jam (a) POT_PR Single ended 2.0 m
 | Ind (a) (b) (a) (b) (c) (c) <td>Image Ind3 Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic PL PL Single ended 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 7 7 7 7 7 7 7 7 7 7 7 <td< td=""><td>-Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier
 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PLL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended</td><td>Image Image Part Amelier 2.0 ms undefined 2000 profi 200</td><td>Ind: (a) (b) (a) (b) (c) (c)< (c) (c)< (c)<</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_FR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefine</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) AL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) AL_UFRAG Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined</td><td>Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Main Phylicity 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended<td>Base Ind3 (a) PL_UFT Anglier 20 ms und4med 2000 µ sep 7 Base Ind5 (a) PL_UFT Anglier 20 ms undefmed 2000 µ sep 7 Base Ind5 (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 200 µ sep 7 Base (a) PULL_PR Single ended 20 ms undefmed 70 7 Base (a) PULL_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7</td></td></td<></td> | Image Ind3
Iai PL_UFT Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image PL_DIAG Amplifier 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic PL PL Single ended 20 ms undefined 2000 µ sep 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 Image Feldbaun-Incutic Feldbaun-Incutic 20 ms undefined 7 7 7 7 7 7 7 7 7 7 7 7 7 <td< td=""><td>-Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PLL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended</td><td>Image Image Part Amelier 2.0 ms undefined 2000 profi 200</td><td>Ind: (a) (b) (a) (b) (c) (c)< (c) (c)< (c)<</td><td>Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_FR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefine</td><td>Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende</td><td></td><td>Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) AL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) AL_UFRAG Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined</td><td>Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Main Phylicity 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended<td>Base Ind3 (a) PL_UFT Anglier 20 ms und4med 2000 µ sep 7 Base Ind5 (a) PL_UFT Anglier 20 ms undefmed 2000 µ sep 7 Base Ind5 (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 200 µ sep 7 Base (a) PULL_PR Single ended 20 ms undefmed 70 7 Base (a) PULL_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7</td></td></td<>
 | -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PL_LIPT Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) AL_UPTA Amplifier 2.0 ms undefined 2000 p.cm; 7 -Ab In/30 (a) PLL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_LPR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended 2.0 ms undefined 7 -Ab (a) PUL_PR Strips ended
 | Image Image Part Amelier 2.0 ms undefined 2000 profi 200
 | Ind: (a) (b) (a) (b) (c) (c)< (c) (c)< (c)<
 | Jack In O3 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O4 [a] PL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O5 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack In O7 [a] AL_UFT Anglier 20 ms undefined 2000 grop 7 Jack [a] PULL_FR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefined 7 Jack [a] PULL_AR Single ended 20 ms undefine | Jac InO3 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) PL_UFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a)
 APLUFT Anglier 20 ms undefined 2000 prior 7 Jac InO5 (a) APLUFT Anglier 20 ms undefined 2000 prior 7 Jac 100 (a) PULL_PR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULL_AR Single ended 20 ms undefined 7 -db 10 PULAR Single ende | | Jack In 603 (a)
 PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) PL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack In 603 (a) AL_UFT Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) AL_UFRAG Amplifier 20 ms undefined 2000 µ cm 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) PULL_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single ended 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined 7 Jack (a) POT_PR Single endd 20 ms undefined | Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Main Phylicity 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 2000 prop 7 Import Amplifier 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended 2.0 mm undefined 7 7 Import Stripte ended <td>Base Ind3 (a) PL_UFT Anglier 20 ms und4med 2000 µ sep 7 Base Ind5 (a) PL_UFT Anglier 20 ms undefmed 2000 µ sep 7 Base Ind5 (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 200 µ sep 7 Base (a) PULL_PR Single ended 20 ms undefmed 70 7 Base (a) PULL_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7</td> | Base Ind3 (a) PL_UFT Anglier 20 ms und4med 2000 µ sep 7 Base Ind5 (a) PL_UFT Anglier 20 ms undefmed 2000 µ sep 7 Base Ind5 (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 2000 µ sep 7 Base (a) APLUT Anglier 20 ms undefmed 200 µ sep 7 Base (a) PULL_PR Single ended 20 ms undefmed 70 7 Base (a) PULL_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7 Base (a) POT_PR Single ended 20 ms undefmed 7 |
| Ab InOA (a) AD_LPT Anglier 20 ms underled 2000 yeps 7 Ab InOA (a) AD_LPT Anglier 20 ms underled 2000 yeps 7 Ab InOA (a) AD_LPT Anglier 20 ms underled 2000 yeps 7 Ab InOA (a) AL_LPT Anglier 20 ms underled 2000 yeps 7 Ab (a) PAL Fallow Anglier 20 ms underled 2000 yeps 7 Ab (a) PUL_PR Single ended 20 ms underled 7 Ab (a) PUL_PR Single ended 20 ms underled 7 Ab (a) PUL_PR Single ended 20 ms underled 7 Ab (a) PUL_PR Single ended 20 ms underled 7 Ab (a) PUL_PR Single ended 20 ms underled 7 <

 | -db. hrólk (a) PL_DTAG Angelfer 2.0 m undefered 2000 a ges 7 -db. hrólk (a) AL_DTFT Angelfer 2.0 m undefered 2000 a ges 7 -db. hrólk (a) AL_DTFT Angelfer 2.0 m undefered 2000 a ges 7 -db. (a) AL_DTFT Angelfer 2.0 m undefered 2000 a ges 7 -db. (a) AL_DTA Angelfer 2.0 m undefered 2000 a ges 7 -db. (a) PLULPT Angelfer 2.0 m undefered 7 -db. (a) PLULPT Single mode 2.0 m undefered 7 -db. (a) PULLAT Single mode 2.0 m undefered 7 -db. (a) PULAT Single mode 2.0 m undefered 7 -db. (a) PULAT Single mode 2.0 m undefered 7

 | Bit Not (a) PL_DPAG Amplifier 2.0 ms undefined 2000 (s esc) 7 Bit Not5 (a) APL_LFT Amplifier 2.0 ms
undefined 2000 (s esc) 7 Bit Not5 (a) APL_LFT Amplifier 2.0 ms undefined 2000 (s esc) 7 Bit Not5 (a) APL_LFT Amplifier 2.0 ms undefined 2000 (s esc) 7 Bit Not5 (a) APL_LFT Amplifier 2.0 ms undefined 2000 (s esc) 7 Bit Not5 (a) APLLFT Amplifier 2.0 ms undefined 2000 (s esc) 7 Bit Not5 (a) PULL_FR Single ended 2.0 ms undefined 7 Bit (a) PULL_FR Single ended 2.0 ms undefined 7 Bit (a) PUT_AL Single ended 2.0 ms undefined 7 Bit (b) PUT_AR Single ended 2.0 ms undefined 7

 | Jack Ind4 (a) PL_DTAG Amplifier 2.0 ms undefined 2000 (p. pp. 7) Jack Ind5 (a) APL/DTAG Amplifier 2.0 ms undefined 2000 (p. pp. 7) Jack Ind5 (a) APL/DTAG Amplifier 2.0 ms undefined 2000 (p. pp. 7) Jack Ind5 (a) APL/DTAG Amplifier 2.0 ms undefined 2000 (p. pp. 7) Jack Ind5 (a) APL/DTAG Amplifier 2.0 ms undefined 2000 (p. pp. 7) Jack Ind5 (a) APL/DTAG Amplifier 2.0 ms undefined 2000 (p. pp. 7) Jack (a) PULL_PR Single ended 2.0 ms undefined 7 Jack (a) POT_PR Single ended 2.0 ms undefined 7 Jack (a) POT_PR Single ended 2.0 ms undefined 7 Jack (a) POT_PR Single ended 2.0 ms undefined

 | Lab. InOA (a) ALL_PR Anglier 2.0 ms undefined 2000 prior 7 Lab. InOA (a) ALL_PT Anglier 2.0 ms undefined 2000 prior 7 Lab. InOA (a) ALL_PT Anglier 2.0 ms undefined 2000 prior 7 Lab. InOA (a) ALL_PT Anglier 2.0 ms undefined 2000 prior 7 Lab. InOA (a) ALL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PUL_PR Single ended 2.0 ms undefined 7 Lab. (a) PUT_PR Single ended 2.0 ms undefined 7 Lab. (a) PUT_PR Single ended 2.0 ms undefined <

 | Lab. InO4 (a) PL_DPAG Amplifier 2.0 ms undefined 2000 pco1 7 Lab. InO5 (a) APLDPT Amplifier 2.0 ms undefined 2000 pco1 7 Lab. InO5 (a) APLDPT Amplifier 2.0 ms undefined 2000 pco1 7 Lab. InO5 (a) APLDPT Amplifier 2.0 ms undefined 2000 pco1 7 Lab. InO5 (a) APLDPT Amplifier 2.0 ms undefined 2.000 pco1 7 Lab. (a) PULL PR Stripte ended 2.0 ms undefined 7 Lab. (a) PULL PR Stripte ended 2.0 ms undefined 7 Lab. (a) PULL PR Stripte ended 2.0 ms undefined 7 Lab. (a) PULL PR Stripte ended 2.0 ms undefined 7 Lab. (a) PUL PR Stripte ended 2.0 ms

 | Lab. IrO4 (a) PL_DRAG Anglifier 2.0 ms undefined 2000 yrops 7 Lab. IrO5 (a) APLDTT Anglifier 2.0 ms undefined 2000 yrops 7 Lab. IrO5 (a) APLDTT Anglifier 2.0 ms undefined 2000 yrops 7 Lab. IrO5 (a) APLDTT Anglifier 2.0 ms undefined 2000 yrops 7 Lab. IrO5 (a) APLDTT Anglifier 2.0 ms undefined 2000 yrops 7 Lab. IrO5 (a) APLDTT Anglifier 2.0 ms undefined 2000 yrops 7 Lab. IrO5 (a) PULLPR Single ended 2.0 ms undefined 7 Lab. IrO7 PRI Single ended 2.0 ms undefined 7 Lab. IrO7 PRI Single ended 2.0 ms undefined 7 Lab. IrO7 PRI<

 | Lab. Ho74 (a) PLD_F04G Anglifer 2.0 ms undefined 2000 µ soil 7 Lab. Ho76 (a) APL_DF46 Anglifer 2.0 ms undefined 2000 µ soil 7 Lab. Ho76 (a) APL_DF46 Anglifer 2.0 ms undefined 2000 µ soil 7 Lab. Ho76 (a) APL_DF46 Anglifer 2.0 ms undefined 2000 µ soil 7
Lab. Ho76 (a) APL_DF4 Anglifer 2.0 ms undefined 2000 µ soil 7 Lab. Ho76 (a) APL_LF4 Styde ended 2.0 ms undefined 7 Lab. Ho7 Styde ended 2.0 ms undefined 7 7 Lab. Ho7 Styde ended 2.0 ms undefined 7 7 Lab. Ho7 Styde ended 2.0 ms undefined 7 7 Lab. Ho7 Styde ended 2.0 ms undefined 7 7 Lab. Ho7 PStyde ended 2.0 ms undefined </td <td>Tab. In-04 (a) PL_DTAG Anglifier 2000 (a) (a)</td> <td>Lab. InO4 (a) PL_DR4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DFT Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DFT Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DFT Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DFT Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO1 InO1 FT Signe endel 20 ms undefined 7 Lab. InO1 PULL_PL Signe endel 20 ms undefined 7 Lab. InO1 PULL_PL Signe endel 20 ms undefined 7 Lab. InO1 PUL_PL Signe endel 20 ms undefined 7 Lab. InO1 PUL_PL Signe endel 2</td> <td>Tab. In ONA (a) PLD_TAG Amplifier 2000 (a) (b) (c) (c)</td> <td>Tab. In ONL (a) PL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) PLUL_PR Single ended 2 0 ms undefined 7 Tab. In OS (a) PULL_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_PR Single ended 2 0 ms undef</td> <td>Tab. In ONL (a) PL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2 0 0 (p op 7) Tab. In OS (a) PLUL_PR Single ended 2 0 ms undefined 7 Tab. (a) PULL_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined</td> <td>Jack In Odd (a) PL_DTAG Angelier 20 ms undefined 2000 (a got 7) Jack In Odd (a) APL(DTAG Angelier 20 ms undefined 2000 (a got 7) Jack In Odd (a) APL(DTG Angelier 20 ms undefined 2000 (a got 7) Jack (a) APL(DTG Angelier 20 ms undefined 2000 (a got 7) Jack (a) APL(DTG Angelier 20 ms undefined 2000 (a got 7) Jack (a) PULL_PIC Single ended 20 ms undefined 7 Jack (a) PULL_PIC Single ended 20 ms undefined 7 Jack (a) PULL_PIC Single ended 20 ms undefined 7 Jack (a) POT PIC Single ended 20 ms undefined 7 Jack (a) POT PIC Single ended 20 ms undefined 7 Jack (a) <td< td=""><td></td><td>Jack Ind4 (a) PL_DTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) PLULP Single ended 2.0 ms undefined 200 p op 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PUTLAI Single ended 2.0 ms undefined 7 Jack (a)</td><td>All Info [a] PL_DMAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 7 All [a] PULL_PL Single ended 2.0 m undefined 7 All [a] PUL_AL Single ended 2.0 m undefined 7 All [a] POT_FA Single ended 2.0 m undefined 7 All [a] POT_FA Single ended 2.0 m</td><td>Lab. InO4 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. InO5 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. InO5 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. InO5 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. (a) PULL_PL Single ended 20 ms undefined 7 Lab. (a) PULL_PL Single ended 20 ms undefined 7 Lab. (a) PUL_PL Single ended 20 ms undefined 7 Lab. (a) PUT_PL Single ended 20 ms undefined 7 Lab. (a) PUT_PL Single ended 20 ms undefined</td><td>Lab. InO3 (a) PL_DR4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. (a) PULL_PR Single ended 20 ms undefined 7
 Lab. (a) PULL_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined</td><td>Lab. InO4 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended</td><td>Lab. InO4 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. InO5 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. InO5 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. InO5 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. (a) APL_DPAG Anglifer 2.0 ms undefined 7 Lab. (a) PLL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7<</td><td>Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2.00 prior 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PUL_AR Single ended 2.0 ms<!--</td--><td>Jack In Odd (a) PL_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a)<td></td><td>Jack Ind4 (a) PL_DTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) PLULP Single ended 2.0 ms undefined 200 p op 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PUTLAI Single ended 2.0 ms undefined 7 Jack (a)</td><td>Jack In ON (a) PL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack In OS (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack In OS (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 -db (a) PULL_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined</td><td>Import init Prof. initial Prof. 2000 prop. 7 Import Amport Amport 20 mm underined 2000 prop. 7 Import Status Inspire 20 mm underined 2000 prop. 7 Import Status Inspire Status Inspire 20 mm underined 7 Import Status Inspire Status Inspire 20 mm underined 7 Import Import Status Inspire Status Inspire 20 mm underined 7 Import Import Status Inspire 20 mm underined 7 Import Import Status Inspire 20 mm u</td><td>Jack Ind4 (a) PL_DTMG Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack (a) PULL_PL Single endde 20 ms undefined 7 Jack (a) PULL_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined</td></td></td></td<></td>
 | Tab. In-04 (a) PL_DTAG Anglifier 2000 (a)

 | Lab. InO4 (a) PL_DR4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DFT Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DFT Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DFT Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DFT Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO1 InO1 FT Signe endel 20 ms undefined 7 Lab. InO1 PULL_PL Signe endel 20 ms undefined 7 Lab. InO1 PULL_PL Signe endel 20 ms undefined 7 Lab. InO1 PUL_PL Signe endel 20 ms undefined 7 Lab. InO1 PUL_PL Signe endel 2

 | Tab. In ONA (a) PLD_TAG Amplifier 2000 (a) (b) (c)

 | Tab. In ONL (a) PL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) PLUL_PR Single ended 2 0 ms undefined 7 Tab. In OS (a) PULL_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_PR Single ended 2 0 ms undef

 | Tab. In ONL (a) PL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2000 (p op 7) Tab. In OS (a) APL_DTAGE Amplifier 2 0 ms undefined 2 0 0 (p op 7) Tab. In OS (a) PLUL_PR Single ended 2 0 ms undefined 7 Tab. (a) PULL_PR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined 7 Tab. (a) POT_FR Single ended 2 0 ms undefined

 | Jack In Odd (a) PL_DTAG Angelier 20 ms undefined 2000 (a got 7) Jack In Odd (a) APL(DTAG Angelier 20 ms undefined 2000 (a got 7) Jack In Odd (a) APL(DTG Angelier 20 ms undefined 2000 (a got 7) Jack (a) APL(DTG Angelier 20 ms undefined 2000 (a got 7) Jack (a) APL(DTG Angelier 20 ms undefined 2000 (a got 7) Jack (a) PULL_PIC Single ended 20 ms undefined 7 Jack (a) PULL_PIC Single ended 20 ms undefined 7 Jack (a) PULL_PIC Single ended 20 ms undefined 7 Jack (a) POT PIC Single ended 20 ms undefined 7 Jack (a) POT PIC Single ended 20 ms undefined 7 Jack (a) <td< td=""><td></td><td>Jack Ind4 (a) PL_DTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) PLULP Single ended 2.0 ms undefined 200 p op 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PUTLAI Single ended 2.0 ms undefined 7 Jack (a)</td><td>All Info [a] PL_DMAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 7 All [a] PULL_PL Single ended 2.0 m undefined 7 All [a] PUL_AL Single ended 2.0 m undefined 7 All [a] POT_FA Single ended 2.0 m undefined 7 All [a] POT_FA Single ended 2.0 m</td><td>Lab. InO4 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. InO5 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. InO5 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. InO5 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. (a) PULL_PL Single ended 20 ms undefined 7 Lab. (a) PULL_PL Single ended 20 ms undefined 7 Lab. (a) PUL_PL Single ended 20 ms undefined 7 Lab. (a) PUT_PL Single ended 20 ms undefined 7 Lab. (a) PUT_PL Single ended 20 ms undefined</td><td>Lab. InO3 (a) PL_DR4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. (a) PULL_PR Single ended 20 ms undefined 7 Lab. (a) PULL_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined</td><td>Lab. InO4 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG
Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended</td><td>Lab. InO4 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. InO5 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. InO5 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. InO5 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. (a) APL_DPAG Anglifer 2.0 ms undefined 7 Lab. (a) PLL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7<</td><td>Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2.00 prior 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PUL_AR Single ended 2.0 ms<!--</td--><td>Jack In Odd (a) PL_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a)<td></td><td>Jack Ind4 (a) PL_DTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) PLULP Single ended 2.0 ms undefined 200 p op 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PUTLAI Single ended 2.0 ms undefined 7 Jack (a)</td><td>Jack In ON (a) PL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack In OS (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack In OS (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 -db (a) PULL_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined</td><td>Import init Prof. initial Prof. 2000 prop. 7 Import Amport Amport 20 mm underined 2000 prop. 7 Import Status Inspire 20 mm underined 2000 prop. 7 Import Status Inspire Status Inspire 20 mm underined 7 Import Status Inspire Status Inspire 20 mm underined 7 Import Import Status Inspire Status Inspire 20 mm underined 7 Import Import Status Inspire 20 mm underined 7 Import Import Status Inspire 20 mm u</td><td>Jack Ind4 (a) PL_DTMG Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack (a) PULL_PL Single endde 20 ms undefined 7 Jack (a) PULL_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined</td></td></td></td<>
 |

 | Jack Ind4 (a) PL_DTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) PLULP Single ended 2.0 ms undefined 200 p op 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PUTLAI Single ended 2.0 ms undefined 7 Jack (a)
 | All Info [a] PL_DMAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 2000 grops 7 All Info [a] APL_DFAG Anglifer 2.0 m undefined 7 All [a] PULL_PL Single ended 2.0 m undefined 7 All [a] PUL_AL Single ended 2.0 m undefined 7 All [a] POT_FA Single ended 2.0 m undefined 7 All [a] POT_FA Single ended 2.0 m

 | Lab. InO4 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. InO5 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. InO5 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. InO5 (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. (a) APL_DPAG Anglifer 20 ms undefined 2000 µ spc 7 Lab. (a) PULL_PL Single ended 20 ms undefined 7 Lab. (a) PULL_PL Single ended 20 ms undefined 7 Lab. (a) PUL_PL Single ended 20 ms undefined 7 Lab. (a) PUT_PL Single ended 20 ms undefined 7 Lab. (a) PUT_PL Single ended 20 ms undefined
 | Lab. InO3 (a) PL_DR4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. InO5 (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. (a) APL_DF4G Anglifer 20 ms undefined 2000 µ sign 7 Lab. (a) PULL_PR Single ended 20 ms undefined 7 Lab. (a) PULL_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined

 | Lab. InO4 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) APL_DPAG Anglier 2.0 ms undefined 2000 proj 7 Lab. InO5 (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended 2.0 ms undefined 7 Lab. (a) PUL_PL Stripte ended
 | Lab. InO4 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. InO5 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. InO5 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. InO5 (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prof. 7 Lab. (a) APL_DPAG Anglifer 2.0 ms undefined 7 Lab. (a) PLL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. (a) PULL_PR Stright ended 2.0 ms undefined 7<
 | Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2000 prior 7 Lab. InOA (a) APL_DPAG Anglifer 2.0 ms undefined 2.00 prior 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PUL_AR Single ended 2.0 ms </td <td>Jack In Odd (a) PL_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a)<td></td><td>Jack Ind4 (a) PL_DTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) PLULP Single ended 2.0 ms undefined 200 p op 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PUTLAI Single ended 2.0 ms undefined 7 Jack (a)</td><td>Jack In ON (a) PL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack In OS (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack In OS (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 -db (a) PULL_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined</td><td>Import init Prof. initial
Prof. 2000 prop. 7 Import Amport Amport 20 mm underined 2000 prop. 7 Import Status Inspire 20 mm underined 2000 prop. 7 Import Status Inspire Status Inspire 20 mm underined 7 Import Status Inspire Status Inspire 20 mm underined 7 Import Import Status Inspire Status Inspire 20 mm underined 7 Import Import Status Inspire 20 mm underined 7 Import Import Status Inspire 20 mm u</td><td>Jack Ind4 (a) PL_DTMG Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack (a) PULL_PL Single endde 20 ms undefined 7 Jack (a) PULL_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined</td></td> | Jack In Odd (a) PL_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a) (b) (a) AD_DTAG Angelier 2.000 (a) (a) <td></td> <td>Jack Ind4 (a) PL_DTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) PLULP Single ended 2.0 ms undefined 200 p op 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PUTLAI Single ended 2.0 ms undefined 7 Jack (a)</td> <td>Jack In ON (a) PL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack In OS (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack In OS (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 -db (a) PULL_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined</td> <td>Import init Prof. initial Prof. 2000 prop. 7 Import Amport Amport 20 mm underined 2000 prop. 7 Import Status Inspire 20 mm underined 2000 prop. 7 Import Status Inspire Status Inspire 20 mm underined 7 Import Status Inspire Status Inspire 20 mm underined 7 Import Import Status Inspire Status Inspire 20 mm underined 7 Import Import Status Inspire 20 mm underined 7 Import Import Status Inspire 20 mm u</td> <td>Jack Ind4 (a) PL_DTMG Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack (a) PULL_PL Single endde 20 ms undefined 7 Jack (a) PULL_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined</td> |
 | Jack Ind4 (a) PL_DTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack Ind5 (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) APLDTAG Amplifier 2.0 ms undefined 2000 p op 7 Jack (a) PLULP Single ended 2.0 ms undefined 200 p op 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PULLP Single ended 2.0 ms undefined 7 Jack (a) PUTLAI Single ended 2.0 ms undefined 7 Jack (a)
 | Jack In ON (a) PL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack In OS (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack In OS (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) APL_DTAG Amplifier 20 ms undefined 2000 (p spc) 7 Jack (a) PULL_PL Single ended 20 ms undefined 7 -db (a) PULL_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined 7 -db (a) POT_PL Single ended 20 ms undefined | Import init Prof. initial Prof. 2000 prop. 7 Import Amport Amport 20 mm underined 2000 prop. 7 Import Status Inspire 20 mm underined 2000 prop. 7 Import Status Inspire Status Inspire 20 mm underined 7 Import Status Inspire Status Inspire 20 mm underined 7 Import Import Status Inspire Status Inspire 20 mm underined 7 Import Import Status Inspire 20 mm underined 7 Import Import Status Inspire 20 mm u | Jack Ind4 (a) PL_DTMG Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack Ind5 (a) APLPT Anglier 20 ms undefined 2000 µ spc 7 Jack (a) PULL_PL Single endde 20 ms undefined 7 Jack (a) PULL_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined 7 Jack (a) POT_PL Single endde 20 ms undefined |
| Ab H005 (a) AR_LIFT Anglifer 20 ms undefined 2000 µ eps 7 Ab H007 (a) AR_LIFT Anglifer 20 ms undefined 2000 µ eps 7 Ab H007 (a) AR_LIFT Anglifer 20 ms undefined 2000 µ eps 7 Ab H007 (a) AR_LIFT Anglifer 20 ms undefined 2000 µ eps 7 Ab H007 (a) PULL_PR Stright ended 20 ms undefined 7 Ab (a) PULL_PR Stright ended 20 ms undefined 7 Ab (a) PULL_PR Stright ended 20 ms undefined 7 Ab (a) PULL_PR Stright ended 20 ms undefined 7 Ab (a) PULL_PR Stright ended 20 ms undefined 7 Ab (a) PUT_PR Stright ended 20 ms undefined 7 Ab (a) PUT_PR Strightended 20 ms undefined

 | -db. hof5 (a) AB_DRAG Angliff Angliffer 2.0 ms undefined 2000 g eps ?
-db. hof7 (a) AL_UFT Angliffer 2.0 ms undefined 2000 g eps ?
-db. hof7 (a) AL_UFT Angliffer 2.0 ms undefined 2000 g eps ?
-db. hof7 (a) AL_UFT Angliffer 2.0 ms undefined 2000 g eps ?
-db. hof7 (a) AL_UFT Angliffer 2.0 ms undefined 2000 g eps ?
-db. hof PULL_PR Stopberdd 2.0 ms undefined ?
-db. hof PUT_PR Stopperdd 2.0 ms undefined ?
-db. hof PUT_PR Sto

 | All InfO (a) APL_IFT Anglifier 2.0 ms undefined 2000 a res 7 Ab InfO (a) APL_OPAG Anglifier 2.0 ms undefined 2000 a res 7 Ab InfO (a) AL_UFT Anglifier 2.0 ms undefined 2000 a res 7 Ab InfO (a) AL_UFT Anglifier 2.0 ms undefined 2000 a res 7 Ab (a) INUL_PF Single moded 2.0 ms undefined 20 7 Ab (a) INUL_PF Single moded 2.0 ms undefined 7 Ab (a) INUL_PF Single moded 2.0 ms undefined 7 Ab (a) INUL_AR Single moded 2.0 ms undefined 7 Ab (a) POT_FR Single moded 2.0 ms undefined 7 Ab (a) POT_FR Single moded 2.0 ms undefined 7 <td></td> <td>Lab. Inf05 (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Inf07 (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Inf07 (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Inf07 (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Inf07 (a) PULL_PR Stright ended 20 ms undefined 7 Lab. (a) PULL_PR Stright
ended 20 ms undefined 7 Lab. (a) PULL_PR Stright ended 20 ms undefined 7 Lab. (a) PUL_AL Stright ended 20 ms undefined 7 Lab. (a) PUL_AL Stright ended 20 ms undefined 7 Lab. (a) PUL_AL Stright ended 20 ms undefined</td> <td>Laß. Iróð (a) APL_UFT Anglifer 20 m: uddnind 2000 µ eps ? Laß. Iróð (a) APLDAGA Anglifer 20 m: uddnind 2000 µ eps ? Laß. Iróð (a) ALLUFT Anglifer 20 m: uddnind 2000 µ eps ? Laß. Iróð (a) ALLUFT Anglifer 20 m: undnind 2000 µ eps ? Laß. Iróð (a) APLDAG Anglifer 20 m: undnind ? Liß. VILL_PR Ströde redde 20 m: undnind ? ? Liß. VILL_PR Ströde redde 20 m: undnind ? ? Liß. (a) POLL_PR Ströde redde 20 m: undnind ? Liß. (a) POLL_PR Ströde redde 20 m: undnind ? Liß. (a) POLFR Ströde redde 20 m: undnind ? Liß. (a) POLA</td> <td>L-db. Iró5 (a) AP_LUFT Anglifer 20 ms undefined 2000 µ eps 7 L-db. Iró5 (a) AP_DRAG Anglifer 20 ms undefined 2000 µ eps 7 L-db. Iró5 (a) AL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Iró5 (a) AL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Iró5 (a) AP_LUPR Single ended 20 ms undefined 7 Lab. (a) PULL PR Single ended 20 ms undefined 7 Lab. (a) PULL PR Single ended 20 ms undefined 7 Lab. (a) PULPR Single ended 20 ms undefined 7 Lab. (a) PULPR Single ended 20 ms undefined 7 Lab. (a) PULPR Single ended 20 ms undefined</td> <td>Lab. in/05 (a) AL_UFT Anglifer 2.0 ms undefined 2000 µ spt) 7 Lab. in/05 (a) AL_UFT Anglifer 2.0 ms undefined 2000 µ spt) 7 Lab. in/07 (a) AL_UFT Anglifer 2.0 ms undefined 2000 µ spt) 7 Lab. in/07 (a) AL_UFT Anglifer 2.0 ms undefined 2000 µ spt) 7 Lab. in/07 (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. in/07 (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. in/0 PULL_PR Stright ended 2.0 ms undefined 7 Lab. in/0 PULL_AR Stright ended 2.0 ms undefined 7 Lab. in/0 PULL_AR Stright ended 2.0 ms undefined 7 Lab. in/0 PULL_AR Stright ended 2.0 ms undefined 7 Lab. in/0 PUT_AL Stright ended <</td> <td>Jab. Ind5 (a) AT_UFT Anglier 20 ms undefined 2000 µ dps 7 Jab. Ind5 (a) AT_UFT Anglier 20 ms undefined 2000 µ dps 7 Jab. Ind5 (a) AT_UFT Anglier 20 ms undefined 2000 µ dps 7 Jab. Ind5 (a) AT_UFT Anglier 20 ms undefined 2000 µ dps 7 Jab. (a) ILL_FR Single ended 20 ms undefined 7 Jab. (a) PULL_FR Single ended 20 ms undefined 7 Jab. (a) PULL_AN Single ended 20 ms undefined 7 Jab. (a) POT_FR Single ended 20 ms undefined 7 Jab. (a) POT_FR Single ended 20 ms undefined 7 Jab. (a) POT_FR Single ended 20 ms undefined 7</td> <td></td> <td></td> <td>Ba No55 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba No77 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba No77 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba (a) APLANS Angelier 2 0 ms undefined 2000 µ eps 7 Ba (a) PULL_PR Single ended 2 0 ms undefined 7 Ba (a) PULL_PR Single ended 2 0 ms undefined 7 Ba (a) PULL_AR Single ended 2 0 ms undefined 7 Ba (a) POT_PR Single ended 2 0 ms undefined 7 Ba (a) POT_PR Single ended 2 0 ms undefined 7 Ba (a) POT_PR Single ended 2 0 ms undefined 7</td> <td>Ba No55 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba No77 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba No77 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba (a) PULL_FR Single ended 2 0 ms undefined 7 Ba (a) PULL_FR Single ended 2 0 ms undefined 7 Ba (a) PULL_AR Single ended 2 0 ms undefined 7 Ba (a) POT_FR Single ended 2 0 ms undefined 7 Ba (a) POT_FR Single ended 2 0 ms undefined 7 Ba (a) POT_FR Single ended 2 0 ms undefined 7</td> <td></td> <td></td> <td></td> <td>Junction (a) APL_LIFT Anglifier 2.0 m undefined 2000 group 7 Junction (a) APL_DFAC Anglifier 2.0 m undefined 2000 group 7 Junction (a) APL_DFAC Anglifier 2.0 m undefined 2000 group 7 Junction (a) APL_DFAC Anglifier 2.0 m undefined 2000 group 7 Junction (a) APL_DFAC Single ended 2.0 m undefined 2.0 m undefined 2.0 m undefined 2.0 m undefined 7 Junction (a) PULL_PR Single ended 2.0 m undefined 7 Junction (a) POT_PA Single ended 2.0 m undefined 7 Junction (a) POT_PA Single ended 2.0 m undefined 7 Junction (a) POT_PA Single ended 2.0 m undefined 7 Junction (a) POT_PA</td> <td>Lab. Hrófs (a) APL_UFT Angelier 2000 µ eps ? Lab. Hrófs (a) APL_UFT Angelier 200 m undefined 2000 µ eps ? Lab. Hrófs (a) APL_UFT Angelier 20 m undefined 2000 µ eps ? Lab. Hrófs (a) APLUFT Angelier 20 m undefined 2000 µ eps ? Lab. Hrófs (a) APLUFT Angelier 20 m undefined 2000 µ eps ? Lab. Hrófs (a) PULL_PR Single ended 20 m undefined 7 Lab. Hrófs (a) PULL_PR Single ended 20 m undefined 7 Lab. Hrófs (a) PULL_PR Single ended 20 m undefined 7 Lab. Hrófs (a) PUT_PL Single ended 20 m undefined 7 Lab. Hrófs (a) PUT_PL Single ended 20 m undefined 7 Lab. Hrófs (a) PUT_PL Single ended 20 m undef</td> <td></td> <td>-Ab. Info (a) APL_UFT Anglifer 20 ms
undefined 2000 µ eps ? -Ab. Info (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps ? -Ab. Info (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps ? -Ab. Info (a) APL_DAGA Anglifer 20 ms undefined 2000 µ eps ? -Ab. (a) PULL_PR Strops ended 20 ms undefined ? ? -Ab. (a) PULL_PR Strops ended 20 ms undefined ? ? -Ab. (a) PULL_AL Strops ended 20 ms undefined ? ? . -Ab. (a) POT_FR Strops ended 20 ms undefined ? ? . -Ab. (a) POT_FR Strops ended 20 ms undefined ? </td> <td>Lab. 1r005 (a) APL_UFT Anglifier 20 ms undefined 2000 µ eps ? Lab. 1r007 (a) APL_UFT Anglifier 20 ms undefined 2000 µ eps ? Lab. 1r007 (a) APL_UFT Anglifier 20 ms undefined 2000 µ eps ? Lab. 1r007 (a) APL_UFT Anglifier 20 ms undefined 2000 µ eps ? Lab. 1r007 (a) PULLPR Strobe ended 20 ms undefined 200 µ eps ? Lab. 1r01 (a) PULLPR Strobe ended 20 ms undefined ? Lab. 1r01 (a) PULLPR Strobe ended 20 ms undefined ? Lab. 1r01 PUTLPR Strobe ended 20 ms undefined ? Lab. 1r01 PUTLPR Strobe ended 20 ms undefined ? Lab. 1r01 PUTLPR Strobe ended 20 ms undefined ? Lab. 1r01 PUTLPR Strobe ended 20 ms undefined ?<td>Lab. 1x005 (a) APL_UFT Anglifier 2000 µ dpt ? Lab. 1x005 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x007 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x007 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x007 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x01 (a) PULL_PR Strade ended 200 m undefined ? Lab. 1x01 PULL_PR Strade ended 20 m undefined ? ? Lab. 1x01 PULL_PR Strade ended 20 m undefined ? ? Lab. 1x01 PULL_PR Strade ended 20 m undefined ? ? Lab. 1x01 PUT_PR Strade ended 20 m undefined ? ? Lab. 1x01 PUT_PL Strade ended 20 m undefined ?</td><td></td><td></td><td></td><td></td><td></td><td></td></td>
 |

 | Lab. Inf05 (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Inf07 (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Inf07 (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Inf07 (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Inf07 (a) PULL_PR Stright ended 20 ms undefined 7 Lab. (a) PULL_PR Stright ended 20 ms undefined 7 Lab. (a) PULL_PR Stright ended 20 ms undefined 7 Lab. (a) PUL_AL Stright ended 20 ms undefined 7 Lab. (a) PUL_AL Stright ended 20 ms undefined 7 Lab. (a) PUL_AL Stright ended 20 ms undefined

 | Laß. Iróð (a) APL_UFT Anglifer 20 m: uddnind 2000 µ eps ? Laß. Iróð (a) APLDAGA Anglifer 20 m: uddnind 2000 µ eps ? Laß. Iróð (a) ALLUFT Anglifer 20 m: uddnind 2000 µ eps ? Laß. Iróð (a) ALLUFT Anglifer 20 m: undnind 2000 µ eps ? Laß. Iróð (a) APLDAG Anglifer 20 m: undnind ? Liß. VILL_PR Ströde redde 20 m: undnind ? ? Liß. VILL_PR Ströde redde 20 m: undnind ? ? Liß. (a) POLL_PR Ströde redde 20 m: undnind ? Liß. (a) POLL_PR Ströde redde 20 m: undnind ? Liß. (a) POLFR Ströde redde 20 m: undnind ? Liß. (a) POLA

 | L-db. Iró5 (a) AP_LUFT Anglifer 20 ms undefined 2000 µ eps 7 L-db. Iró5 (a) AP_DRAG Anglifer 20 ms undefined 2000 µ eps 7 L-db. Iró5 (a) AL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Iró5 (a) AL_UFT Anglifer 20 ms undefined 2000 µ eps 7 Lab. Iró5 (a) AP_LUPR Single ended 20 ms undefined 7 Lab. (a) PULL PR Single ended 20 ms undefined 7 Lab. (a) PULL PR Single ended 20 ms undefined 7 Lab. (a) PULPR Single ended 20 ms undefined 7 Lab. (a) PULPR Single ended 20 ms undefined 7 Lab. (a) PULPR Single ended 20 ms undefined

 | Lab. in/05 (a) AL_UFT Anglifer 2.0 ms undefined 2000 µ spt) 7 Lab. in/05 (a) AL_UFT Anglifer 2.0 ms undefined 2000 µ spt) 7 Lab. in/07 (a) AL_UFT Anglifer 2.0 ms undefined 2000 µ spt) 7 Lab. in/07 (a) AL_UFT Anglifer 2.0 ms undefined 2000 µ spt) 7 Lab. in/07 (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. in/07 (a) PULL_PR Stright ended 2.0 ms undefined 7 Lab. in/0 PULL_PR Stright ended 2.0 ms undefined 7 Lab. in/0 PULL_AR Stright ended 2.0 ms undefined 7 Lab. in/0 PULL_AR Stright ended 2.0 ms undefined 7 Lab. in/0 PULL_AR Stright ended 2.0 ms undefined 7 Lab. in/0 PUT_AL Stright ended <

 | Jab. Ind5 (a) AT_UFT Anglier 20 ms undefined 2000 µ dps 7 Jab. Ind5 (a) AT_UFT Anglier 20 ms undefined 2000 µ dps 7 Jab. Ind5 (a) AT_UFT Anglier 20 ms undefined 2000 µ dps 7 Jab. Ind5 (a) AT_UFT Anglier 20 ms undefined 2000 µ dps 7 Jab. (a) ILL_FR Single ended 20 ms undefined 7 Jab. (a) PULL_FR Single ended 20 ms undefined 7 Jab. (a) PULL_AN Single ended 20 ms undefined 7 Jab. (a) POT_FR Single ended 20 ms undefined 7 Jab. (a) POT_FR Single ended 20 ms undefined 7 Jab. (a) POT_FR Single ended 20 ms undefined 7

 |

 |

 | Ba No55 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba No77 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba No77 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba (a) APLANS Angelier 2 0 ms undefined 2000 µ eps 7 Ba (a) PULL_PR Single ended 2 0 ms undefined 7 Ba (a) PULL_PR Single ended 2 0 ms undefined 7 Ba (a) PULL_AR Single ended 2 0 ms undefined 7 Ba (a) POT_PR Single ended 2 0 ms undefined 7 Ba (a) POT_PR Single ended 2 0 ms undefined 7 Ba (a) POT_PR Single ended 2 0 ms undefined 7

 | Ba No55 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba No77 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba No77 (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba (a) APL_UFT Angelier 2 0 ms undefined 2000 µ eps 7 Ba (a) PULL_FR Single ended 2 0 ms undefined 7 Ba (a) PULL_FR Single ended 2 0 ms undefined 7 Ba (a) PULL_AR Single ended 2 0 ms undefined 7 Ba (a) POT_FR Single ended 2 0 ms undefined 7 Ba (a) POT_FR Single ended 2 0 ms undefined 7 Ba (a) POT_FR Single ended 2 0 ms undefined 7

 |

 |

 |

 | Junction (a) APL_LIFT Anglifier 2.0 m undefined 2000 group 7 Junction (a) APL_DFAC Anglifier 2.0 m undefined 2000 group 7 Junction (a) APL_DFAC Anglifier 2.0 m undefined 2000 group 7 Junction (a) APL_DFAC Anglifier 2.0 m undefined 2000 group 7 Junction (a) APL_DFAC Single ended 2.0 m undefined 2.0 m undefined 2.0 m undefined 2.0 m undefined 7 Junction (a) PULL_PR Single ended 2.0 m undefined 7 Junction (a) POT_PA Single ended 2.0 m undefined 7 Junction (a) POT_PA Single ended 2.0 m undefined 7 Junction (a) POT_PA Single ended 2.0 m undefined 7 Junction (a) POT_PA
 | Lab. Hrófs (a) APL_UFT Angelier 2000 µ eps ? Lab. Hrófs (a) APL_UFT Angelier 200 m undefined 2000 µ eps ? Lab. Hrófs (a) APL_UFT Angelier 20 m undefined 2000 µ eps ? Lab. Hrófs (a) APLUFT Angelier 20 m undefined 2000 µ eps ? Lab. Hrófs (a) APLUFT Angelier 20 m undefined 2000 µ eps ? Lab. Hrófs (a) PULL_PR Single ended 20 m undefined 7 Lab. Hrófs (a) PULL_PR Single ended 20 m undefined 7 Lab. Hrófs (a) PULL_PR Single ended 20 m undefined 7 Lab. Hrófs (a) PUT_PL Single ended 20 m undefined 7 Lab. Hrófs (a) PUT_PL Single ended 20 m undefined 7 Lab. Hrófs (a) PUT_PL Single ended 20 m undef

 |
 | -Ab. Info (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps ? -Ab. Info (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps ? -Ab. Info (a) APL_UFT Anglifer 20 ms undefined 2000 µ eps ? -Ab. Info (a) APL_DAGA Anglifer 20 ms undefined 2000 µ eps ? -Ab. (a) PULL_PR Strops ended 20 ms undefined ? ? -Ab. (a) PULL_PR Strops ended 20 ms undefined ? ? -Ab. (a) PULL_AL Strops ended 20 ms undefined ? ? . -Ab. (a) POT_FR Strops ended 20 ms undefined ? ? . -Ab. (a) POT_FR Strops ended 20 ms undefined ?
 | Lab. 1r005 (a) APL_UFT Anglifier 20 ms undefined 2000 µ eps ? Lab. 1r007 (a) APL_UFT Anglifier 20 ms undefined 2000 µ eps ? Lab. 1r007 (a) APL_UFT Anglifier 20 ms undefined 2000 µ eps ? Lab. 1r007 (a) APL_UFT Anglifier 20 ms undefined 2000 µ eps ? Lab. 1r007 (a) PULLPR Strobe ended 20 ms undefined 200 µ eps ? Lab. 1r01 (a) PULLPR Strobe ended 20 ms undefined
 ? Lab. 1r01 (a) PULLPR Strobe ended 20 ms undefined ? Lab. 1r01 PUTLPR Strobe ended 20 ms undefined ? Lab. 1r01 PUTLPR Strobe ended 20 ms undefined ? Lab. 1r01 PUTLPR Strobe ended 20 ms undefined ? Lab. 1r01 PUTLPR Strobe ended 20 ms undefined ? <td>Lab. 1x005 (a) APL_UFT Anglifier 2000 µ dpt ? Lab. 1x005 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x007 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x007 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x007 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x01 (a) PULL_PR Strade ended 200 m undefined ? Lab. 1x01 PULL_PR Strade ended 20 m undefined ? ? Lab. 1x01 PULL_PR Strade ended 20 m undefined ? ? Lab. 1x01 PULL_PR Strade ended 20 m undefined ? ? Lab. 1x01 PUT_PR Strade ended 20 m undefined ? ? Lab. 1x01 PUT_PL Strade ended 20 m undefined ?</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td>
 | Lab. 1x005 (a) APL_UFT Anglifier 2000 µ dpt ? Lab. 1x005 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x007 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x007 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x007 (a) APL_UFT Anglifier 200 m undefined 2000 µ dpt ? Lab. 1x01 (a) PULL_PR Strade ended 200 m undefined ? Lab. 1x01 PULL_PR Strade ended 20 m undefined ? ? Lab. 1x01 PULL_PR Strade ended 20 m undefined ? ? Lab. 1x01 PULL_PR Strade ended 20 m undefined ? ? Lab. 1x01 PUT_PR Strade ended 20 m undefined ? ? Lab. 1x01 PUT_PL Strade ended 20 m undefined ? |
 |
 | |
 | | |
| -Ab. In/OG (a) AL_DRAG Anglier 20 ms undefined 2000 µ eps ? -Ab. In/OR (a) AL_DRAG Anglier 20 ms undefined 2000 µ eps ? -Ab. In/OR (a) AL_ET Anglier 20 ms undefined 2000 µ eps ? -Ab. (a) PUL_PL Single roded 20 ms undefined ? ? . -Ab. (a) PUL_PL Single roded 20 ms undefined ? .

 | -db. bróß (a) AL_DFNAG Anglér Anglér 20 m. undefined 2000 a esa ?
-db. bróß (a) AL_DFT Anglér 20 m. undefined 2000 a esa ?
-db. bróß (a) AL_DFT Anglér 20 m. undefined 2000 a esa ?
-db. bróß (a) AL_DFT Strate and 2000 a esa ?
-db. bróß (a) AL_DFNAG Anglér 20 m. undefined ?
-db. bróß (a) PULL_PR Strate and 20 m. undefined ?
-db. (b) PULL_PR Strate

 | Ab Info (a) APL_DPAG Anaptitier 2.00 ms undefined 2000 ms reps 7 Ab Info (a) AL_LPF Anaptitier 2.0 ms undefined 2000 ms reps 7 Ab Info (a) AL_LPF Single ended 2.0 ms undefined 2000 ms reps 7 Ab Info FUL_PF Single ended 2.0 ms undefined 7 Ab (a) FUL_PF Single ended 2.0 ms undefined 7 Ab (a) FUL_PF Single ended 2.0 ms undefined 7 Ab (a) FUL_PF Single ended 2.0 ms undefined 7 Ab (a) FUT_PF Single ended 2.0 ms undefined 7 Ab (a) FUT_PF Single ended 2.0 ms undefined 7 Ab (a) FUT_PF Single ended 2.0 ms undefined 7 Ab (b) <td></td> <td></td> <td>-db. info6 (a) AL_DEPAG Angelier 20 ms undefined 2000 µ eps ? -db. info8 (a) AL_DEPAG Angelier 20 ms undefined 2000 µ eps ? -db. info8 (a) AL_DEPAG Angelier 20 ms undefined 2000 µ eps ? -db. (a) PULL_PR Single ended 20 ms undefined ? -db. (a) PULL_PR Single ended 20 ms undefined ? -db. (a) PULL_PL Single ended 20 ms undefined ? -db. (a) PULL_PL Single ended 20 ms undefined
 ? -db. (a) PULL_PL Single ended 20 ms undefined ? -db. (a) PUL_PL Single ended 20 ms undefined ? -db. (a) PUT_PR Single ended 20 ms undefined ? -db. (a) PUT_PR Singl</td> <td>-db. in/06 (a) AL_DET Anglifier 20 ms undefined 2000 µeps ? -db. in/07 (a) AL_DET Anglifier 20 ms undefined 2000 µeps ? -db. in/08 (a) AL_DET Anglifier 20 ms undefined 2000 µeps ? -db. in/07 (a) PLLPF Single ended 20 ms undefined 2000 µeps ? -db. (a) PLLPF Single ended 20 ms undefined ? -db. (a) PULP Single ended 20 ms undefined ? -db. (a) PULP Single ended 20 ms undefined ? -db. (a) POTP Single ended 20 ms undefined ? -db. (a) POTP Single ended 20 ms undefined ? -db. (a) POTP Single ended 20 ms undefined ?</td> <td>Jab. In VOG (a) AL_DFRAG Angelier 20 ms undefined 2000 μ spin ? Jab. In VOB (a) AL_DFRAG Angelier 20 ms undefined 2000 μ spin ? Jab. In VOB (a) AL_DFRAG Angelier 20 ms undefined 2000 μ spin ? Jab. Feldbur Interdie Interdient 2000 μ spin ? 2 Interdient 2000 μ spin ? Jab. Feldbur Interdie Interdient 20 ms undefined ? Jab. Interdient PULL_PR Straße ended 20 ms undefined ? Jab. Interdient PULL_PR Straße ended 20 ms undefined ? Jab. Interdient POT_FPR Straße ended 20 ms undefined ? Jab. Interdient POT_FPR Straße ended 20 ms undefined ? Jab. Interdient POT_FPR Straße ended 20 ms undefined ? Jab.</td> <td>-Imb No66 (a) AL_UFR Analytic 2.0 ms undefined 2000 µ eps ? -Imb No70 (a) AL_UFR Analytic 2.0 ms undefined 2000 µ eps ? -Imb No70 (a) AL_UFR Analytic 2.0 ms undefined 2000 µ eps ? -Imb Febtbas (a) PULL_PR Stords ended 2.0 ms undefined ? -Imb (a) PULL_PR Stords ended 2.0 ms undefined ? -Imb (a) PULL_PR Stords ended 2.0 ms undefined ? -Imb (a) POT_PR Stords ended 2.0 ms undefined ? -Imb (a) POT_PR Stords ended 2.0 ms undefined ? -Imb (a) POT_PR Stords ended 2.0 ms undefined ? -Imb (a) POT_AL Stords ended 2.0 ms undefined ? <</td> <td>Image Problem Provide <thprovide< th=""> <thprovide< th=""> <thpro< td=""><td>- db. 1676 (a) AL_UFT Anglér 20 m undéried 2000 µ eps ?
- db. 1677 (a) AL_UFT Anglér 20 m undéried 2000 µ eps ?
- db. 1678 (a) AL_UFT Service 20 m undéried 2000 µ eps ?
- db. 1678 (a) PULL_PR Sequencies 20 m undéried 7 m undéried 8 m undéried 20 m undéried 7 m undéried 8 m undéried 8 m undéried 20 m undéried 7 m undéried 8 m undéried 9 m undéried 9 m undéried 7 m undéried 9 m undéried 7 m undéried 7 m undéried 9 m undéried 7 m undéried 9 m u</td><td></td><td>- 1. 1005 (a) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (b) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ on undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ on undefined 20000 µ on undefined 2000 µ on undefined 2000 µ on u</td><td></td><td></td><td>- Ab InfO (a) AA_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (a) A_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (b) (a) A_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (b) (b) (a) A_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (b) (b) (b) (b) (b) (b) (b) (b) (b) (b)</td><td>ab. Info (a) APL_DPAG Anglifier 2.0 m undefined 2000 µ eps 7 ab. Info (a) APL_DPAG Anglifier 2.0 m undefined 2000 µ eps 7 ab. Info (a) APL_DPAG Anglifier 2.0 m undefined 2000 µ eps 7 ab. (a) PULL_PL Single ended 2.0 m undefined 7 ab. (a) PULL_PL Single ended 2.0 m undefined 7 ab. (a) PULL_AL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a)</td><td>-Lb. In/OG (a) AL_DFRAG Angelier 20 ms undefined 2000 µ eps ? -Lb. In/OZ (a) AL_DFRAG Angelier 20 ms undefined 2000 µ eps ? -Lb. In/OZ (a) AL_DFRAG Angelier 20 ms undefined 2000 µ eps ? -Lb. In/OZ (a) FMLL PL Strage ended 20 ms undefined ? -Lb. In/OZ (a) FMLL PL Strage ended 20 ms undefined ? -Lb. Iai (a) FMLL PL Strage ended 20 ms undefined ? -Lb. Iai (a) FMLL PL Strage ended 20 ms undefined ? -Lb. Iai (a) FOT_FR1 Strage ended 20 ms undefined ? -Lb. Iai (a) FOT_FR1 Strage ended 20 ms undefined ? -Lb. Iai (a) FOT_FR1 Strage ended 20 ms undefined ? -Lb. Iai (b)</td><td>Image Image Open Subscript Open Subscript</td><td>-Ab InfoG (a) AL_DRAG Angelier 20 ms undefined 2000 μeps ? -Ab InfOG (a) AL_DRAG Angelier 20 ms undefined 2000 μeps ? -Ab InfOG (a) AL_DRAG Angelier 20 ms undefined 2000 μeps ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) POT_PR Strage ended 20 ms undefined ? -Ab (a) POT_PR Strage ended 20 ms undefined ? -Ab (a) POT_PR Strage ended 20 ms undefined ? -Ab (a)</td><td>-Ab In OG (a) AL_DFRAG Anaplier 20 ms undefined 2000 μ spin ? -Ab In OB (a) AL_DFRAG Anaplier 20 ms undefined 2000 μ spin ? -Ab In OB (a) AL_DFRAG Anaplier 20 ms undefined 2000 μ spin ? -Ab Into Mathematics 20 ms undefined 2000 μ spin ? -Ab Into PULL_PR Strage ended 20 ms undefined ? -Ab Into PULL_PR Strage ended 20 ms undefined ? -Ab Into PULL_PR Strage ended 20 ms undefined ? -Ab
Into PUL_PR Strage ended 20 ms undefined ? -Ab Into PUL_PR Strage ended 20 ms undefined ? -Ab Into PUL_PR Strage ended 20 ms undefined ? -Ab <t< td=""><td>-Ab In/OG (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB INIL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PUL_PR Single ended 20 ms undefined ? -Ab Iai PUL_PR Single ended 20 ms undefined ? -Ab Iai PUT_PR Single ended 20 ms undefined ? -Ab Iai</td><td></td><td></td><td></td><td>-db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Sigle ended 20 m undefined 2000 µ eps ?
-db. hrófi (a) FULL_FR Single ended 20 m undefined ?
-db. (a) FULL_FR Single ended 20 m undefined ?
-db. (a) FULL_AL Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 1000 m lot m
-db. (c) POT_FR Single Single</td><td>Da In-06 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da In-07 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da In-07 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da</td><td></td></t<></td></thpro<></thprovide<></thprovide<></td> |

 |

 | -db. info6 (a) AL_DEPAG Angelier 20 ms undefined 2000 µ eps ? -db. info8 (a) AL_DEPAG Angelier 20 ms undefined 2000 µ eps ? -db. info8 (a) AL_DEPAG Angelier 20 ms undefined 2000 µ eps ? -db. (a) PULL_PR Single ended 20 ms undefined ? -db. (a) PULL_PR Single ended 20 ms undefined ? -db. (a) PULL_PL Single ended 20 ms undefined ? -db. (a) PULL_PL Single ended 20 ms undefined ? -db. (a) PULL_PL Single ended 20 ms undefined ? -db. (a) PUL_PL Single ended 20 ms undefined ? -db. (a) PUT_PR Single ended 20 ms undefined ? -db. (a) PUT_PR Singl

 | -db. in/06 (a) AL_DET Anglifier 20 ms undefined 2000 µeps ? -db. in/07 (a) AL_DET Anglifier 20 ms undefined 2000 µeps ? -db. in/08 (a) AL_DET Anglifier 20 ms undefined 2000 µeps ? -db. in/07 (a) PLLPF Single ended 20
ms undefined 2000 µeps ? -db. (a) PLLPF Single ended 20 ms undefined ? -db. (a) PULP Single ended 20 ms undefined ? -db. (a) PULP Single ended 20 ms undefined ? -db. (a) POTP Single ended 20 ms undefined ? -db. (a) POTP Single ended 20 ms undefined ? -db. (a) POTP Single ended 20 ms undefined ?

 | Jab. In VOG (a) AL_DFRAG Angelier 20 ms undefined 2000 μ spin ? Jab. In VOB (a) AL_DFRAG Angelier 20 ms undefined 2000 μ spin ? Jab. In VOB (a) AL_DFRAG Angelier 20 ms undefined 2000 μ spin ? Jab. Feldbur Interdie Interdient 2000 μ spin ? 2 Interdient 2000 μ spin ? Jab. Feldbur Interdie Interdient 20 ms undefined ? Jab. Interdient PULL_PR Straße ended 20 ms undefined ? Jab. Interdient PULL_PR Straße ended 20 ms undefined ? Jab. Interdient POT_FPR Straße ended 20 ms undefined ? Jab. Interdient POT_FPR Straße ended 20 ms undefined ? Jab. Interdient POT_FPR Straße ended 20 ms undefined ? Jab.

 | -Imb No66 (a) AL_UFR Analytic 2.0 ms undefined 2000 µ eps ? -Imb No70 (a) AL_UFR Analytic 2.0 ms undefined 2000 µ eps ? -Imb No70 (a) AL_UFR Analytic 2.0 ms undefined 2000 µ eps ? -Imb Febtbas (a) PULL_PR Stords ended 2.0 ms undefined ? -Imb (a) PULL_PR Stords ended 2.0 ms undefined ? -Imb (a) PULL_PR Stords ended 2.0 ms undefined ? -Imb (a) POT_PR Stords ended 2.0 ms undefined ? -Imb (a) POT_PR Stords ended 2.0 ms undefined ? -Imb (a) POT_PR Stords ended 2.0 ms undefined ? -Imb (a) POT_AL Stords ended 2.0 ms undefined ? <

 | Image Problem Provide Provide <thprovide< th=""> <thprovide< th=""> <thpro< td=""><td>- db. 1676 (a) AL_UFT Anglér 20 m undéried 2000 µ eps ?
- db. 1677 (a) AL_UFT Anglér 20 m undéried 2000 µ eps ?
- db. 1678 (a) AL_UFT Service 20 m undéried 2000 µ eps ?
- db. 1678 (a) PULL_PR Sequencies 20 m undéried 7 m undéried 8 m undéried 20 m undéried 7 m undéried 8 m undéried 8 m undéried 20 m undéried 7 m undéried 8 m undéried 9 m undéried 9 m undéried 7 m undéried 9 m undéried 7 m undéried 7 m undéried 9 m undéried 7 m undéried 9 m u</td><td></td><td>- 1. 1005 (a) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (b) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ on undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ on undefined 20000 µ on undefined 2000 µ on undefined 2000 µ on u</td><td></td><td></td><td>- Ab InfO (a) AA_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (a) A_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (b) (a) A_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (b) (b) (a) A_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (b) (b) (b) (b) (b) (b) (b) (b) (b) (b)</td><td>ab. Info (a) APL_DPAG Anglifier 2.0 m undefined 2000 µ eps 7 ab. Info (a) APL_DPAG Anglifier 2.0 m undefined 2000 µ eps 7 ab. Info (a) APL_DPAG Anglifier 2.0 m undefined 2000 µ eps 7 ab. (a) PULL_PL Single ended 2.0 m undefined 7 ab. (a) PULL_PL Single ended 2.0 m undefined 7 ab. (a) PULL_AL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a)</td><td>-Lb. In/OG (a) AL_DFRAG Angelier 20 ms undefined 2000 µ eps ? -Lb. In/OZ (a) AL_DFRAG Angelier 20 ms undefined 2000 µ eps ? -Lb. In/OZ (a) AL_DFRAG Angelier 20 ms undefined 2000 µ eps ? -Lb. In/OZ (a) FMLL PL Strage ended 20 ms undefined ? -Lb. In/OZ (a) FMLL PL Strage ended 20 ms undefined ? -Lb. Iai (a) FMLL PL Strage ended 20 ms undefined ? -Lb. Iai (a) FMLL PL Strage ended 20 ms undefined ? -Lb. Iai (a) FOT_FR1 Strage ended 20 ms undefined ? -Lb. Iai (a) FOT_FR1 Strage ended 20 ms undefined ? -Lb. Iai (a) FOT_FR1 Strage ended 20 ms undefined ? -Lb. Iai (b)</td><td>Image Image Open Subscript Open Subscript</td><td>-Ab InfoG (a) AL_DRAG Angelier 20 ms undefined 2000 μeps ? -Ab InfOG (a) AL_DRAG Angelier 20 ms undefined 2000 μeps ? -Ab InfOG (a) AL_DRAG Angelier 20 ms undefined 2000 μeps ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) POT_PR Strage ended 20 ms undefined ? -Ab (a) POT_PR Strage ended 20 ms undefined ? -Ab (a) POT_PR Strage ended 20 ms undefined ? -Ab (a)</td><td>-Ab In OG (a) AL_DFRAG Anaplier 20 ms undefined 2000 μ spin ? -Ab In OB (a) AL_DFRAG Anaplier 20 ms undefined 2000 μ spin ? -Ab In OB (a) AL_DFRAG Anaplier 20 ms undefined 2000 μ spin ? -Ab Into Mathematics 20 ms undefined 2000 μ spin ? -Ab Into PULL_PR Strage ended 20 ms undefined ? -Ab Into PULL_PR Strage ended 20 ms undefined ? -Ab Into PULL_PR Strage ended 20 ms undefined ? -Ab Into PUL_PR Strage ended 20 ms undefined ? -Ab Into PUL_PR Strage ended 20 ms undefined ? -Ab Into PUL_PR Strage ended 20 ms undefined ? -Ab <t< td=""><td>-Ab In/OG (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB INIL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PUL_PR Single ended 20 ms undefined ? -Ab Iai PUL_PR Single ended 20 ms undefined ? -Ab Iai PUT_PR Single ended 20 ms undefined ? -Ab Iai</td><td></td><td></td><td></td><td>-db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Sigle ended 20 m undefined 2000 µ eps ?
-db. hrófi (a) FULL_FR Single ended 20 m undefined ?
-db. (a) FULL_FR Single ended 20 m undefined ?
-db. (a) FULL_AL Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR
Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 1000 m lot m
-db. (c) POT_FR Single Single</td><td>Da In-06 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da In-07 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da In-07 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da</td><td></td></t<></td></thpro<></thprovide<></thprovide<>
 | - db. 1676 (a) AL_UFT Anglér 20 m undéried 2000 µ eps ?
- db. 1677 (a) AL_UFT Anglér 20 m undéried 2000 µ eps ?
- db. 1678 (a) AL_UFT Service 20 m undéried 2000 µ eps ?
- db. 1678 (a) PULL_PR Sequencies 20 m undéried 7 m undéried 8 m undéried 20 m undéried 7 m undéried 8 m undéried 8 m undéried 20 m undéried 7 m undéried 8 m undéried 9 m undéried 9 m undéried 7 m undéried 9 m undéried 7 m undéried 7 m undéried 9 m undéried 7 m undéried 9 m u

 |

 | - 1. 1005 (a) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (b) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ on undefined 2000 µ op 2
- 1. 1005 (c) ALUFT Anaptier 20 m undefined 2000 µ on undefined 20000 µ on undefined 2000 µ on undefined 2000 µ on u

 |

 |

 | - Ab InfO (a) AA_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (a) A_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (b) (a) A_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (b) (b) (a) A_UPT Anglier 2 0 ms underhed 2000 g ep 7
- Ab InfO (b)
 | ab. Info (a) APL_DPAG Anglifier 2.0 m undefined 2000 µ eps 7 ab. Info (a) APL_DPAG Anglifier 2.0 m undefined 2000 µ eps 7 ab. Info (a) APL_DPAG Anglifier 2.0 m undefined 2000 µ eps 7 ab. (a) PULL_PL Single ended 2.0 m undefined 7 ab. (a) PULL_PL Single ended 2.0 m undefined 7 ab. (a) PULL_AL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a) POI_PL Single ended 2.0 m undefined 7 ab. (a)

 | -Lb. In/OG (a) AL_DFRAG Angelier 20 ms undefined 2000 µ eps ? -Lb. In/OZ (a) AL_DFRAG Angelier 20 ms undefined 2000 µ eps ? -Lb. In/OZ (a) AL_DFRAG Angelier 20 ms undefined 2000 µ eps ? -Lb. In/OZ (a) FMLL PL Strage ended 20 ms undefined ? -Lb. In/OZ (a) FMLL PL Strage ended 20 ms undefined ? -Lb. Iai (a) FMLL PL Strage ended 20 ms undefined ? -Lb. Iai (a) FMLL PL Strage ended 20 ms undefined ? -Lb. Iai (a) FOT_FR1 Strage ended 20 ms undefined ? -Lb. Iai (a) FOT_FR1 Strage ended 20 ms undefined ? -Lb. Iai (a) FOT_FR1 Strage ended 20 ms undefined ? -Lb. Iai (b)
 | Image Image Open Subscript
 | -Ab InfoG (a) AL_DRAG Angelier 20 ms undefined 2000 μeps ? -Ab InfOG (a) AL_DRAG Angelier 20 ms undefined 2000 μeps ? -Ab InfOG (a) AL_DRAG Angelier 20
ms undefined 2000 μeps ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) PULL_PR Strage ended 20 ms undefined ? -Ab (a) POT_PR Strage ended 20 ms undefined ? -Ab (a) POT_PR Strage ended 20 ms undefined ? -Ab (a) POT_PR Strage ended 20 ms undefined ? -Ab (a)
 | -Ab In OG (a) AL_DFRAG Anaplier 20 ms undefined 2000 μ spin ? -Ab In OB (a) AL_DFRAG Anaplier 20 ms undefined 2000 μ spin ? -Ab In OB (a) AL_DFRAG Anaplier 20 ms undefined 2000 μ spin ? -Ab Into Mathematics 20 ms undefined 2000 μ spin ? -Ab Into PULL_PR Strage ended 20 ms undefined ? -Ab Into PULL_PR Strage ended 20 ms undefined ? -Ab Into PULL_PR Strage ended 20 ms undefined ? -Ab Into PUL_PR Strage ended 20 ms undefined ? -Ab Into PUL_PR Strage ended 20 ms undefined ? -Ab Into PUL_PR Strage ended 20 ms undefined ? -Ab <t< td=""><td>-Ab In/OG (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB INIL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PUL_PR Single ended 20 ms undefined ? -Ab Iai PUL_PR Single ended 20 ms undefined ? -Ab Iai PUT_PR Single ended 20 ms undefined ? -Ab Iai</td><td></td><td></td><td></td><td>-db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Sigle ended 20 m undefined 2000 µ eps ?
-db. hrófi (a) FULL_FR Single ended 20 m undefined ?
-db. (a) FULL_FR Single ended 20 m undefined ?
-db. (a) FULL_AL Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 1000 m lot m
-db. (c) POT_FR Single Single</td><td>Da In-06 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da In-07 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da In-07 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da</td><td></td></t<>
 | -Ab In/OG (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB (a) AL_DFMAG Angelier 20 ms undefined 2000 µ eps ? -Ab In/OB INIL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PULL_PR Single ended 20 ms undefined ? -Ab Iai PUL_PR Single ended 20 ms undefined ? -Ab Iai PUL_PR Single ended 20 ms undefined ? -Ab Iai PUT_PR Single ended 20 ms undefined ? -Ab Iai
 | |
 | | -db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Anglére 20 m undefined 2000 µ eps ?
-db. hrófi (a) AL_UFT Sigle ended 20 m undefined 2000 µ eps ?
-db. hrófi (a) FULL_FR Single ended 20 m undefined ?
-db. (a) FULL_FR Single ended 20 m undefined ?
-db. (a) FULL_AL Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined ?
-db. (a) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined ?
-db. (b) POT_FR Single ended 20 m undefined
?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 20 m undefined ?
-db. (c) POT_FR Single ended 1000 m undefined ?
-db. (c) POT_FR Single ended 1000 m lot m
-db. (c) POT_FR Single | Da In-06 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da In-07 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da In-07 (a) AL_DFAG Angelier 2.0 ms undefined 2000 μeps ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) PULL_PR Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da (a) POT_PL Stradge ended 2.0 ms undefined ? Da | |
| Lab. Inf07 (a) AL_LFF Anglier 20 ms undefined 2000 µ spo ? Lab. Inf08 (a) AL_LFF Single ended 20 ms undefined 2000 µ spo ? Lab. Inf08 FRILLFF Single ended 20 ms undefined 2000 µ spo ? Lab. Inf08 FRILLFF Single ended 20 ms undefined ? Lab. Inf08 IAI FRILLFF Single ended 20 ms undefined ? Lab. IAI IAI FRILLFF Single ended 20 ms undefined ? Lab. IAI IAI FRILLFF Single ended 20 ms undefined ? Lab. IAI IAI FRILLFF Single ended 20 ms undefined ? Lab. IAI IAI FRILLFF Single ended 20 ms undefined ? Lab. IAI IAI FRILLFF Single ended 20 ms undefined ? ? Lab. IAI IAI FRILLFF Single ended

 | -db. hof7 (a) AL_LIFT Anglér 200 gep 7 -db. (a) AL_DRAG Anglér 20 muddened 2000 gep 7 -db. (a) AL_DRAG Anglér 20 muddened 2000 gep 7 -db. (a) PULL_PR Single ended 20 muddened 7 -db. (a) POT_PR Single ended 20 muddened 7 -db. (a) POT_PR Single ended 20 muddened 7 -db. (a) POT_AL Single ended 20 muddened 7 -db. (a) POT_AL Single ended 20 mud

 | bit M07 (a) AL_LFF Augilier 2.0 ms undefined 2000 µ eps ? Dath Ind8 ADPA45 Amplier 2.0 ms undefined 2000 µ eps ? Feld During ADPA45 Amplier 2.0 ms undefined 2000 µ eps ? A

 |

 | Lab. Inf07 (a) AL_UFT Amplifier 2.0 ms undefined 2000 µ sps) ? Ab. InfoIrus (a) AL_UFT Amplifier 2.0 ms undefined 2000 µ sps) ? Ab. InfoIrus (a) PULL_PR Strade redded 2.0 ms undefined ? Ab. InfoIrus (a) PULL_PR Strade redded 2.0 ms undefined ? Ab. (a) PULL_PR Strade redde 2.0 ms undefined ? -ab. (a) PULL_PR Strade redde 2.0 ms undefined ? -ab. (a) PULL_PR Strade redde 2.0 ms undefined ? -ab. (a) PUT_PR Strade redde 2.0 ms undefined ? -ab. (a) PUT_PR Strade redde 2.0 ms undefined ? -ab. (a) PUT_PR Strade redde 2.0 ms undefined ? -ab. (a) PUT_PR Strade r

 | Lab. Inf07 (a) AL_LIFT Angulier 2.0 ms undefined 2000 µ eps ? Lab. Inf0. (a) AL_LOPAGA Angulier 2.0 ms undefined 2000 µ eps ? Lab. Inf0. PRUL.PR Single ended 2.0 ms undefined ? Lab. Inf0. PRUL.PR Single ended 2.0 ms undefined ? Lab. Inf0. PRUL.PR Single ended 2.0 ms undefined ? Lab. Inf0. PRUL.PR Single ended 2.0 ms undefined ? Lab. Inf0. PRUL.PR Single ended 2.0 ms undefined ? Lab. Inf0. PRUL.PR Single ended 2.0 ms undefined ? Lab. Inf0. PRUL.PR Single ended 2.0 ms undefined ? Lab. Inf0. PRUL.PR Single ended 2.0 ms undefined ? Lab. Inf0.

 | Lab. Inf07 (a) AL_LIFT Applifier 2.0 ms undefined 2.000 µ eps 7 Lab. (a) AL_LIFT Applifier 2.0 ms undefined 2.000 µ eps 7 Lab. (a) AL_LIFT Single ended 2.0 ms undefined 700 µ eps 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PULL_PR Single ended 2.0 ms undefined 7 Lab. (a) PDT_PR Single ended 2.0 ms undefined 7 Lab. (a) PDT_PR Single ended 2.0 ms undefined 7 Lab. (a) PDT_PR Single ended 2.0 ms undefined 7 Lab. (a) PDT_PR Single ended 2.0 ms undefined 7 Lab. (a) PDT_PR Single ended 2.0 ms undefined 7 Lab. (a) PDT_PR

 | Image: Indian and Section 1000 Bit ALLUFT Anglier 2000 µ spot 2 Image: Indian and Section 10000 Bit ALLUFT Anglier 2000 µ spot 2 Image: Indian and Section 10000 Bit ALLUFT Stage ended 20 ms undianed 2000 µ spot 7 Image: Indian and Section 10000 PRULL PR Stage ended 20 ms undianed 7 Image: Indiana Indiana PRULL PR Stage ended 20 ms undianed 7 Image: Indiana Bit PRULL PR Stage ended 20 ms undianed 7 Image: Indiana Indiana Stage ended 20 ms undianed 7 Image: Indiana Bit Provide
 20 ms undianed 7 Image: Indiana Dit Provide Bit Provide 10 ms 10 ms Image: Ind

 | Image: Instance Image: Im

 |

 | Jack Interf Augulier 2 0 m underined 2000 µ spo 7 Ab Interfect Augulier 2 0 m underined 2000 µ spo 7 Ab Interfect 10 MUL, FR Strage ended 2 0 m underined 2000 µ spo 7 Ab Interfect 10 MUL, FR Strage ended 2 0 m underined 7 Ab Interfect Strage ended 2 0 m underined 7 Ab Interfect Strage ended 2 0 m underined 7 Ab Interfect Strage ended 2 0 m underined 7 Ab Interfect Strage ended 2 0 m underined 7 Ab Interfect Strage ended 2 0 m underined 7 Ab Interfect Strage ended 2 0 m underined 7 Ab Interfect Strage ended 2 0 m underined 7 Ab Interad and mode Strage ended

 | Jack Int OT (a) AL_UFT Anglier 20 m underined 2000 µ spo 7 AL bridd (a) AL_DFAG Anglier 20 m underined 2000 µ spo 7 AL Fable state fragat (a) PULL_FR Single ended 20 m underined 2000 µ spo 7 AL Fable state (a) PULL_FR Single ended 20 m underined 7 -AL (a) PULL_AR Single ended 20 m underined 7 -AL (a) POT_FR Single ended 20 m underined 7 -AL (a) POT_FR Single ended 20 m underined 7 -AL (a) POT_FR Single ended 20 m underined 7 -AL (a) POT_FR Single ended 20 m underined 7 -AL (b) POT_AL Single ended 20 m underined 7 -AL </td <td>Jack Info (a) AL_UFF Anglier 20 m underined 2000 µ spo 7 AL Info (a) AL_DFAG Anglier 20 m underined 2000 µ spo 7 AL Field Law Fredu (a) PULL_PR Strade ended 20 m underined 7 AL Field Law Fredu Strade ended 20 m underined 7 -AL (a) PULL_PR Strade ended 20 m underined 7 -AL (a) PULL_AR Strade ended 20 m underined 7 -AL (a) POT_FR Strade ended 20 ms underined 7 -AL (a) POT_FR Strade ended 20 ms underined 7 -AL (a) POT_FR Strade ended 20 ms underined 7 -AL (a) POT_AR Strade ended 20 ms underined 7 -AL DOT_AR Strade ended 2</td> <td>Job Info (a) AL_UFT Amplier 20 m underined 2000 µ spp. 7 B Job Vieto Information Amplier 20 m underined 2000 µ spp. 7 B Feldbauringuts Information 20 m underined 2000 µ spp. 7 B Feldbauringuts Information 20 m underined 200 µ P AL Feldbauringuts Information 20 m underined 7 -dB Information Feldbauringuts 20 m underined 7 -dB Information Frequencial 20 m underined 7 -dB Info</td> <td>Judia Info (a) AL_UFT Amplier 20 m underined 2000 µ spo 7 B Judia Info (a) AL_DFAG Amplier 20 m underined 2000 µ spo 7 B Feld Low Input Feld Low Input 20 m underined 2000 µ spo 7 B Feld Low Input Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PUL_AN Single noded 20 m underined 7 -ab (a) POT_FPL Single noded 20 m underined 7 -ab (a) POT_AR Single noded 20 m underined 7 -ab (a) POT_AR Single node 20 m underined 7 -ab (a) POT_AR Single node</td> <td>Julia Into? (a) AL_UFT Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 M_LDRAG Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 POT_PR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m
undefined 7 -AB 60 POT_AR Single ended 2 0 m</td> <td>ab. Inf7 (a) AL_UFT Angelies 2.0 m undefined 20.00 prop. Fold Inf7 (a) AL_UFT Angelies 2.0 m undefined 20.00 prop. Fold Inf7 (a) AL_UFT Single model 2.0 m undefined 2.00 prop. ab. (a) PULL_PFR Single model 2.0 m undefined 7 ab. (a) PULL_APR Single model 2.0 m undefined 7 ab. (a) PULL_APR Single model 2.0 m undefined 7 ab. (a) PUL_AR Single model 2.0 m undefined 7 ab. (a) PUT_AR Single model 2.0 m undefined 7 ab. (a) PUT_AR Single model 2.0 m undefined 7 ab. (a) PUT_AR Single model 2.0 m undefined 7 ab. (b) PUT_AR Single model</td> <td>Lab. Info7 (a) LUFT Anglier 20 ms undefined 2000 µ sps ? Lab. Info8 (a) AL_DRAG Anglier 20 ms undefined 2000 µ sps ? Lab. (a) PUL_PR Stoph ended 20 ms undefined ? Lab. (a) PUL_PR Stoph ended 20 ms undefined ? Lab. (a) PUL_PR Stoph ended 20 ms undefined ? Lab. (a) PUL_PR Stoph ended 20 ms undefined ? Lab. (a) PUL_PR Stoph ended 20 ms undefined ? Lab. (a) POT_PR Stoph ended 20 ms undefined ? Lab. (a) POT_PR Stoph ended 20 ms undefined ? Lab. (a) POT_PR Stoph ended 20 ms undefined ? Lab. (a) POT_AR Stoph ended</td> <td>India India <td< td=""><td>Lab. Info7 (a) AL_LIFT Angulier 2.0 ms undefined 2000 µ eps ? Lab. Info10 (a) AL_DRAG Angulier 2.0 ms undefined 2000 µ eps ? Lab. Info10 (a) PRLLPR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (b) PULAR Single ended 2.0 ms undefined ? ? Lab. Info10 (b) PULA</td><td>Lab. Info Tele AL_UFT Anglier 20 ms undefined 2000 µ sps ? Lab. Info Info AL_UFT Anglier 20 ms undefined 2000 µ sps ? Lab. Info Info Info Info Info 1 Info Info ? Lab. Info Info Info Stoph ended 20 ms undefined ? ? Lab. Info Info Stoph ended 20 ms undefined ? ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ?</td><td>Lab. Info7 (a) LUFT Anglére 20 ms undérined 2000 µ sps ? Lb. Info8 (a) AL_DRAG Anglére 20 ms undérined 2000 µ sps ? Lb. Info8 Info8 20 ms undérined 2000 µ sps ? Lb. Info8 Info8 Stode model 20 ms undérined ? Lb. Info8 Info8 Stode model 20 ms undérined ? Lb. Info8 Info8 Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model</td></td<><td>Job Info (a) AL_UFT Amplier 20 m underined 2000 µ spo 7 AL Info (a) AL_DFAGS Amplier 20 m underined 2000 µ spo 7 AL Foldbasingda (a) PALL_PR Stopk ended 20 m underined 2000 µ spo 7 AL Foldbasingda (a) PALL_PR Stopk ended 20 m underined 7 -dB (a) PALL_ARI Stopk ended 20 m underined 7 -dB (a) POT_FR Stopk ended 20 m underined 7 -dB (a) POT_FR Stopk ended 20 m underined 7 -dB (a) POT_AR Stopk ended 20 m underined 7 -dB (a) POT_AR Stopk ended 20 m underined 7 -dB (a) POT_AR Stopk ended 20 m underined 7 -dB <td< td=""><td>Judia Info (a) AL_UFT Amplier 20 m underined 2000 µ spo 7 B Judia Info (a) AL_DFAG Amplier 20 m underined 2000 µ spo 7 B Feld Low Input Feld Low Input 20 m underined 2000 µ spo 7 B Feld Low Input Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PUL_AN Single noded 20 m underined 7 -ab (a) POT_FPL Single noded 20 m underined 7 -ab (a) POT_AR Single noded 20 m underined 7 -ab (a) POT_AR Single node 20 m underined 7 -ab (a) POT_AR Single node</td><td>Julia Into? (a) AL_UFT Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 M_LDRAG Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 POT_PR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m undefined 7 -AB 60 POT_AR Single ended 2 0 m</td><td>Job Info (a) AL_UFF Amplier 20 m undefined 2000 µ spp ? Briefd bar-inputs AL_DFAG Amplier 20 m undefined 2000 µ spp ? Briefd bar-inputs Iai PALL_PR Single ended 20 ms undefined ? AL Iai PALL_PR Single ended 20 ms undefined ? AL Iai PALL_PR
 Single ended 20 ms undefined ? AL Iai PALL_PR Single ended 20 ms undefined ? -AL Iai POLL_PR Single ended 20 ms undefined ? -AL Iai POT_FR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms</td><td>Image: Ind Control (International Control (Internatio)))</td><td>John Int/7 (a) AL_UFT Anaplier 2 0 m underined 2000 µ spon 7 John Mark Anaplier 2 0 m underined 2000 µ spon 7 John Mark Mark Anaplier 2 0 m underined 2000 µ spon 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Single ended 2 0 m underined 2 0 mutchined 7 John Single ended 2 0 m underined 2 0 mutchined 7 John Single ended 2 0 m underined 2 0 m underined 7 John Honglend 2 0 m underined</td></td<></td></td> | Jack Info (a) AL_UFF Anglier 20 m underined 2000 µ spo 7 AL Info (a) AL_DFAG Anglier 20 m underined 2000 µ spo 7 AL Field Law Fredu (a) PULL_PR Strade ended 20 m underined 7 AL Field Law Fredu Strade ended 20 m underined 7 -AL (a) PULL_PR Strade ended 20 m underined 7 -AL (a) PULL_AR Strade ended 20 m underined 7 -AL (a) POT_FR Strade ended 20 ms underined 7 -AL (a) POT_FR Strade ended 20 ms underined 7 -AL (a) POT_FR Strade ended 20 ms underined 7 -AL (a) POT_AR Strade ended 20 ms underined 7 -AL DOT_AR Strade ended 2

 | Job Info (a) AL_UFT Amplier 20 m underined 2000 µ spp. 7 B Job Vieto Information Amplier 20 m underined 2000 µ spp. 7 B Feldbauringuts Information 20 m underined 2000 µ spp. 7 B Feldbauringuts Information 20 m underined 200 µ P AL Feldbauringuts Information 20 m underined 7 -dB Information Feldbauringuts 20 m underined 7 -dB Information Frequencial 20 m underined 7 -dB Info

 | Judia Info (a) AL_UFT Amplier 20 m underined 2000 µ spo 7 B Judia Info (a) AL_DFAG Amplier 20 m underined 2000 µ spo 7 B Feld Low Input Feld Low Input 20 m underined 2000 µ spo 7 B Feld Low Input Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PUL_AN Single noded 20 m underined 7 -ab (a) POT_FPL Single noded 20 m underined 7 -ab (a) POT_AR Single noded 20 m underined 7 -ab (a) POT_AR Single node 20 m underined 7 -ab (a) POT_AR Single node

 | Julia Into? (a) AL_UFT Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 M_LDRAG Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 POT_PR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m undefined 7 -AB 60 POT_AR Single ended 2 0 m
 | ab. Inf7 (a) AL_UFT Angelies 2.0 m undefined 20.00 prop. Fold Inf7 (a) AL_UFT Angelies 2.0 m undefined 20.00 prop. Fold Inf7 (a) AL_UFT Single model 2.0 m undefined 2.00 prop. ab. (a) PULL_PFR Single model 2.0 m undefined 7 ab. (a) PULL_APR Single model 2.0 m undefined 7 ab. (a) PULL_APR Single model 2.0 m undefined 7 ab. (a) PUL_AR Single model 2.0 m undefined 7 ab. (a) PUT_AR Single model 2.0 m undefined 7 ab. (a) PUT_AR Single model 2.0 m undefined 7 ab. (a) PUT_AR Single model 2.0 m undefined 7 ab. (b) PUT_AR Single model

 | Lab. Info7 (a) LUFT Anglier 20 ms undefined 2000 µ sps ? Lab. Info8 (a) AL_DRAG Anglier 20 ms undefined 2000 µ sps ? Lab. (a) PUL_PR Stoph ended 20 ms undefined ? Lab. (a) PUL_PR Stoph ended 20 ms undefined ? Lab. (a) PUL_PR Stoph ended 20 ms undefined ? Lab. (a) PUL_PR Stoph ended 20 ms undefined ? Lab. (a) PUL_PR Stoph ended 20 ms undefined ? Lab. (a) POT_PR Stoph ended 20 ms undefined ? Lab. (a) POT_PR Stoph ended 20 ms undefined ? Lab. (a) POT_PR Stoph ended 20 ms undefined ? Lab. (a) POT_AR Stoph ended
 | India India <td< td=""><td>Lab. Info7 (a) AL_LIFT Angulier 2.0 ms undefined 2000 µ eps ? Lab. Info10 (a) AL_DRAG Angulier 2.0 ms undefined 2000 µ eps ? Lab. Info10 (a) PRLLPR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (b) PULAR Single ended 2.0 ms undefined ? ? Lab. Info10 (b) PULA</td><td>Lab. Info Tele AL_UFT Anglier 20 ms undefined 2000 µ sps ? Lab. Info Info AL_UFT Anglier 20 ms undefined 2000 µ sps ? Lab. Info Info Info Info Info 1 Info Info ? Lab. Info Info Info
 Stoph ended 20 ms undefined ? ? Lab. Info Info Stoph ended 20 ms undefined ? ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ?</td><td>Lab. Info7 (a) LUFT Anglére 20 ms undérined 2000 µ sps ? Lb. Info8 (a) AL_DRAG Anglére 20 ms undérined 2000 µ sps ? Lb. Info8 Info8 20 ms undérined 2000 µ sps ? Lb. Info8 Info8 Stode model 20 ms undérined ? Lb. Info8 Info8 Stode model 20 ms undérined ? Lb. Info8 Info8 Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model</td></td<> <td>Job Info (a) AL_UFT Amplier 20 m underined 2000 µ spo 7 AL Info (a) AL_DFAGS Amplier 20 m underined 2000 µ spo 7 AL Foldbasingda (a) PALL_PR Stopk ended 20 m underined 2000 µ spo 7 AL Foldbasingda (a) PALL_PR Stopk ended 20 m underined 7 -dB (a) PALL_ARI Stopk ended 20 m underined 7 -dB (a) POT_FR Stopk ended 20 m underined 7 -dB (a) POT_FR Stopk ended 20 m underined 7 -dB (a) POT_AR Stopk ended 20 m underined 7 -dB (a) POT_AR Stopk ended 20 m underined 7 -dB (a) POT_AR Stopk ended 20 m underined 7 -dB <td< td=""><td>Judia Info (a) AL_UFT Amplier 20 m underined 2000 µ spo 7 B Judia Info (a) AL_DFAG Amplier 20 m underined 2000 µ spo 7 B Feld Low Input Feld Low Input 20 m underined 2000 µ spo 7 B Feld Low Input Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PUL_AN Single noded 20 m underined 7 -ab (a) POT_FPL Single noded 20 m underined 7 -ab (a) POT_AR Single noded 20 m underined 7 -ab (a) POT_AR Single node 20 m underined 7 -ab (a) POT_AR Single node</td><td>Julia Into? (a) AL_UFT Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 M_LDRAG Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 POT_PR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m undefined 7 -AB 60 POT_AR Single ended 2 0 m</td><td>Job Info (a) AL_UFF Amplier 20 m undefined 2000 µ spp ? Briefd bar-inputs AL_DFAG Amplier 20 m undefined 2000 µ spp ? Briefd bar-inputs Iai PALL_PR Single ended 20 ms undefined ? AL Iai PALL_PR Single ended 20 ms undefined ? AL Iai PALL_PR Single ended 20 ms undefined ? AL Iai PALL_PR Single ended 20 ms undefined ? -AL Iai POLL_PR Single ended 20 ms undefined ? -AL Iai POT_FR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms</td><td>Image: Ind Control (International Control (Internatio)))</td><td>John Int/7 (a) AL_UFT Anaplier 2 0 m underined 2000 µ spon 7 John Mark Anaplier 2 0 m underined 2000 µ spon 7 John Mark Mark Anaplier 2 0 m underined 2000 µ spon 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Single ended 2 0 m underined 2 0 mutchined 7 John Single ended 2 0 m underined 2 0 mutchined 7 John Single ended 2 0 m underined 2 0 m underined 7 John Honglend 2 0 m underined</td></td<></td> | Lab. Info7 (a) AL_LIFT Angulier 2.0 ms undefined 2000 µ eps ? Lab. Info10 (a) AL_DRAG Angulier 2.0 ms undefined 2000 µ eps ? Lab. Info10 (a) PRLLPR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0
ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (a) PULL PR Single ended 2.0 ms undefined ? Lab. Info10 (b) PULAR Single ended 2.0 ms undefined ? ? Lab. Info10 (b) PULA
 | Lab. Info Tele AL_UFT Anglier 20 ms undefined 2000 µ sps ? Lab. Info Info AL_UFT Anglier 20 ms undefined 2000 µ sps ? Lab. Info Info Info Info Info 1 Info Info ? Lab. Info Info Info Stoph ended 20 ms undefined ? ? Lab. Info Info Stoph ended 20 ms undefined ? ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ? Lab. Info PULL PR Stoph ended 20 ms undefined ?
 | Lab. Info7 (a) LUFT Anglére 20 ms undérined 2000 µ sps ? Lb. Info8 (a) AL_DRAG Anglére 20 ms undérined 2000 µ sps ? Lb. Info8 Info8 20 ms undérined 2000 µ sps ? Lb. Info8 Info8 Stode model 20 ms undérined ? Lb. Info8 Info8 Stode model 20 ms undérined ? Lb. Info8 Info8 Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model 20 ms undérined ? Lb. Info8 PRULAPR Stode model
 | Job Info (a) AL_UFT Amplier 20 m underined 2000 µ spo 7 AL Info (a) AL_DFAGS Amplier 20 m underined 2000 µ spo 7 AL Foldbasingda (a) PALL_PR Stopk ended 20 m underined 2000 µ spo 7 AL Foldbasingda (a) PALL_PR Stopk ended 20 m underined 7 -dB (a) PALL_ARI Stopk ended 20 m underined 7 -dB (a) POT_FR Stopk ended 20 m underined 7 -dB (a) POT_FR Stopk ended 20 m underined 7 -dB (a) POT_AR Stopk ended 20 m underined 7 -dB (a) POT_AR Stopk ended 20 m underined 7 -dB (a) POT_AR Stopk ended 20 m underined 7 -dB <td< td=""><td>Judia Info (a) AL_UFT Amplier 20 m underined 2000 µ spo 7 B Judia Info (a) AL_DFAG Amplier 20 m underined 2000 µ spo 7 B Feld Low Input Feld Low Input 20 m underined 2000 µ spo 7 B Feld Low Input Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PUL_AN Single noded 20 m underined 7 -ab (a) POT_FPL Single noded 20 m underined 7 -ab (a) POT_AR Single noded 20 m underined 7 -ab (a) POT_AR Single node 20 m underined 7 -ab (a) POT_AR Single node</td><td>Julia Into? (a) AL_UFT Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 M_LDRAG Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 POT_PR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m undefined 7 -AB 60 POT_AR Single ended 2 0 m</td><td>Job Info (a) AL_UFF Amplier 20 m undefined 2000 µ spp ? Briefd bar-inputs AL_DFAG Amplier 20 m undefined 2000 µ spp ? Briefd bar-inputs Iai PALL_PR Single ended 20 ms undefined ? AL Iai PALL_PR Single ended 20 ms undefined ? AL Iai PALL_PR Single ended 20 ms undefined ? AL Iai PALL_PR Single ended 20 ms undefined ? -AL Iai POLL_PR Single ended 20 ms undefined ? -AL Iai POT_FR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms</td><td>Image: Ind Control (International Control (Internatio)))</td><td>John Int/7 (a) AL_UFT Anaplier 2 0 m underined 2000 µ spon 7 John Mark Anaplier 2 0 m underined 2000 µ spon 7 John Mark Mark Anaplier 2 0 m underined 2000 µ spon 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Single ended 2 0 m underined 2 0 mutchined 7 John Single ended 2 0 m underined 2 0 mutchined 7 John Single ended 2 0 m underined 2 0 m underined 7 John Honglend 2 0 m underined</td></td<> | Judia Info (a) AL_UFT Amplier 20 m underined 2000 µ spo 7 B Judia Info (a) AL_DFAG Amplier 20 m underined 2000 µ spo 7 B Feld Low Input Feld Low Input 20 m underined 2000 µ spo 7 B Feld Low Input Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PULL_AN Single noded 20 m underined 7 -ab (a) PUL_AN Single noded 20 m underined 7 -ab (a) POT_FPL Single noded 20 m underined 7 -ab (a) POT_AR Single noded 20 m underined 7 -ab (a) POT_AR Single node 20 m underined 7 -ab (a) POT_AR Single node
 | Julia Into? (a) AL_UFT Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 M_LDRAG Amplifier 2 0 m undefined 2000 µ spp. 7 B 164 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 AB 64 PLL_PR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 PLL_AR Single ended 2 0 m undefined 7 -AB 64 POT_PR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m undefined 7 -AB 64 POT_AR Single ended 2 0 m undefined 7 -AB 60 POT_AR Single ended 2 0 m | Job Info (a) AL_UFF Amplier 20 m undefined 2000 µ spp ? Briefd bar-inputs AL_DFAG Amplier 20 m undefined 2000 µ spp ? Briefd bar-inputs Iai PALL_PR Single ended 20 ms undefined ? AL Iai PALL_PR Single ended 20 ms undefined ? AL Iai PALL_PR Single ended 20 ms undefined ? AL Iai PALL_PR Single ended 20 ms undefined ? -AL Iai POLL_PR Single ended 20 ms undefined ? -AL Iai POT_FR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms undefined ? -AL Iai POT_AR Single ended 20 ms
 | Image: Ind Control (International Control (Internatio))) | John Int/7 (a) AL_UFT Anaplier 2 0 m underined 2000 µ spon 7 John Mark Anaplier 2 0 m underined 2000 µ spon 7 John Mark Mark Anaplier 2 0 m underined 2000 µ spon 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Mark Mark Single ended 2 0 m underined 7 John Single ended 2 0 m underined 2 0 mutchined 7 John Single ended 2 0 m underined 2 0 mutchined 7 John Single ended 2 0 m underined 2 0 m underined 7 John Honglend 2 0 m underined |
| Lab. Ind8 (a) Lab. (a) RLL_PR Single rode 20 ms undefined 7 Lab. (a) PULL_PR Single rode 20 ms undefined 7 Lab. (a) PULL_PR Single rode 20 ms undefined 7 Lab. (a) PULL_PR Single rode 20 ms undefined 7 Lab. (a) PULL_PR Single rode 20 ms undefined 7 Lab. (a) POT_PR Single rode 20 ms undefined 7 Lab. (a) POT_PR Single rode 20 ms undefined 7 Lab. (a) POT_PR Single rode 20 ms undefined 7 Lab. (a) POT_PR Single rode 20 ms undefined 7 Lab. (a) POT_PR Single rode 10 ms 10 s 10 s Lab. (a) POT_PR Single rode 10 ms <

 | Lall briefs AL_DPAG Angelfere 20 m undefined 2000 preps 7 Lall briefs AL_DPAG Single ended 20 m undefined 7 Lall briefs and PULL_PL Single ended 20 m undefined 7 Lall briefs Briefs Single ended 20 m undefined 7 Lall briefs Single ended 20 m

 | Bit Mole (a) AL_DRAG Ampleter 2.0 ms undefined 2.00 ms undefined 2.00 ms undefined 7 Bit Mole (a) FULL_FR Single ended 2.0 ms undefined 7 Bit Mole (a) FULL_FR Single ended 2.0 ms undefined 7 Bit Mole (a) FULL_AL Single ended 2.0 ms undefined 7 Bit Mole (a) FULL_AL Single ended 2.0 ms undefined 7 Bit Mole (a) FUL_AL Single ended 2.0 ms undefined 7 Bit Mole (a) FUT_FR Single ended 2.0 ms undefined 7 Bit Mole
 (a) FUT_FR Single ended 2.0 ms undefined 7 Bit Mole (b) FUT_FR Single ended 2.0 ms undefined 7 Bit Mole (b) FUT_FR Single ended 2.0 ms undefined 7 Bit Mole

 | Call DetB Intelligent Topology PILL PR Sequenced 20 mm undefined 7 data 1 PILL PR Sequenced 20 mm undefined 7 data 1 PILL PR Sequenced 20 mm undefined 7 -data 1 PILL PR Sequenced 20 mm undefined 7 -data 10 PILL PR Sequenced 20 mm undefined 7 -data 10 PILL PR Sequenced 20 mm undefined 7 -data 10 PILL PR Sequenced 20 mm undefined 7 -data 10 PIT_AR Sequenced 20 mm undefined 7

 | Lab. Ind8 (a) AL_PRAS Amplier 20 ms undefined 2000 prop. ? Lab. Ind1 PRL_PR Strobs enoded 20 ms undefined ? Lab. Ind1 PRL_PR Strobs enoded 20 ms undefined ? Lab. Ind1 PRL_PR Strobs enoded 20 ms undefined ? Lab. Ind1 PRL_PR Strobs enoded 20 ms undefined ? Lab. Ind1 PRL_PR Strobs enoded 20 ms undefined ? Lab. Ind1 PRL_PR Strobs enoded 20 ms undefined ? Lab. Ind1 PRL_PR Strobs enoded 20 ms undefined ? Lab. Ind1 PRL_PR Strobs enoded 20 ms undefined ? Lab. Ind1 PRL_PR Strobs enoded 20 ms undefined ? Lab. Ind1 PRL_PR Strobs enoded 20 ms unde

 | Lab. (a) AL_0PAG Angular 20 ms underined 2000 preps 7 Adb. (a) PULL_PR Strade ended 20 ms underined 7 Adb. (a) PULL_PR Strade ended 20 ms underined 7 Adb. (a) PULL_PR Strade ended 20 ms underined 7 Adb. (a) PULL_AL Strade ended 20 ms underined 7 Adb. (a) PUL_AL Strade ended 20 ms underined 7 Adb. (a) PUL_PR Strade ended 20 ms underined 7 Adb. (a) PUL_PR Strade ended 20 ms underined 7 Adb. (a) PUT_PR Strade ended 20 ms underined 7 Adb. (a) PUT_PR Strade ended 20 ms underined 7 Adb. (a) PUT_PR Strade ended 20 ms underined <t< td=""><td>Lab. 1008 a) AL_DRAG Anglere 20 ms underined 3000 yrsps 2 Lab. 100 IPALL_PR Single ended 20 ms underined 7 Lab. 101 IPALL_PR Single ended 20 ms underined 7 Lab. 101 IPALL_PR Single ended 20 ms underined 7 Lab. 101 IPALL_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined</td><td>LaB. Iv08 (a) AL_DRAG Angler 200 props 7 LaB. Feldbur Herk Styde redde 20 audred 2000 props 7 LaB. HAL_PR Styde redde 20 audred 7 7 LaB. HAL_PR Styde redde 20 audred 7 LaB. HA PULL_PR Styde redde 20 audred 7 LaB. HA PULL_PR Styde redde 20 audred 7 LaB. HA PULL_PR Styde redde 20 audred 7 LaB. HA POT_PL Styde redde 20 audred 7 LaB. HA POT_PL Styde redde 20 audred 7 LaB. HA POT_PL Styde redde 20 audred 7 LaB. HA POT_AL Styde redde 20 mudred 7 LaB. HA PO</td><td>Lab. boths (a) AL_DRAGS Angleier 20 m underned 200 m pop. 7 Lab. boths (a) PAL_PR Stodp ended 20 ms underned 7 Lab. boths (a) PAL_PR Stodp ended 20 ms underned 7 Lab. (a) PAL_PR Stodp ended 20 ms underned 7 Lab. (a) PAL_PR Stodp ended 20 ms underned 7 -Lab. (a) POT_FR Stopp ended 20 ms underned 7 -Lab. (a) POT_FR Stopp ended 20 ms underned 7 -Lab. (a) POT_FR Stopp ended 20 ms underned 7 -Lab. (a) POT_AL Stopp ended 20 ms underned 7 -Lab. (b) POT_AL Stopp ended 20 ms underned 7 -Lab. (b) POT_AL Stopp ended 20 ms underned 7 -Lab. (b) POT_AL Stopp ended 20 ms</td><td>Lab. 608 (a) AL_PRG Anglete 20 m underned 200 m pop. ? Lab. FridDur Internet (a) FALL_PR Stode model 20 m underned ? Lab. 100 (a) FALL_PR Stode model 20 m underned ? Lab. 101 FALL_PR Stode model 20 m underned ?
 Lab. 101 FALL_PR Stode model 20 m underned ? Lab. 101 FALL_AL Stoje model 20 m underned ? Lab. 101 FALL_PR Stoje model 20 m underned ? Lab. 101 FALL_AL Stoje model 20 m underned ? Lab. 101 FALL_PR Stoje model 20 m underned ? Lab. 102 FALL_PR Stoje model 20 m underned ? Lab. 102 FALL_PR Stoje model 20 m underned ? Lab. 102 FALL_PR Stoje model 20 m</td><td>LaB. br88 (a) AL, DRAGS Angleire 2 Dm underned 2000 prop. 7 LaB. br81 bits Field Law Field Law 20 mm underned 2000 prop. 7 LaB. 64 FILL, FR. Single model 20 mm undefined 7 LaB. 64 FILL, FR. Single model 20 mm undefined 7 LaB. 64 FILL, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model<!--</td--><td>LaB. br88 (a) AL, DRAGS Anglew 20 m underned 2000 prop. 7 LaB. br84 (a) PALL_PR Steph model 20 m underned 7 LaB. (a) PALL_PR Steph model 20 m underned 7 LaB. (a) PALL_PR Steph model 20 m underned 7 LaB. (a) PALL_PR Steph model 20 m underned 7 -LB. (a) POT_FR Steph model 20 m underned 7 -LB. (a) POT_FR Steph endel 20 m underned 7 -LB. (a) POT_FR Steph endel 20 m underned 7 -LB. (a) POT_AL Steph endel 20 m underned 7 -LB. (a) POT_AL Steph endel 20 m underned 7 -LB. (a) POT_AL Steph endel 20 m 10 m</td><td>LaB. br88 (a) ALD, DPAGS Angleter 2 D ms underined 2 000 prop. 7 LaB. br84 (a) PALL_PPA Single medid 2 D ms underined 7 LaB. (a) PALL_PPA Single medid 2 D ms underined 7 LaB. (a) PALL_PA Single medid 2 D ms underined 7 LaB. (a) PALL_AL Single medid 2 D ms underined 7 LaB. (a) POT_FL Single medid 2 D ms underined 7 LaB. (a) POT_FL Single medid 2 D ms underined 7 LB (b) POT_FL Single medid 2 D ms underined 7 LB (b) POT_FL Single medid 2 D ms underined 7 LB (b) DOT_FL Single medid 2 D ms underined 7 LB DOT_FL Single medid 2 D ms</td><td>Lab. briefs [a] [b] [b] [c] [c]</td><td>Lab. Lords (a) AL_DRAG Amplier 20 m undered 2000 prop. 7 Lab. Lords (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (b) POT_PR Single ended</td><td>Call Lot 08 (a) AL_DARAG Amplifier 20 ms underherd 2000 prop. 7 Call Fridstbur-tropped PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) POT_PR Single ended 20 ms underherd 7 Call (b) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PLA Single ended 20 ms<</td><td>Add Intelligence 2.0 min underlined 2.00 m; underlined 2.00 m; underlined 2.00 m; underlined 7 Add Intelligence 2.0 m; underlined 2.0 m; underlined 7 Add Intelligence 2.0 m; underlined 7 Ad</td><td>Lalls Ind8 (a) AL_DRAG Angleier 200 p. eps. ? Lalls Feldbur internation (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PL Stodge ended 20 ms undefined ? Lalls (a) FNL_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined</td><td>Lab. MolB (a) AL_DRAG Angletic 2 0 m undefined 2 00 p. eps. 7 Lab. Friddbur Hendral 10 Friddbur Hendral 2 0 ms undefined 7 Lab. (a) FULL_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FULL_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FULL_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (b) FOT_PL Stopke ended 2.</td><td>Lab. holds (a) AL_ORAG Amplier 20 ms underined 2000 pers; 7 Lab. holds (a) FulL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended
 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluT_PR Stradge ended 2.0 ms underined 7 Lab. in PluT_PR Stradge ended 2.0 ms underined 7<!--</td--><td>Lab. Ind8 (a) AL_DRAG Angleir 20 m. undefined 2000 prop. ? Lab. Ind80 FMSLur FMSLur Stode model 20 m. undefined ? Lab. Ind80 (a) FMLL PR Stode model 20 m. undefined ? Lab. Ind80 (a) FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined</td><td>Lab. Ind/B [a] AL_DRAG Angleir 200 prop. ? Lab. Fieldsurfer 200 prop. ? 200 prop. ? Lab. Fieldsurfer 101 PULL_PR Stodge ended 20 ms undefined 7 Lab. Init 101 PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended</td><td>Lab. total (a) LLD FARG Amplies 2 D m undered 2 000 prop. 7 Lab. Frainblass-information Frainblass-information 2 D m undered 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (b) FOT_FL Single ended 2 D m underlined 7 Lab. (b) FOT_FL Single ended 2 D m 10 s</td><td>Lab. Lords (a) AL_DRAG Amplier 20 m undered 2000 prop. 7 Lab. Lords (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (b) POT_PR Single ended</td><td>Call Lot 08 (a) AL_DARAG Amplifier 20 ms underherd 2000 prop. 7 Call Fridstbur-tropped PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) POT_PR Single ended 20 ms underherd 7 Call (b) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PLA Single ended 20 ms<</td><td>Lab. total (a) ALL DRAG Anglew 2 D m underned 2000 prop. 7 Lab. FridLbs-right (a) FRLL PR Single ended 2 D m underned 7 Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended</td><td>Lab. (v08) (a) (b) (a) (b) (a) (b) (c) (c)</td><td>Call briefs Control Co</td></td></td></t<>
 | Lab. 1008 a) AL_DRAG Anglere 20 ms underined 3000 yrsps 2 Lab. 100 IPALL_PR Single ended 20 ms underined 7 Lab. 101 IPALL_PR Single ended 20 ms underined 7 Lab. 101 IPALL_PR Single ended 20 ms underined 7 Lab. 101 IPALL_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined 7 Lab. 101 POT_PR Single ended 20 ms underined

 | LaB. Iv08 (a) AL_DRAG Angler 200 props 7 LaB. Feldbur Herk Styde redde 20 audred 2000 props 7 LaB. HAL_PR Styde redde 20 audred 7 7 LaB. HAL_PR Styde redde 20 audred 7 LaB. HA PULL_PR Styde redde 20 audred 7 LaB. HA PULL_PR Styde redde 20 audred 7 LaB. HA PULL_PR Styde redde 20 audred 7 LaB. HA POT_PL Styde redde 20 audred 7 LaB. HA POT_PL Styde redde 20 audred 7 LaB. HA POT_PL Styde redde 20 audred 7 LaB. HA POT_AL Styde redde 20 mudred 7 LaB. HA PO

 | Lab. boths (a) AL_DRAGS Angleier 20 m underned 200 m pop. 7 Lab. boths (a) PAL_PR Stodp ended 20 ms underned 7 Lab. boths (a) PAL_PR Stodp ended 20 ms underned 7 Lab. (a) PAL_PR Stodp ended 20 ms underned 7 Lab. (a) PAL_PR Stodp ended 20 ms underned 7 -Lab. (a) POT_FR Stopp ended 20 ms underned 7 -Lab. (a) POT_FR Stopp ended 20 ms underned 7 -Lab. (a) POT_FR Stopp ended 20 ms underned 7 -Lab. (a) POT_AL Stopp ended 20 ms underned 7 -Lab. (b) POT_AL Stopp ended 20 ms underned 7 -Lab. (b) POT_AL Stopp ended 20 ms underned 7 -Lab. (b) POT_AL Stopp ended 20 ms

 | Lab. 608 (a) AL_PRG Anglete 20 m underned 200 m pop. ? Lab. FridDur Internet (a) FALL_PR Stode model 20 m underned ? Lab. 100 (a) FALL_PR Stode model 20 m underned ? Lab. 101 FALL_PR Stode model 20 m underned ? Lab. 101 FALL_PR Stode model 20 m underned ? Lab. 101 FALL_AL Stoje model 20 m underned ? Lab. 101 FALL_PR Stoje model 20 m underned ? Lab. 101 FALL_AL Stoje model 20 m underned ? Lab. 101 FALL_PR Stoje model 20 m underned ? Lab. 102 FALL_PR Stoje model 20 m underned ? Lab. 102 FALL_PR Stoje model 20 m underned ? Lab. 102 FALL_PR Stoje model 20 m

 | LaB. br88 (a) AL, DRAGS Angleire 2 Dm underned 2000 prop. 7 LaB. br81 bits Field Law Field Law 20 mm underned 2000 prop. 7 LaB. 64 FILL, FR. Single model 20 mm undefined 7 LaB. 64 FILL, FR. Single model 20 mm undefined 7 LaB. 64 FILL, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model 20 mm undefined 7 LaB. 64 FOT, FR. Single model </td <td>LaB. br88 (a) AL, DRAGS Anglew 20 m underned 2000 prop. 7 LaB. br84 (a) PALL_PR Steph model 20 m underned 7 LaB. (a) PALL_PR Steph model 20 m underned 7 LaB. (a) PALL_PR Steph model 20 m underned 7 LaB. (a) PALL_PR Steph model 20 m underned 7 -LB. (a) POT_FR Steph model 20 m underned 7 -LB. (a) POT_FR Steph endel 20 m underned 7 -LB. (a) POT_FR Steph endel 20 m underned 7 -LB. (a) POT_AL Steph endel 20 m underned 7 -LB. (a) POT_AL Steph endel 20 m underned 7 -LB. (a) POT_AL Steph endel 20 m 10 m</td> <td>LaB. br88 (a) ALD, DPAGS Angleter 2 D ms underined 2 000 prop. 7 LaB. br84 (a) PALL_PPA Single medid 2 D ms underined 7 LaB. (a) PALL_PPA Single medid 2 D ms underined 7 LaB. (a) PALL_PA Single medid 2 D ms underined 7 LaB. (a) PALL_AL Single medid 2 D ms underined 7 LaB. (a) POT_FL Single medid 2 D ms underined 7 LaB. (a) POT_FL Single medid 2 D ms underined 7 LB (b) POT_FL Single medid 2 D ms underined 7 LB (b) POT_FL Single medid 2 D ms underined 7 LB (b) DOT_FL Single medid 2 D ms underined 7 LB DOT_FL Single medid 2 D ms</td> <td>Lab. briefs [a] [b] [b] [c] [c]</td> <td>Lab. Lords (a) AL_DRAG Amplier 20 m undered 2000 prop. 7 Lab. Lords (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (b) POT_PR Single ended</td> <td>Call Lot 08 (a) AL_DARAG Amplifier 20 ms underherd 2000 prop. 7 Call Fridstbur-tropped PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) POT_PR Single ended 20 ms underherd 7 Call (b) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PLA Single ended 20 ms<</td> <td>Add Intelligence 2.0 min underlined 2.00 m; underlined 2.00 m; underlined 2.00 m; underlined 7 Add Intelligence 2.0 m; underlined 2.0 m; underlined 7 Add Intelligence 2.0 m; underlined 7 Ad</td> <td>Lalls Ind8 (a) AL_DRAG Angleier 200 p. eps. ? Lalls Feldbur internation (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PL Stodge ended 20 ms undefined ? Lalls (a) FNL_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined</td> <td>Lab. MolB (a) AL_DRAG Angletic 2 0 m undefined 2 00 p. eps. 7 Lab. Friddbur Hendral 10 Friddbur Hendral 2 0 ms undefined 7 Lab. (a) FULL_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FULL_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FULL_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (b) FOT_PL Stopke ended 2.</td> <td>Lab. holds (a) AL_ORAG Amplier 20 ms underined 2000 pers; 7 Lab. holds (a) FulL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR
 Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluT_PR Stradge ended 2.0 ms underined 7 Lab. in PluT_PR Stradge ended 2.0 ms underined 7<!--</td--><td>Lab. Ind8 (a) AL_DRAG Angleir 20 m. undefined 2000 prop. ? Lab. Ind80 FMSLur FMSLur Stode model 20 m. undefined ? Lab. Ind80 (a) FMLL PR Stode model 20 m. undefined ? Lab. Ind80 (a) FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined</td><td>Lab. Ind/B [a] AL_DRAG Angleir 200 prop. ? Lab. Fieldsurfer 200 prop. ? 200 prop. ? Lab. Fieldsurfer 101 PULL_PR Stodge ended 20 ms undefined 7 Lab. Init 101 PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended</td><td>Lab. total (a) LLD FARG Amplies 2 D m undered 2 000 prop. 7 Lab. Frainblass-information Frainblass-information 2 D m undered 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (b) FOT_FL Single ended 2 D m underlined 7 Lab. (b) FOT_FL Single ended 2 D m 10 s</td><td>Lab. Lords (a) AL_DRAG Amplier 20 m undered 2000 prop. 7 Lab. Lords (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (b) POT_PR Single ended</td><td>Call Lot 08 (a) AL_DARAG Amplifier 20 ms underherd 2000 prop. 7 Call Fridstbur-tropped PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) POT_PR Single ended 20 ms underherd 7 Call (b) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PLA Single ended 20 ms<</td><td>Lab. total (a) ALL DRAG Anglew 2 D m underned 2000 prop. 7 Lab. FridLbs-right (a) FRLL PR Single ended 2 D m underned 7 Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended</td><td>Lab. (v08) (a) (b) (a) (b) (a) (b) (c) (c)</td><td>Call briefs Control Co</td></td> | LaB. br88 (a) AL, DRAGS Anglew 20 m underned 2000 prop. 7 LaB. br84 (a) PALL_PR Steph model 20 m underned 7 LaB. (a) PALL_PR Steph model 20 m underned 7 LaB. (a) PALL_PR Steph model 20 m underned 7 LaB. (a) PALL_PR Steph model 20 m underned 7 -LB. (a) POT_FR Steph model 20 m underned 7 -LB. (a) POT_FR Steph endel 20 m underned 7 -LB. (a) POT_FR Steph endel 20 m underned 7 -LB. (a) POT_AL Steph endel 20 m underned 7 -LB. (a) POT_AL Steph endel 20 m underned 7 -LB. (a) POT_AL Steph endel 20 m 10 m

 | LaB. br88 (a) ALD, DPAGS Angleter 2 D ms underined 2 000 prop. 7 LaB. br84 (a) PALL_PPA Single medid 2 D ms underined 7 LaB. (a) PALL_PPA Single medid 2 D ms underined 7 LaB. (a) PALL_PA Single medid 2 D ms underined 7 LaB. (a) PALL_AL Single medid 2 D ms underined 7 LaB. (a) POT_FL Single medid 2 D ms underined 7 LaB. (a) POT_FL Single medid 2 D ms underined 7 LB (b) POT_FL Single medid 2 D ms underined 7 LB (b) POT_FL Single medid 2 D ms underined 7 LB (b) DOT_FL Single medid 2 D ms underined 7 LB DOT_FL Single medid 2 D ms

 | Lab. briefs [a] [b] [b] [c]

 | Lab. Lords (a) AL_DRAG Amplier 20 m undered 2000 prop. 7 Lab. Lords (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab.
(b) POT_PR Single ended
 | Call Lot 08 (a) AL_DARAG Amplifier 20 ms underherd 2000 prop. 7 Call Fridstbur-tropped PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) POT_PR Single ended 20 ms underherd 7 Call (b) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PLA Single ended 20 ms<

 | Add Intelligence 2.0 min underlined 2.00 m; underlined 2.00 m; underlined 2.00 m; underlined 7 Add Intelligence 2.0 m; underlined 2.0 m; underlined 7 Add Intelligence 2.0 m; underlined 7 Ad
 | Lalls Ind8 (a) AL_DRAG Angleier 200 p. eps. ? Lalls Feldbur internation (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PR Stodge ended 20 ms undefined ? Lalls (a) FNLL_PL Stodge ended 20 ms undefined ? Lalls (a) FNL_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined ? Lalls (a) FOT_PL Stodge ended 20 ms undefined

 | Lab. MolB (a) AL_DRAG Angletic 2 0 m undefined 2 00 p. eps. 7 Lab. Friddbur Hendral 10 Friddbur Hendral 2 0 ms undefined 7 Lab. (a) FULL_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FULL_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FULL_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (a) FOT_PL Stopke ended 2.0 ms undefined 7 Lab. (b) FOT_PL Stopke ended 2.
 | Lab. holds (a) AL_ORAG Amplier 20 ms underined 2000 pers; 7 Lab. holds (a) FulL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluL_PR Stradge ended 2.0 ms underined 7 Lab. in PluT_PR Stradge ended 2.0 ms underined 7 Lab. in PluT_PR Stradge ended 2.0 ms underined 7 </td <td>Lab. Ind8 (a) AL_DRAG Angleir 20 m. undefined 2000 prop. ? Lab. Ind80 FMSLur FMSLur Stode model 20 m. undefined ? Lab. Ind80 (a) FMLL PR Stode model 20 m. undefined ? Lab. Ind80 (a) FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined</td> <td>Lab. Ind/B [a] AL_DRAG Angleir 200 prop. ? Lab. Fieldsurfer 200 prop. ? 200 prop. ?
 Lab. Fieldsurfer 101 PULL_PR Stodge ended 20 ms undefined 7 Lab. Init 101 PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended</td> <td>Lab. total (a) LLD FARG Amplies 2 D m undered 2 000 prop. 7 Lab. Frainblass-information Frainblass-information 2 D m undered 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (b) FOT_FL Single ended 2 D m underlined 7 Lab. (b) FOT_FL Single ended 2 D m 10 s</td> <td>Lab. Lords (a) AL_DRAG Amplier 20 m undered 2000 prop. 7 Lab. Lords (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (b) POT_PR Single ended</td> <td>Call Lot 08 (a) AL_DARAG Amplifier 20 ms underherd 2000 prop. 7 Call Fridstbur-tropped PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) POT_PR Single ended 20 ms underherd 7 Call (b) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PLA Single ended 20 ms<</td> <td>Lab. total (a) ALL DRAG Anglew 2 D m underned 2000 prop. 7 Lab. FridLbs-right (a) FRLL PR Single ended 2 D m underned 7 Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended</td> <td>Lab. (v08) (a) (b) (a) (b) (a) (b) (c) (c)</td> <td>Call briefs Control Co</td> | Lab. Ind8 (a) AL_DRAG Angleir 20 m. undefined 2000 prop. ? Lab. Ind80 FMSLur FMSLur Stode model 20 m. undefined ? Lab. Ind80 (a) FMLL PR Stode model 20 m. undefined ? Lab. Ind80 (a) FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMLL PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined ? Lab. Ind8 FMT_PR Stode model 20 m. undefined
 | Lab. Ind/B [a] AL_DRAG Angleir 200 prop. ? Lab. Fieldsurfer 200 prop. ? 200 prop. ? Lab. Fieldsurfer 101 PULL_PR Stodge ended 20 ms undefined 7 Lab. Init 101 PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init PULL_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended 20 ms
 undefined 7 Lab. Init POT_PR Stodge ended 20 ms undefined 7 Lab. Init POT_PR Stodge ended | Lab. total (a) LLD FARG Amplies 2 D m undered 2 000 prop. 7 Lab. Frainblass-information Frainblass-information 2 D m undered 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FULL_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (a) FOT_FL Single ended 2 D m underlined 7 Lab. (b) FOT_FL Single ended 2 D m underlined 7 Lab. (b) FOT_FL Single ended 2 D m 10 s
 | Lab. Lords (a) AL_DRAG Amplier 20 m undered 2000 prop. 7 Lab. Lords (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) PULL_PR Single ended 20 m underlined 7 Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (a) POT_PR Single ended 20 m underlined 7 -Lab. (b) POT_PR Single ended
 | Call Lot 08 (a) AL_DARAG Amplifier 20 ms underherd 2000 prop. 7 Call Fridstbur-tropped PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) PULL_PR Single ended 20 ms underherd 7 Call (a) POT_PR Single ended 20 ms underherd 7 Call (b) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PR Single ended 20 ms underherd 7 Call (c) POT_PLA Single ended 20 ms< | Lab. total (a) ALL DRAG Anglew 2 D m underned 2000 prop. 7 Lab. FridLbs-right (a) FRLL PR Single ended 2 D m underned 7 Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FRLL PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended 2 D m underned 7 -Lab. (a) FOT_PR Single ended | Lab. (v08) (a) (b) (a) (b) (a) (b) (c)
 | Call briefs Control Co |
| All Politicul reput PRL_PR Supple model 20 min undefined 7 All Field PRL_PR Stright ended 20 min undefined 7 All Field PRULAR Stright ended 20 min undefined 7 All Field PRULAR Stright ended 20 min undefined 7 All Field PRULAR Stright ended 20 min undefined 7 All Field PRULAR Stright ended 20 min undefined 7 All Field PRULAR Stright ended 20 min undefined 7 All Field PRULAR Stright ended 20 min undefined 7 All Field Divort Divort Stright ended 20 min undefined 7 All Divort Divort Divort Stright ended 20 min undefined 7 All Divort Divort Divort Bit logid 20 min undefined 7 All Divort Divort Divort Bit logid 100 min 100 min 100 min All Divort Divort Bit logid </td <td>adb. Fill_PR Single model 2.0 m. undefined 7 adb. (a) Fill_PR Single model 2.0 m. undefined 7 adb. (a) Fill_PR Single model 2.0 m. undefined 7 -adb. (a) Fill_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (b) FOT_AL Single model 2.0 m. undefined 7 <tr< td=""><td>TextBurn input: Image: Single ended 2.0 ms undefined 7 Ab (a) PULL_PR Single ended 2.0 ms undefined 7 Ab (a) PULL_AR Single ended 2.0 ms undefined 7 Ab (a) PULL_AR Single ended 2.0 ms undefined 7 Ab (a) PUL_PR Single ended 2.0 ms undefined 7 Ab (a) POT_PR Single ended 2.0 ms undefined 7 Ab (a) POT_PR Single ended 2.0 ms undefined 7 Ab (a) POT_AR Single ended 2.0 ms undefined 7 Ab (b) POT_AR Single ended 2.0 ms undefined 7 Ab (b) POT_AR Single ended 2.0 ms undefined 7 Ab (b) POT_AR Single ended 2.0 ms undefined 7 Ab (</td><td>B. And Duran Tryots Single moded 20 ms and defined 7 Bab Init JUL, PR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab<td>Deck Structure Structure 20 and ordered 2 and orderededededededededededededededededede</td><td>Alls Field <thf< td=""><td>All helitou-inputs Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -All</td><td>Bit Polity Styde reduct 20 Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PUT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_Abb Styde reduct 20 ms undefined 7 -Bab 141 POT_Abb<td>Ab. Fedda - Ingular Stagle reddet 20 Stagle reddet 7 Ab. 14) PULL_PR Stagle reddet 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_FPL Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 <td< td=""><td>Bit roll business Stage rodot 20 Bab Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot</td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Display regular Strage model 20 subdimed 7 B. 14 PULL_PR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms undefined 7 B.
 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 16 PUL_AR Single model 20 ms undefined 7 B. 10 PUL_AR Single model 100 ms 101 ms 101 ms 101 ms</td><td>B. Feld Low Teguta Single model 20 subdimed 7 -ab. 14 PRLL_PR Single model 20 ms subdimed 7 -ab. 14 PRLL_ARI Single model 20 ms subdimed 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRL_ARI Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 100</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>Test Durit PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (b) PUIT_PR Single medied 2.0 ms undefined 7</td><td>Abs Field Line Field Field Line Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PUT_AR Single ended 2.0 ms undefined 7 -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 104 PULM Bit Input 10 ms 10 ms 10 ms</td><td>Ab Fedda - Teodal [A] PULL_PR Single ended 2.0 ms undefined 7 </td><td>Ab FredUru Frouts Fill_LPR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 10 ms 10 ms 10 ms -Ab (b) PUT_AR Single ended 10 ms 10 ms 10 ms</td><td>Display Point Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUT_PR Stride ended 20 ms undefined 7 LB 61 PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20</td><td>Date Constraint Constant Constraint Constraint</td><td>B. Feld Law Pitt Single model 20 and official 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 16 PRL_PR Single model 20 ms undefined 7 Lab 16</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40
 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>B. Feld Dava - Ingula 14 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 POT_FR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 164 POT_AR Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Bit Injust 100 uns 10 s 10 s</td><td>Abs. Feedbare - Reput Feedbare - Reput Feedbare - Reput 7 </td><td>B. feld burnstepsis Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl POT FR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. Pot Unit Bit ing</td></thdisplay></td></thdisplay></td></td<></td></td></thf<></td></td></tr<></td> | adb. Fill_PR Single model 2.0 m. undefined 7 adb. (a) Fill_PR Single model 2.0 m. undefined 7 adb. (a) Fill_PR Single model 2.0 m. undefined 7 -adb. (a) Fill_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (a) FOT_PR Single model 2.0 m. undefined 7 -adb. (b) FOT_AL Single model 2.0 m. undefined 7 <tr< td=""><td>TextBurn input: Image: Single ended 2.0 ms undefined 7 Ab (a) PULL_PR Single ended 2.0 ms undefined 7 Ab (a) PULL_AR Single ended 2.0 ms undefined 7 Ab (a) PULL_AR Single ended 2.0 ms undefined 7 Ab (a) PUL_PR Single ended 2.0 ms undefined 7 Ab (a) POT_PR Single ended 2.0 ms undefined 7 Ab (a) POT_PR Single ended 2.0 ms undefined 7 Ab (a) POT_AR Single ended 2.0 ms undefined 7 Ab (b) POT_AR Single ended 2.0 ms undefined 7 Ab (b) POT_AR Single ended 2.0 ms undefined 7 Ab (b) POT_AR Single ended 2.0 ms undefined 7 Ab (</td><td>B. And Duran Tryots Single moded 20 ms and defined 7 Bab Init JUL, PR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab<td>Deck Structure Structure 20 and ordered 2 and orderededededededededededededededededede</td><td>Alls Field <thf< td=""><td>All helitou-inputs Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR
 Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -All</td><td>Bit Polity Styde reduct 20 Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PUT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_Abb Styde reduct 20 ms undefined 7 -Bab 141 POT_Abb<td>Ab. Fedda - Ingular Stagle reddet 20 Stagle reddet 7 Ab. 14) PULL_PR Stagle reddet 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_FPL Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 <td< td=""><td>Bit roll business Stage rodot 20 Bab Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot</td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Display regular Strage model 20 subdimed 7 B. 14 PULL_PR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 16 PUL_AR Single model 20 ms undefined 7 B. 10 PUL_AR Single model 100 ms 101 ms 101 ms 101 ms</td><td>B. Feld Low Teguta Single model 20 subdimed 7 -ab. 14 PRLL_PR Single model 20 ms subdimed 7 -ab. 14 PRLL_ARI Single model 20 ms subdimed 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRL_ARI Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 100</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>Test Durit PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (b) PUIT_PR Single medied 2.0 ms undefined 7</td><td>Abs Field Line Field Field Line Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PUT_AR Single ended 2.0 ms undefined 7 -Abs 164 PUT_AR Single
ended 100 ms 10 ms 10 ms -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 104 PULM Bit Input 10 ms 10 ms 10 ms</td><td>Ab Fedda - Teodal [A] PULL_PR Single ended 2.0 ms undefined 7 </td><td>Ab FredUru Frouts Fill_LPR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 10 ms 10 ms 10 ms -Ab (b) PUT_AR Single ended 10 ms 10 ms 10 ms</td><td>Display Point Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUT_PR Stride ended 20 ms undefined 7 LB 61 PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20</td><td>Date Constraint Constant Constraint Constraint</td><td>B. Feld Law Pitt Single model 20 and official 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 16 PRL_PR Single model 20 ms undefined 7 Lab 16</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>B. Feld Dava - Ingula 14 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 POT_FR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 164 POT_AR Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Bit Injust 100 uns 10 s 10 s</td><td>Abs. Feedbare - Reput Feedbare - Reput Feedbare - Reput 7 </td><td>B. feld burnstepsis Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl POT FR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. Pot Unit Bit ing</td></thdisplay></td></thdisplay></td></td<></td></td></thf<></td></td></tr<>
 | TextBurn input: Image: Single ended 2.0 ms undefined 7 Ab (a) PULL_PR Single ended 2.0 ms undefined 7 Ab (a) PULL_AR Single ended 2.0 ms undefined 7 Ab (a) PULL_AR Single ended 2.0 ms undefined 7 Ab (a) PUL_PR Single ended 2.0 ms undefined 7 Ab (a) POT_PR Single ended 2.0 ms undefined 7 Ab (a) POT_PR Single ended 2.0 ms undefined 7 Ab (a) POT_AR Single ended 2.0 ms undefined 7 Ab (b) POT_AR Single ended 2.0 ms undefined 7 Ab (b) POT_AR Single ended 2.0 ms undefined 7 Ab (b) POT_AR Single ended 2.0 ms undefined 7 Ab (

 | B. And Duran Tryots Single moded 20 ms and defined 7 Bab Init JUL, PR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab Init JUL, AR Single moded 20 ms undefined 7 Bab <td>Deck Structure Structure 20 and ordered 2 and orderededededededededededededededededede</td> <td>Alls Field <thf< td=""><td>All helitou-inputs Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -All</td><td>Bit Polity Styde reduct 20 Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PUT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_Abb Styde reduct 20 ms undefined 7 -Bab 141 POT_Abb<td>Ab. Fedda - Ingular Stagle reddet 20 Stagle reddet 7 Ab. 14) PULL_PR Stagle reddet 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_FPL Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 <td< td=""><td>Bit roll business Stage rodot 20 Bab Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai
 Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot</td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Display regular Strage model 20 subdimed 7 B. 14 PULL_PR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 16 PUL_AR Single model 20 ms undefined 7 B. 10 PUL_AR Single model 100 ms 101 ms 101 ms 101 ms</td><td>B. Feld Low Teguta Single model 20 subdimed 7 -ab. 14 PRLL_PR Single model 20 ms subdimed 7 -ab. 14 PRLL_ARI Single model 20 ms subdimed 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRL_ARI Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 100</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>Test Durit PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (b) PUIT_PR Single medied 2.0 ms undefined 7</td><td>Abs Field Line Field Field Line Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PUT_AR Single ended 2.0 ms undefined 7 -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 104 PULM Bit Input 10 ms 10 ms 10 ms</td><td>Ab Fedda - Teodal [A] PULL_PR Single ended 2.0 ms undefined 7 </td><td>Ab FredUru Frouts Fill_LPR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 10 ms 10 ms 10 ms -Ab (b) PUT_AR Single ended 10 ms 10 ms 10 ms</td><td>Display Point Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUT_PR Stride ended 20 ms undefined 7 LB 61 PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20</td><td>Date Constraint Constant Constraint Constraint</td><td>B. Feld Law Pitt Single model 20 and official 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab
 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 16 PRL_PR Single model 20 ms undefined 7 Lab 16</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>B. Feld Dava - Ingula 14 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 POT_FR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 164 POT_AR Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Bit Injust 100 uns 10 s 10 s</td><td>Abs. Feedbare - Reput Feedbare - Reput Feedbare - Reput 7 </td><td>B. feld burnstepsis Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl POT FR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. Pot Unit Bit ing</td></thdisplay></td></thdisplay></td></td<></td></td></thf<></td> | Deck Structure Structure 20 and ordered 2 and orderededededededededededededededededede

 | Alls Field Field <thf< td=""><td>All helitou-inputs Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -All</td><td>Bit Polity Styde reduct 20 Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PUT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_Abb Styde reduct 20 ms undefined 7 -Bab 141 POT_Abb<td>Ab. Fedda - Ingular Stagle reddet 20 Stagle reddet 7 Ab. 14) PULL_PR Stagle reddet 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_FPL Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 <td< td=""><td>Bit roll business Stage rodot 20 Bab Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR
Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot</td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Display regular Strage model 20 subdimed 7 B. 14 PULL_PR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 16 PUL_AR Single model 20 ms undefined 7 B. 10 PUL_AR Single model 100 ms 101 ms 101 ms 101 ms</td><td>B. Feld Low Teguta Single model 20 subdimed 7 -ab. 14 PRLL_PR Single model 20 ms subdimed 7 -ab. 14 PRLL_ARI Single model 20 ms subdimed 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRL_ARI Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 100</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>Test Durit PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (b) PUIT_PR Single medied 2.0 ms undefined 7</td><td>Abs Field Line Field Field Line Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PUT_AR Single ended 2.0 ms undefined 7 -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 104 PULM Bit Input 10 ms 10 ms 10 ms</td><td>Ab Fedda - Teodal [A] PULL_PR Single ended 2.0 ms undefined 7 </td><td>Ab FredUru Frouts Fill_LPR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 10 ms 10 ms 10 ms -Ab (b) PUT_AR Single ended 10 ms 10 ms 10 ms</td><td>Display Point Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUT_PR Stride ended 20 ms undefined 7 LB 61 PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20</td><td>Date Constraint Constant Constraint Constraint</td><td>B. Feld Law Pitt Single model 20 and official 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14
PRLL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 16 PRL_PR Single model 20 ms undefined 7 Lab 16</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>B. Feld Dava - Ingula 14 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 POT_FR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 164 POT_AR Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Bit Injust 100 uns 10 s 10 s</td><td>Abs. Feedbare - Reput Feedbare - Reput Feedbare - Reput 7 </td><td>B. feld burnstepsis Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl POT FR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. Pot Unit Bit ing</td></thdisplay></td></thdisplay></td></td<></td></td></thf<>
 | All helitou-inputs Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PULL_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -Alls 14 PUT_PR Single ended 2.0 ms undefined 7 -All

 | Bit Polity Styde reduct 20 Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PULL_PR Styde reduct 20 ms undefined 7 -Bab 141 PUT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_PR Styde reduct 20 ms undefined 7 -Bab 141 POT_Abb Styde reduct 20 ms undefined 7 -Bab 141 POT_Abb <td>Ab. Fedda - Ingular Stagle reddet 20 Stagle reddet 7 Ab. 14) PULL_PR Stagle reddet 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_FPL Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 <td< td=""><td>Bit roll business Stage rodot 20 Bab Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot</td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Display regular Strage model 20 subdimed 7 B. 14 PULL_PR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 16 PUL_AR Single model 20 ms undefined 7 B. 10 PUL_AR Single model 100 ms 101 ms 101 ms 101 ms</td><td>B. Feld Low Teguta Single model 20 subdimed 7 -ab. 14 PRLL_PR Single model 20 ms subdimed 7 -ab. 14 PRLL_ARI Single model 20 ms subdimed 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRL_ARI Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7
 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 100</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>Test Durit PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (b) PUIT_PR Single medied 2.0 ms undefined 7</td><td>Abs Field Line Field Field Line Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PUT_AR Single ended 2.0 ms undefined 7 -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 104 PULM Bit Input 10 ms 10 ms 10 ms</td><td>Ab Fedda - Teodal [A] PULL_PR Single ended 2.0 ms undefined 7 </td><td>Ab FredUru Frouts Fill_LPR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 10 ms 10 ms 10 ms -Ab (b) PUT_AR Single ended 10 ms 10 ms 10 ms</td><td>Display Point Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUT_PR Stride ended 20 ms undefined 7 LB 61 PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20</td><td>Date Constraint Constant Constraint Constraint</td><td>B. Feld Law Pitt Single model 20 and official 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 16 PRL_PR Single model 20 ms undefined 7 Lab 16</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>B. Feld Dava - Ingula 14 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 POT_FR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 164 POT_AR Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Bit Injust
 100 uns 10 s 10 s</td><td>Abs. Feedbare - Reput Feedbare - Reput Feedbare - Reput 7 </td><td>B. feld burnstepsis Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl POT FR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. Pot Unit Bit ing</td></thdisplay></td></thdisplay></td></td<></td> | Ab. Fedda - Ingular Stagle reddet 20 Stagle reddet 7 Ab. 14) PULL_PR Stagle reddet 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 14) PULL_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_FPL Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 Ab. 16) POT_AR Stagle ended 20 ms undefined 7 <td< td=""><td>Bit roll business Stage rodot 20 Bab Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot</td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Stage model 20 subdimed 7 </td><td>July Control Display regular Strage model 20 subdimed 7 B. 14 PULL_PR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20
ms subdimed 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 16 PUL_AR Single model 20 ms undefined 7 B. 10 PUL_AR Single model 100 ms 101 ms 101 ms 101 ms</td><td>B. Feld Low Teguta Single model 20 subdimed 7 -ab. 14 PRLL_PR Single model 20 ms subdimed 7 -ab. 14 PRLL_ARI Single model 20 ms subdimed 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRL_ARI Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 100</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>Test Durit PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (b) PUIT_PR Single medied 2.0 ms undefined 7</td><td>Abs Field Line Field Field Line Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PUT_AR Single ended 2.0 ms undefined 7 -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 104 PULM Bit Input 10 ms 10 ms 10 ms</td><td>Ab Fedda - Teodal [A] PULL_PR Single ended 2.0 ms undefined 7 </td><td>Ab FredUru Frouts Fill_LPR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 10 ms 10 ms 10 ms -Ab (b) PUT_AR Single ended 10 ms 10 ms 10 ms</td><td>Display Point Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUT_PR Stride ended 20 ms undefined 7 LB 61 PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20</td><td>Date Constraint Constant Constraint Constraint</td><td>B. Feld Law Pitt Single model 20 and official 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 16 PRL_PR Single model 20 ms undefined 7 Lab 16</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms
undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>B. Feld Dava - Ingula 14 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 POT_FR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 164 POT_AR Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Bit Injust 100 uns 10 s 10 s</td><td>Abs. Feedbare - Reput Feedbare - Reput Feedbare - Reput 7 </td><td>B. feld burnstepsis Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl POT FR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. Pot Unit Bit ing</td></thdisplay></td></thdisplay></td></td<> | Bit roll business Stage rodot 20 Bab Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LPR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_LAR Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot Stage rodot 20 ms undefined 7 -Bab Hai Pall_Low rodot

 | July Control Stage model 20 subdimed 7

 | July Control Stage model 20 subdimed 7

 | July Control Display regular Strage model 20 subdimed 7 B. 14 PULL_PR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms subdimed 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PULL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 14 PUL_AR Single model 20 ms undefined 7 B. 16 PUL_AR Single model 20 ms undefined 7 B. 10 PUL_AR Single model 100 ms 101 ms 101 ms 101 ms

 | B. Feld Low Teguta Single model 20 subdimed 7 -ab. 14 PRLL_PR Single model 20 ms subdimed 7 -ab. 14 PRLL_ARI Single model 20 ms subdimed 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRLL_ARI Single model 20 ms undefined 7 -ab. 16 PRL_ARI Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 14 PRD_AR Single model 20 ms undefined 7 -ab. 16 PRD_AR Single model 20 ms undefined 7 -ab. 100

 | B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>Test Durit PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (b) PUIT_PR Single medied 2.0 ms undefined 7</td><td>Abs Field Line Field Field Line Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PUT_AR Single ended 2.0 ms undefined 7 -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 104 PULM Bit Input 10 ms 10 ms 10 ms</td><td>Ab Fedda - Teodal [A] PULL_PR Single ended 2.0 ms undefined 7 </td><td>Ab FredUru Frouts Fill_LPR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms
 undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 10 ms 10 ms 10 ms -Ab (b) PUT_AR Single ended 10 ms 10 ms 10 ms</td><td>Display Point Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUT_PR Stride ended 20 ms undefined 7 LB 61 PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20</td><td>Date Constraint Constant Constraint Constraint</td><td>B. Feld Law Pitt Single model 20 and official 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 16 PRL_PR Single model 20 ms undefined 7 Lab 16</td><td>B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>B. Feld Dava - Ingula 14 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 POT_FR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 164 POT_AR Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Bit Injust 100 uns 10 s 10 s</td><td>Abs. Feedbare - Reput Feedbare - Reput Feedbare - Reput 7 </td><td>B. feld burnstepsis Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl POT FR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. Pot Unit Bit ing</td></thdisplay></td></thdisplay>
 | b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL
 | Test Durit PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIL_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (a) PUIT_PR Single medied 2.0 ms undefined 7 db (b) PUIT_PR Single medied 2.0 ms undefined 7

 | Abs Field Line Field Field Line Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULL_PR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PULFR Single ended 2.0 ms undefined 7 -Abs 64 PUT_AR Single ended 2.0 ms undefined 7 -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 164 PUT_AR Single ended 100 ms 10 ms 10 ms -Abs 104 PULM Bit Input 10 ms 10 ms 10 ms
 | Ab Fedda - Teodal [A] PULL_PR Single ended 2.0 ms undefined 7

 | Ab FredUru Frouts Fill_LPR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PULL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUL_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_PR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 2.0 ms undefined 7 -Ab (a) PUT_AR Single ended 10 ms 10 ms 10 ms -Ab (b) PUT_AR Single ended 10 ms 10 ms 10 ms
 | Display Point Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUL_PR Stride ended 20 ms undefined 7 LB 64 PUT_PR Stride ended 20 ms undefined 7 LB 61 PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20 ms undefined 7 LB PUT_PR Stride ended 20
 | Date Constraint Constant Constraint Constraint
 | B. Feld Law Pitt Single model 20 and official 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRLL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 14 PRL_PR Single model 20 ms undefined 7 Lab 16 PRL_PR Single model 20 ms undefined 7 Lab 16 | B. reliab integration Display integratintegration <thdisplay integrat<="" td=""><td>b. feld burn rigods PRLL_PR Single ended
 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL</td><td>B. Feld Dava - Ingula 14 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 POT_FR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 164 POT_AR Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Bit Injust 100 uns 10 s 10 s</td><td>Abs. Feedbare - Reput Feedbare - Reput Feedbare - Reput 7 </td><td>B. feld burnstepsis Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl POT FR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. Pot Unit Bit ing</td></thdisplay> | b. feld burn rigods PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_PR Single ended 2.0 ms undefined 7 db 44 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRLL_Am Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 46 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db 40 PRL_AM Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL Single ended 2.0 ms undefined 7 db PRL_MON_MAL | B. Feld Dava - Ingula 14 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 PULL_PR Single ended 2.0 ms undefined 7 -db 64 POT_FR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 64 POT_AR Single ended 2.0 ms undefined 7 -db 164 POT_AR Single ended 2.0 ms undefined 7 -db 100 UNT DUTO Single ended 2.0 ms undefined 7 -db 100 UNT
 DUTO Bit Injust 100 uns 10 s 10 s | Abs. Feedbare - Reput Feedbare - Reput Feedbare - Reput 7 | B. feld burnstepsis Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl PULL_PR Single ended 2.0 ms undefined 7 dB. Idl POT FR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. Idl POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. POT AR Single ended 2.0 ms undefined 7 dB. Pot Unit Bit ing |
| Lab. (a) PULL_PR Strage ended 2.0 ms undefined 7 Lab. (a) PULL_PR Strage ended 2.0 ms undefined 7 Lab. (a) PULL_PR Strage ended 2.0 ms undefined 7 Lab. (a) PULL_PR Strage ended 2.0 ms undefined 7 Lab. (a) PUT_PR Strage ended 2.0 ms undefined 7 Lab. (a) PUT_PR Strage ended 2.0 ms undefined 7 Lab. (a) PUT_PR Strage ended 2.0 ms undefined 7 Lab. (a) PUT_PR Strage ended 2.0 ms undefined 7 LB. Dota Dota Dota Strage ended 2.0 ms undefined 7 LB. Dota Dota Dota Strage ended 2.0 ms undefined 7 LB. Dota Dota Dota Strage ended 2.0 ms undefined 7 LB. Dota Dota Dota Strage ended 2.0 ms undefined 7 LB. Dota Dota Dota Dota Strage ended <t< td=""><td>dab. (a) P(LL,PR) Single mode 2.0 mr. undefined 7 dab. (a) P(LL,PR) Single mode 2.0 mr. undefined 7 -db. (a) P(LL,PR) Single mode 2.0 mr. undefined 7 -db. (a) P(LL,PR) Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7</td><td>db (a) PULL_PFL Single ended 2.0 ms undefined 7 db (a) PULL_PFL Single ended 2.0 ms undefined 7 db (a) PULL_AL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (b) DOUT Bit Input 2.0 ms undefined 7 <td< td=""><td>db (a) PULL_PR Single ended 20 m undefined 7 db (a) PULL_PR Single ended 20 m undefined 7 db (a) PULL_PR Single ended 20 m undefined 7 db (a) PULL_PR Single ended 20 m undefined 7 db (a) POT_PR Single ended 20 m undefined 7 db (a) POT_PR Single ended 20 m undefined 7 db (a) POT_AL Single ended 20 m undefined 7 db (b) POT_AL Single ended 20 m undefined 7 db (b) POT_AL Single ended 20 m undefined 7 db (b) POT_AL Single ended 20 m undefined 7 db (b) (b) No No 10 m 10 m db (b)</td><td>Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PR Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PL Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PL Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PL Strage ended 2.0 ms undefined 7 LB. [b] PUT_AL Strage ended 2.0 ms undefined 7 LB. [b] [b] [b] [b] [b] [b] [b] [b] LB. [b] [b] [b] [b] [b] [b]</td><td>Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PL Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PL Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 10.0 ms 10 s 10 s</td><td>Lab. (a) PULL_PR Strage ended 20 audimed 7 Lab. (a) PULL_PL Strage ended 20 audimed 7 Lab. (a) PULL_PL Strage ended 20 audimed 7 Lab. (a) PULL_PL Strage ended 20 audimed 7 Lab. (a) POL_PL Strage ended 20 audimed 7 Lab. POL_PL Strage ended 20 audime</td><td>Lab. [Al.] PAL_PR Single ended 20 undefined 7 Lab. [Al.] PAL_PR Single ended 20 mudefined 7 Lab. [Al.] PAL_PR Single ended 20 mudefined 7 Lab. [Al.] PAL_PR Single ended 20 mudefined 7 <td< td=""><td>Lab. [a] [a] [a] [b] [c] [c]</td></td<></td></td<></td></t<> <td>Lab. [A] PULL_PR Single endod 20 Lab. [A] POT_PR Single endod 20 Lab. [A] POT_AL Single endod 20 Lab. [A] POT_AL Single endod 20 undefined 7 Lab. [A] POT_AL Single endod 20 ms< undefined</td> 7 Lab. [A] POT_AL Single endod 20 10 is 10 is

 | dab. (a) P(LL,PR) Single mode 2.0 mr. undefined 7 dab. (a) P(LL,PR) Single mode 2.0 mr. undefined 7 -db. (a) P(LL,PR) Single mode 2.0 mr. undefined 7 -db. (a) P(LL,PR) Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined
 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7 -db. (a) POT.PR Single mode 2.0 mr. undefined 7

 | db (a) PULL_PFL Single ended 2.0 ms undefined 7 db (a) PULL_PFL Single ended 2.0 ms undefined 7 db (a) PULL_AL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (b) DOUT Bit Input 2.0 ms undefined 7 <td< td=""><td>db (a) PULL_PR Single ended 20 m undefined 7 db (a) PULL_PR Single ended 20 m undefined 7 db (a) PULL_PR Single ended 20 m undefined 7 db (a) PULL_PR Single ended 20 m undefined 7 db (a) POT_PR Single ended 20 m undefined 7 db (a) POT_PR Single ended 20 m undefined 7 db (a) POT_AL Single ended 20 m undefined 7 db (b) POT_AL Single ended 20 m undefined 7 db (b) POT_AL Single ended 20 m undefined 7 db (b) POT_AL Single ended 20 m undefined 7 db (b) (b) No No 10 m 10 m db (b)</td><td>Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PR Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PL Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PL Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PL Strage ended 2.0 ms undefined 7 LB. [b] PUT_AL
Strage ended 2.0 ms undefined 7 LB. [b] [b] [b] [b] [b] [b] [b] [b] LB. [b] [b] [b] [b] [b] [b]</td><td>Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PL Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PL Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 10.0 ms 10 s 10 s</td><td>Lab. (a) PULL_PR Strage ended 20 audimed 7 Lab. (a) PULL_PL Strage ended 20 audimed 7 Lab. (a) PULL_PL Strage ended 20 audimed 7 Lab. (a) PULL_PL Strage ended 20 audimed 7 Lab. (a) POL_PL Strage ended 20 audimed 7 Lab. POL_PL Strage ended 20 audime</td><td>Lab. [Al.] PAL_PR Single ended 20 undefined 7 Lab. [Al.] PAL_PR Single ended 20 mudefined 7 Lab. [Al.] PAL_PR Single ended 20 mudefined 7 Lab. [Al.] PAL_PR Single ended 20 mudefined 7 <td< td=""><td>Lab. [a] [a] [a] [b] [c] [c]</td></td<></td></td<>

 | db (a) PULL_PR Single ended 20 m undefined 7 db (a) PULL_PR Single ended 20 m undefined 7 db (a) PULL_PR Single ended 20 m undefined 7 db (a) PULL_PR Single ended 20 m undefined 7 db (a) POT_PR Single ended 20 m undefined 7 db (a) POT_PR Single ended 20 m undefined 7 db (a) POT_AL Single ended 20 m undefined 7 db (b) POT_AL Single ended 20 m undefined 7 db (b) POT_AL Single ended 20 m undefined 7 db (b) POT_AL Single ended 20 m undefined 7 db (b) (b) No No 10 m 10 m db (b)

 | Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PR Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PL Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PL Strage ended 2.0 ms undefined 7 Lab. [a] PUT_PL Strage ended 2.0 ms undefined 7 LB. [b] PUT_AL Strage ended 2.0 ms undefined 7 LB. [b] [b] [b] [b] [b] [b] [b] [b] LB. [b] [b] [b] [b] [b] [b]

 | Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PR Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PL Strage ended 2.0 ms undefined 7 Lab. [a] PULL_PL Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 2.0 ms undefined 7 Lab. [a] PULPR Strage ended 10.0 ms 10 s 10 s

 | Lab. (a) PULL_PR Strage ended 20 audimed 7 Lab. (a) PULL_PL Strage ended 20 audimed 7 Lab. (a) PULL_PL Strage ended 20 audimed 7 Lab. (a) PULL_PL Strage ended 20 audimed 7 Lab. (a) POL_PL Strage ended 20 audimed 7 Lab. POL_PL Strage ended 20 audime

 | Lab. [Al.] PAL_PR Single ended 20 undefined 7 Lab. [Al.] PAL_PR Single ended 20 mudefined 7 Lab. [Al.] PAL_PR Single ended 20 mudefined 7 Lab. [Al.] PAL_PR Single ended 20 mudefined 7 <td< td=""><td>Lab. [a] [a] [a] [b] [c] [c]</td></td<>

 | Lab. [a] [a] [a] [b] [c]

 | Lab. [A] PULL_PR Single endod 20 Lab. [A] POT_PR Single endod 20 Lab. [A] POT_AL Single endod 20 Lab. [A] POT_AL Single endod 20 undefined 7 Lab. [A] POT_AL Single endod 20 ms< undefined

 | Lib. (a) PULL_PR Single endod 20 Lib. (a) POT_PR Single endod 20 -db. (a) POT_AL Single endod 20 undefined 7 -db. (b) POT_AL Single endod 20 undefined 7 -db. (c) POT_AL Single endod 20 10 10

 | Lib. (a) PULL_PR Single endod 20 Lib. AL (a) PULL_PR Single endod 20 Lib. (a) PULL_PL Single endod 20 undefined 7 Lib. (a) PULL_PL Single endod 20 undefined 7 Lib. (a) PULL_PL Single endod 20 undefined 7 Lib. (a) PUT_PL Single endod 20 undefined 7 Lib. (b) POT_FL Single endod 20 undefined 7 Lib. (b) POT_FL Single endod 20 undefined 7 Lib. (b) POT_LAL Single endod 20 undefined 7 Lib. (b) POT_LAL Single endod 20 undefined 7 Lib. POT_LAL Single endod 20 undefined 7 Lib. POT_LAL Single endod 20 undefined

 | Lib. (a) PULL_PR Single endod 20 Lib. AL (a) PULL_PR Single endod 20 Lib. (a) PULL_PL Single endod 20 undefined 7 Lib. (a) PULL_PL Single endod 20 undefined 7 Lib. (a) PULL_PL Single endod 20 undefined 7 Lib. (a) POT_FL Single endod 20 undefined 7 Lib. (a) POT_FL Single endod 20 undefined 7 Lib. (b) POT_FL Single endod 20 undefined 7 Lib. (b) POT_AL Single endod 20 undefined 7 Lib. (b) POT_AL Single endod 20 undefined 7 Lib. (b) DINUT B) Single endod 20 undefined 7 Lib. (b) DINUT B)<

 | dL (a) PULL_PR Single reduct 20 undefined 7 dL (a) PULL_PR Single reduct 20 m undefined 7 dL (a) PULL_PR Single reduct 20 m undefined 7 dL (a) PULL_PR Single reduct 20 m undefined 7 dL (a) PULL_PR Single reduct 20 m undefined 7 -dL (a) PUT_PR Single reduct 20 m undefined 7 -dL (a) PUT_PR Single reduct 20 m undefined 7 -dL (a) PUT_PR Single reduct 20 m undefined 7 -dL (b) PUT_PR Single reduct 20 m
undefined 7 -dL (b) PUT_PR Single reduct 20 m undefined 7 -dL (b) (b) (b) Single reduct 20 m undefined 7
 | db. (a) PULL_PR Single ended 20 ms undefined 7 db. (a) PULL_PL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended
 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (b) PUT_AL Single ended 20 ms undefined 7 db. (b) (b) (b) Single ended 20 ms undefined 7
 | db. (a) PILL_PR Single ended 20 m undefined 7 db. (a) PILL_PR Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended 20 m undefined 7 -db. (a) PIDT_SR. Single ended 20 m undefined 7 -db. (a) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 <tr< td=""><td>db. (a) PULL_PR Single ended 2.0 m undefined 7 db. (b) PUL_PR Single ended 2.0 m undefined 7 db. (c) PUL_PR Single ended 2.0 m undefined 7 db. (c) PUL_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended
 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 <t< td=""><td>Lab. [a] PULL_PR Single endod 20 undefined 2 Lab. [a] PULL_PR Single endod 20 undefined 7 Lab. [a] PULL_PL Single endod 20 undefined 7 Lab. [a] PULL_PL Single endod 20 undefined 7 Lab. [a] PULL_PL Single endod 20 undefined 7 Lab. [a] PUT_PL Single endod 20 undefined 7 Lab.</td><td>Lab. [A] PALL_PR Single model 20 Lab. [A] PALL_PR Single model 20 muldined 7 Lab. [A] PATL_PR Single model 20 muldined 7 Lab. [A] POT_PL Single model 20 muldined 7 Lab. [A] POT_PL Single model 20 muldined 7 Lab. [A] POT_AL Sing</td><td>Lab. [a] PULL_PR Straße mödel 2.0 undefined 7 Lab. [a] POT_PR Straße mödel 2.0 undefined 7 Lab. [a] POT_PL Straße mödel 2.0 undefined 7 Lab. [a] POT_PL Straße mödel 2.0 undefined 7 Lab. [a] POT_PL Straße mödel 2.0 undefined 7 Lab. [a] POT_AL Straße mödel 2.0 undefined 7 Lab. [a] POT_AL Straße mödel 2.0 undefined 7 Lab. [a] POT_AL Straße mödel 2.0 undefined 7 Lab.<!--</td--><td>Lab. Init [11, 19] Strophenold 20 ms. undefined 7 Lab. Init [12, 14] Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined</td><td>Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 LB. Distort Single ended 20 ms undefined 7 LB. Distort Distort Single ended 20 ms undefined 7 LB. Distort Distort Distort Distort Distort Distort</td><td>dL (a) PULL_PR Single endod 20 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 -dL (a) PULL_PL Single endod 20 ms undefined 7 -dL (a) PUT_PL Single endod 20 ms undefined 7 -dL (a) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) (b)<!--</td--><td>db. (a) PULL_PR Single ended 20 ms undefined 7 db. (a) PULL_PL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (b) PUT_AL Single ended 20 ms undefined 7 db. (b) (b) (b) Single ended 20 ms undefined 7</td><td>db. (a) PILL_PR Single ended 20 m undefined 7 db. (a) PILL_PR Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended 20 m undefined 7 -db. (a) PIDT_SR. Single ended 20 m undefined 7 -db. (a) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 <tr< td=""><td>dLb (a) PULL_PR Single endod 20 dLb (a) PULL_PR Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) DUTM Blingle<td>Lab. [A] PALL_PR Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A]
POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 10.0 undefined 7</td><td>db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_AL Single endod 20 undefined 7 db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PL Single endod 20 undefined 7 -db (a) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db POT_AR</td></td></tr<></td></td></td></t<></td></tr<> | db. (a) PULL_PR Single ended 2.0 m undefined 7 db. (b) PUL_PR Single ended 2.0 m undefined 7 db. (c) PUL_PR Single ended 2.0 m undefined 7 db. (c) PUL_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 db. (c) POT_PR Single ended 2.0 m undefined 7 <t< td=""><td>Lab. [a] PULL_PR Single endod 20 undefined 2 Lab. [a] PULL_PR Single endod 20 undefined 7 Lab. [a] PULL_PL Single endod 20 undefined 7 Lab. [a] PULL_PL Single endod 20 undefined 7 Lab. [a] PULL_PL Single endod 20 undefined 7 Lab. [a] PUT_PL Single endod 20 undefined 7 Lab.</td><td>Lab. [A] PALL_PR Single model 20 Lab. [A] PALL_PR Single model 20 muldined 7 Lab. [A] PATL_PR Single model 20 muldined 7 Lab. [A] POT_PL Single model 20 muldined 7 Lab. [A] POT_PL Single model 20 muldined 7 Lab. [A] POT_AL Sing</td><td>Lab. [a] PULL_PR Straße mödel 2.0 undefined 7 Lab. [a] POT_PR Straße mödel 2.0 undefined 7 Lab. [a] POT_PL Straße mödel 2.0 undefined 7 Lab. [a] POT_PL Straße mödel 2.0 undefined 7 Lab. [a] POT_PL Straße mödel 2.0 undefined 7 Lab. [a] POT_AL Straße mödel 2.0 undefined 7 Lab. [a] POT_AL Straße mödel 2.0 undefined 7 Lab. [a] POT_AL Straße mödel 2.0 undefined 7 Lab.<!--</td--><td>Lab. Init [11, 19] Strophenold 20 ms. undefined 7 Lab. Init [12, 14] Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined</td><td>Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 LB. Distort Single ended 20 ms undefined 7 LB. Distort Distort Single ended 20 ms undefined 7 LB. Distort Distort Distort Distort Distort Distort</td><td>dL (a) PULL_PR Single endod 20 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 -dL (a) PULL_PL Single endod 20 ms undefined 7 -dL (a) PUT_PL Single endod 20 ms undefined 7 -dL (a) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) (b)<!--</td--><td>db. (a) PULL_PR Single ended 20 ms undefined 7 db. (a) PULL_PL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (b) PUT_AL Single ended 20 ms undefined 7 db. (b) (b) (b) Single ended 20 ms undefined 7</td><td>db. (a) PILL_PR Single ended 20 m undefined 7 db. (a) PILL_PR Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended
20 m undefined 7 -db. (a) PIDT_SR. Single ended 20 m undefined 7 -db. (a) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 <tr< td=""><td>dLb (a) PULL_PR Single endod 20 dLb (a) PULL_PR Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) DUTM Blingle<td>Lab. [A] PALL_PR Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 10.0 undefined 7</td><td>db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_AL Single endod 20 undefined 7 db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PL Single endod 20 undefined 7 -db (a) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db POT_AR</td></td></tr<></td></td></td></t<> | Lab. [a] PULL_PR Single endod 20 undefined 2 Lab. [a] PULL_PR Single endod 20 undefined 7 Lab. [a] PULL_PL Single endod 20 undefined 7 Lab. [a] PULL_PL Single endod 20 undefined 7 Lab. [a] PULL_PL Single endod 20 undefined 7 Lab. [a] PUT_PL Single endod 20 undefined 7 Lab.

 | Lab. [A] PALL_PR Single model 20 Lab. [A] PALL_PR Single model 20 muldined 7 Lab. [A] PATL_PR Single model 20 muldined 7 Lab. [A] POT_PL Single model 20 muldined 7 Lab. [A] POT_PL Single model 20 muldined 7 Lab. [A] POT_AL Sing
 | Lab. [a] PULL_PR Straße mödel 2.0 undefined 7 Lab. [a] POT_PR Straße mödel 2.0 undefined 7 Lab. [a] POT_PL Straße mödel 2.0 undefined 7 Lab. [a] POT_PL Straße mödel 2.0 undefined 7 Lab. [a] POT_PL Straße mödel 2.0 undefined 7 Lab. [a] POT_AL Straße mödel 2.0 undefined 7 Lab. [a] POT_AL Straße mödel 2.0 undefined 7 Lab. [a] POT_AL Straße mödel 2.0 undefined 7 Lab. </td <td>Lab. Init [11, 19] Strophenold 20 ms. undefined 7 Lab. Init [12, 14] Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined</td> <td>Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 LB. Distort Single ended 20 ms undefined 7 LB. Distort Distort Single ended 20 ms undefined 7 LB. Distort Distort Distort Distort Distort Distort</td> <td>dL (a) PULL_PR Single endod 20 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 -dL (a) PULL_PL Single endod 20 ms undefined 7 -dL (a) PUT_PL Single endod 20 ms undefined 7 -dL (a) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) (b)<!--</td--><td>db. (a) PULL_PR Single ended 20 ms undefined 7 db. (a) PULL_PL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (b) PUT_AL Single ended 20 ms undefined 7 db. (b) (b) (b) Single ended 20 ms undefined 7</td><td>db. (a) PILL_PR Single ended 20 m undefined 7 db. (a) PILL_PR Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended 20 m undefined 7 -db. (a) PIDT_SR. Single ended 20 m
 undefined 7 -db. (a) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 <tr< td=""><td>dLb (a) PULL_PR Single endod 20 dLb (a) PULL_PR Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) DUTM Blingle<td>Lab. [A] PALL_PR Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 10.0 undefined 7</td><td>db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_AL Single endod 20 undefined 7 db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PL Single endod 20 undefined 7 -db (a) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db POT_AR</td></td></tr<></td></td> | Lab. Init [11, 19] Strophenold 20 ms. undefined 7 Lab. Init [12, 14] Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVLL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined 7 Lab. Init [14] FVL_PL Strophenold 20 ms. undefined
 | Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PULL_PR Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 Lab. [a] PUT_PL Single ended 20 ms undefined 7 LB. Distort Single ended 20 ms undefined 7 LB. Distort Distort Single ended 20 ms undefined 7 LB. Distort Distort Distort Distort Distort Distort
 | dL (a) PULL_PR Single endod 20 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 dL (a) PULL_PL Single endod 20 ms undefined 7 -dL (a) PULL_PL Single endod 20 ms undefined 7 -dL (a) PUT_PL Single endod 20 ms undefined 7 -dL (a) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) PUT_PL Single endod 20 ms undefined 7 -dL (b) (b) </td <td>db. (a) PULL_PR Single ended 20 ms undefined 7 db. (a) PULL_PL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (b) PUT_AL Single ended 20 ms undefined 7 db. (b) (b) (b) Single ended 20 ms undefined 7</td> <td>db. (a) PILL_PR Single ended 20 m undefined 7 db. (a) PILL_PR Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended 20 m undefined 7 -db. (a) PIDT_SR. Single ended 20 m undefined 7 -db. (a) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 <tr< td=""><td>dLb (a) PULL_PR Single endod 20 dLb (a) PULL_PR Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) DUTM Blingle<td>Lab. [A] PALL_PR Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL
Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 10.0 undefined 7</td><td>db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_AL Single endod 20 undefined 7 db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PL Single endod 20 undefined 7 -db (a) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db POT_AR</td></td></tr<></td> | db. (a) PULL_PR Single ended 20 ms undefined 7 db. (a) PULL_PL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PULL_APL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (a) PUT_AL Single ended 20 ms undefined 7 db. (b) PUT_AL Single ended 20 ms undefined 7 db. (b) (b) (b) Single ended 20 ms undefined 7 | db. (a) PILL_PR Single ended 20 m undefined 7 db. (a) PILL_PR Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PILL_AL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended 20 m undefined 7 -db. (a) PIDT_PL Single ended 20 m undefined 7 -db. (a) PIDT_SR. Single ended 20 m undefined 7 -db. (a) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 -db. (b) PIDT_SR. Single ended 20 m undefined 7 <tr< td=""><td>dLb (a) PULL_PR Single endod 20 dLb (a) PULL_PR Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod
20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) DUTM Blingle<td>Lab. [A] PALL_PR Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 10.0 undefined 7</td><td>db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_AL Single endod 20 undefined 7 db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PL Single endod 20 undefined 7 -db (a) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db POT_AR</td></td></tr<> | dLb (a) PULL_PR Single endod 20 dLb (a) PULL_PR Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) PULL_PL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_FL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) POT_AL Single endod 20 m undefined 7 -dLb (a) DUTM Blingle <td>Lab. [A] PALL_PR Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 10.0 undefined 7</td> <td>db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_AL Single endod 20 undefined 7 db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PL Single endod 20 undefined 7 -db (a) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db POT_AR</td> | Lab. [A] PALL_PR Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_PL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 2.0 undefined 7 Lab. [A] POT_AL Stradge ended 10.0 undefined 7 | db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_PR Single endod 20 undefined 7 db (a) PULL_AL Single endod 20 undefined 7 db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PR Single endod 20 undefined 7 -db (a) POT_PL Single endod 20 undefined 7 -db (a) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db (b) POT_AR Single endod 20 undefined 7 -db POT_AR |
| data 18 PULL PA Straße ende 2.0 ms underned 7 data 18 PULL PA Straße ende 2.0 ms underned 7 data 18 PULL PA Straße ende 2.0 ms underned 7 data 18 PULT PA Straße ende 2.0 ms underned 7 data 18 PUT PA Straße ende 2.0 ms underned 7 data 19 PUT AR Straße ende 2.0 ms underned 7 data 19 PUT AR Straße ende 2.0 ms underned 7 data 19 PUT AR Straße ende 2.0 ms underned 7 data 10 port PUT AR Straße ende 2.0 ms underned 7 data 10 port PUT AR Straße ende 2.0 ms underned 7 data 10 port PUT AR Straße ende 10 ms 10 ms 10 ms

 | udia (a) PULL_PL Single model 2.0 mm undefined 7 udia (a) PULL_PL Single model 2.0 mm undefined 7 udia (a) PULL_PL Single model 2.0 mm undefined 7 udia (a) PULL_PL Single model 2.0 mm undefined 7 udia (a) POT_PL Single model 2.0 mm undefined 7 udia (a) POT_PL Single model 2.0 mm undefined 7 udia (a) POT_PL Single model 2.0 mm undefined 7 udia (a) POT_AL Single model 2.0 mm undefined 7 udia (a) POT_AL Single model 2.0 mm undefined 7 udia (a) POT_AL Single model 2.0 mm undefined 7 udia (b) (b) (b) (b) POT_AL Single model <td< td=""><td>db (a) PULL_PL Single ended 2.0 ms undefined 7 db (a) PULL_PL Single ended 2.0 ms undefined 7 db (a) PULL_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_AR Single ended 2.0 ms undefined 7 db (a) POT_AR Single ended 2.0 ms undefined 7 db (a) POT_AR Single ended 2.0 ms undefined 7 db (b) POT_AR Single ended 2.0 ms undefined 7 db (b) POT_AR Single ended 2.0 ms undefined 7 db (b) POT_AR Single ended 10.0 ms 10.1 ms db</td><td>dda (a) PULL_PL Single ended 2.0 ms undefined 7 dda (a) PULL_AT Single ended 2.0 ms undefined 7 dda (a) PULL_AT Single ended 2.0 ms undefined 7 dda (a) PULL_AT Single ended 2.0 ms undefined 7 dda (a) PUT_AT Single ended 2.0 ms undefined 7 dda (a) PUT_AT Single ended 2.0 ms undefined 7 dda (a) PUT_AR Single ended 2.0 ms undefined 7 dda (a) PUT_AR Single ended 2.0 ms undefined 7 dda (b) PUT_AR Single ended 2.0 ms undefined 7 dda (b) PUT_AR Single ended 2.0 ms undefined 7 dda (b) PUT Single ended 2.0 ms undefined 7</td><td>Lab. Bit PULL PL
Delta Stringe ended
PULL PL
PL Stringe ended
PULL PL
PL Pull
PULL PL Pull PULL P</td><td>Lab. (a) PULL PL Stright ended 20 undefined 7 Lab. (a) PULL PL Stright ended 20 muldefined 7 Lab. (a) PULT PL Stright ended 20 muldefined 7 Lab. (a) PULT PL Stright ended 20 muldefined 7 Lab. (a) PULT PL Stright ended 20 muldefined 7 Lab. (a) PULT PL Stright ended 20 muldefined 7 Lab. (a) PULT PL Stright ended 20 muldefined 7 Lab. (a) PULT_AL Stright ended 20 muldefined 7 Lab. (a) PULT_AL Stright ended 20 muldefined 7 Lab. (a) PULT_AL Stright ended 20 muldefined 7 Lab. (b) PULT_AL Stright ended 20 muldefined 7</td><td>ubb Iai PolL_PI Strage ender 20 outlined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb PolL_PI Strage ender 20 ms undefined 7 ubb PolL_PI Strage ender 20 ms undefined 7 ubb PolL_PI</td><td>Lab. [a] PULL, PL Single ended 20 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 100 undefined 7 Lab. [b] PULL, PL Single ended 100 mudefined 7 Lab. [b] PULL, PL Single ended 100 mudefined 7 Lab. [b] <td< td=""><td>Lall Isia PULL_PL Straph ended 20 undefined 7 Lall INIT_PL Straph ended 20 undefined 7 Lall DUIN DUIN Straph ended 20 undefined 7 Lall DUIN DUIN Straph ended 20 undefined 7 Lall <</td><td>Lab. [a] [A]L., PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a]
 PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [b] PULL, PL. Single ended 100 0 10 7 Lab. [b] PULL, PL. Single ended 100 0 10 10 10 10 10 10 10 10 10 10 10<!--</td--><td>ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7</td><td>Ladia Isia PULL_PA Straph ended 20 undefined 7 Ladia Isia POT_PA Straph ended 20 undefined 7 Ladia Isia POT_PA Straph ended 20 undefined 7 Ladia Isia POT_AR Straph ended 20 undefined 7</td><td>Ladia Isia PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PUL_PA Strage endod 20 ms undefined 7 -dia 10 PUL_PA Strage endod 20 ms undefined 7 -dia PUL PUL PUL</td></td></td<><td>addla (a) PULL_PL Single ended 20 m undefined 7 addla (a) PULL_PL Single ended 20 m undefined 7 -addla (a) PULL_PL Single ended 20 m undefined 7 -addla (a) POT_FPL Single ended 20 m undefined 7 -addla (a) POT_FPL Single ended 20 m undefined 7 -addla (a) POT_AR Single ended 20 m undefined 7 -addla (a) POT_AR Single ended 20 ms undefined 7 -addla (a) POT_AR Single ended 20 ms undefined 7 -addla (b) POT_AR Single ended 20 ms undefined 7 -addla (b) POT_AR Single ended 20 ms undefined 7 -addla (b) DUT02 B Floput 10 s 10 s 10 s <td>data (a) PULL_PL Single ended 20 m undefined 7 data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PUT_PL Single ended 20 m undefined 7 -data (a) POT_PL Single ended 20 m undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (b)<pot_ar< td=""> Single ended 20 ms undefined 7 -data (b) POT_AR Single ended 20 ms undefined 7 -data (b) (c) (c) (c) (c) (c) (c) 10 m</pot_ar<></td><td>dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PUT_PL Single ended 20 m undefined 7 -db (a) POT_PL Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 10 m 10 m 10 m 10 m 10 m<!--</td--><td>db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULT_PL Single ended 2.0 m undefined 7 db (a) PUT_PL Single ended 2.0 m undefined 7 db (a) PUT_PL Single ended 2.0 m undefined 7 db (b) PUT_PL Single ended 2.0 m undefined 7 db (b) PUT_PL Single ended 2.0 m undefined 7 db (b) DUT Bit hout 10 m 10 m 10 m db (b) DUT Bit hout 10 m 10 m 10 m 10 m 10 m</td><td>Lab. [a] PULL_PL Single ended 20 Lab. [a] PULL_PL Single ended 20 undefined 7 Lab. [a] PUL_PL Single ended 100 m 10 s Lab. [a] PUL_PL Single ended 10 s 10 s 10 s</td><td>Lab. [a] PULL_PL Single endod 20 Lab. PULL_PL Single endod 20 undefined 7 Lab. PULL_PL Single endod 20 undefined 7 Lab. PULL_PL Single endod 20 mundefined 7 Lab. Divitit</td><td>Lab. [a] PULL, PL Straße endel 20 undefined 7 Lab. [a] PULL, PL Straße endel 20 ms undefined 7 Lab. [a] PULL, PL Straße endel 20 ms undefined 7 Lab. [a] PULT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7</td><td>Lab. [a] PULL, PL Stright ended 20 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULL, PL Stright ended
 20 Multimed 7 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, AL Stright ended 20 Multimed 7 Lab. [a] PUT, AL Stright ended 20 multimed 7 Lab. [b] PUL, PL Stright ended 20 multimed 7 Lab. [b] PUT, AL Stright ended 10 multimed 7 Lab. Distright ended 10 multimed<</td><td>Lab. 1al PULL, PL Single ended 20 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 10 windling 7 Lab. 1al PULL, PL Sin</td><td>adia (a) PULL_PL Single ended 20 adia (a) PULL_PL Single ended 20 m undefined 7 adia (a) PULL_PL Single ended 20 m undefined 7 -abit (a) POT_PL Single ended 20 m undefined 7 -abit (a) POT_PL Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (b) POT_AR Single ended 20 m undefined 7 -abit (b) (c) Single ended 20 m undefined 7 -abit (c) (c) (c)<td>data (a) PULL_PL Single ended 20 m undefined 7 data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PUT_PL Single ended 20 m undefined 7 -data (a) POT_PL Single ended 20 m undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (b)<pot_ar< td=""> Single ended 20 ms undefined 7 -data (b) POT_AR Single ended 20 ms undefined 7 -data (b) (c) (c) (c) (c) (c) (c) 10 m</pot_ar<></td><td>dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PUT_PL Single ended 20 m undefined 7 -db (a) POT_PL Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 10 m 10 m 10 m 10 m 10 m<!--</td--><td>data (a) PULL_PL Single ended 20 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data <td< td=""><td>Lab. [a] PULL_PL Stright ended 20 Lab. [a] PULL_PL Stright ended 20 muddlined 7 Lab. [a] PU</td><td>dda (a) PULL_PL Single ended 20 -dda (a) PULL_PL Single ended 20 -dda (a) PULL_AR Single ended 20 -dda (a) PUT_AR Single ended 20 -dda (b) PUT_AR Single ended 20 -dda (c) PUT_AR PUT_AR PUT_AR</td></td<></td></td></td></td></td></td></td<>
 | db (a) PULL_PL Single ended 2.0 ms undefined 7 db (a) PULL_PL Single ended 2.0 ms undefined 7 db (a) PULL_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_PL Single ended 2.0 ms undefined 7 db (a) POT_AR Single ended 2.0 ms undefined 7 db (a) POT_AR Single ended 2.0 ms undefined 7 db (a) POT_AR Single ended 2.0 ms undefined 7 db (b) POT_AR Single ended 2.0 ms undefined 7 db (b) POT_AR Single ended 2.0 ms undefined 7 db (b) POT_AR Single ended 10.0 ms 10.1 ms db

 | dda (a) PULL_PL Single ended 2.0 ms undefined 7 dda (a) PULL_AT Single ended 2.0 ms undefined 7 dda (a) PULL_AT Single ended 2.0 ms undefined 7 dda (a) PULL_AT Single ended 2.0 ms undefined 7 dda (a) PUT_AT Single ended 2.0 ms undefined 7 dda (a) PUT_AT Single ended 2.0 ms undefined 7 dda (a) PUT_AR Single ended 2.0 ms undefined 7 dda (a) PUT_AR Single ended 2.0 ms undefined 7 dda (b) PUT_AR Single ended 2.0 ms undefined 7 dda (b) PUT_AR Single ended 2.0 ms undefined 7 dda (b) PUT Single ended 2.0 ms undefined 7

 | Lab. Bit PULL PL
Delta Stringe ended
PULL PL
PL Stringe ended
PULL PL
PL Pull
PULL PL
Pull PULL P

 | Lab. (a) PULL PL Stright ended 20 undefined 7 Lab. (a) PULL PL Stright ended 20 muldefined 7 Lab. (a) PULT PL Stright ended 20 muldefined 7 Lab. (a) PULT PL Stright ended 20 muldefined 7 Lab. (a) PULT PL Stright ended 20 muldefined 7 Lab. (a) PULT PL Stright ended 20 muldefined 7 Lab. (a) PULT PL Stright ended 20 muldefined 7 Lab. (a) PULT_AL Stright ended 20 muldefined 7 Lab. (a) PULT_AL Stright ended 20 muldefined 7 Lab. (a) PULT_AL Stright ended 20 muldefined 7 Lab. (b) PULT_AL Stright ended 20 muldefined 7

 | ubb Iai PolL_PI Strage ender 20 outlined 7 ubb Iai PolL_PI Strage ender 20 ms undefined
7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb Iai PolL_PI Strage ender 20 ms undefined 7 ubb PolL_PI Strage ender 20 ms undefined 7 ubb PolL_PI Strage ender 20 ms undefined 7 ubb PolL_PI

 | Lab. [a] PULL, PL Single ended 20 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 20 undefined 7 Lab. [a] PULL, PL Single ended 100 undefined 7 Lab. [b] PULL, PL Single ended 100 mudefined 7 Lab. [b] PULL, PL Single ended 100 mudefined 7 Lab. [b] <td< td=""><td>Lall Isia PULL_PL Straph ended 20 undefined 7 Lall INIT_PL Straph ended 20 undefined 7 Lall DUIN DUIN Straph ended 20 undefined 7 Lall DUIN DUIN Straph ended 20 undefined 7 Lall <</td><td>Lab. [a] [A]L., PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [b] PULL, PL. Single ended 100 0 10 7 Lab. [b] PULL, PL. Single ended 100 0 10 10 10 10 10 10 10 10 10 10 10<!--</td--><td>ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7</td><td>Ladia Isia PULL_PA Straph ended 20 undefined 7 Ladia Isia POT_PA Straph ended 20 undefined 7 Ladia Isia POT_PA Straph ended 20 undefined 7 Ladia Isia POT_AR Straph ended 20 undefined 7</td><td>Ladia Isia PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PUL_PA Strage endod 20 ms undefined 7 -dia 10 PUL_PA Strage endod 20 ms undefined 7 -dia PUL PUL PUL</td></td></td<> <td>addla (a) PULL_PL Single ended 20 m undefined 7 addla (a) PULL_PL Single ended 20 m undefined 7 -addla (a) PULL_PL Single ended 20 m undefined 7 -addla (a) POT_FPL Single ended 20 m undefined 7 -addla (a) POT_FPL Single ended 20 m undefined 7 -addla (a) POT_AR Single ended 20 m undefined 7 -addla (a) POT_AR Single ended 20 ms undefined 7 -addla (a) POT_AR Single ended 20 ms undefined 7 -addla (b) POT_AR Single ended 20 ms undefined 7 -addla (b) POT_AR Single ended 20 ms undefined 7 -addla (b) DUT02 B Floput 10 s 10 s 10 s <td>data (a) PULL_PL Single ended 20 m undefined 7 data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PUT_PL Single ended 20 m undefined 7 -data (a) POT_PL Single ended 20 m undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (b)<pot_ar< td=""> Single ended 20 ms undefined 7 -data (b) POT_AR Single ended 20 ms undefined 7 -data (b) (c) (c) (c) (c) (c) (c) 10 m</pot_ar<></td><td>dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PUT_PL Single ended 20 m undefined 7 -db (a) POT_PL Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b)
POT_AR Single ended 10 m 10 m 10 m 10 m 10 m<!--</td--><td>db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULT_PL Single ended 2.0 m undefined 7 db (a) PUT_PL Single ended 2.0 m undefined 7 db (a) PUT_PL Single ended 2.0 m undefined 7 db (b) PUT_PL Single ended 2.0 m undefined 7 db (b) PUT_PL Single ended 2.0 m undefined 7 db (b) DUT Bit hout 10 m 10 m 10 m db (b) DUT Bit hout 10 m 10 m 10 m 10 m 10 m</td><td>Lab. [a] PULL_PL Single ended 20 Lab. [a] PULL_PL Single ended 20 undefined 7 Lab. [a] PUL_PL Single ended 100 m 10 s Lab. [a] PUL_PL Single ended 10 s 10 s 10 s</td><td>Lab. [a] PULL_PL Single endod 20 Lab. PULL_PL Single endod 20 undefined 7 Lab. PULL_PL Single endod 20 undefined 7 Lab. PULL_PL Single endod 20 mundefined 7 Lab. Divitit</td><td>Lab. [a] PULL, PL Straße endel 20 undefined 7 Lab. [a] PULL, PL Straße endel 20 ms undefined 7 Lab. [a] PULL, PL Straße endel 20 ms undefined 7 Lab. [a] PULT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7</td><td>Lab. [a] PULL, PL Stright ended 20 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, AL Stright ended 20 Multimed 7 Lab. [a] PUT, AL Stright ended 20 multimed 7 Lab. [b] PUL, PL Stright ended 20 multimed 7 Lab. [b] PUT, AL Stright ended 10 multimed 7 Lab. Distright ended 10 multimed<</td><td>Lab. 1al PULL, PL Single ended 20 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 10 windling 7 Lab. 1al PULL, PL Sin</td><td>adia (a) PULL_PL Single ended 20 adia (a) PULL_PL Single ended 20 m undefined 7 adia (a) PULL_PL Single ended 20 m undefined 7 -abit (a) POT_PL Single ended 20 m undefined 7 -abit (a) POT_PL Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (b) POT_AR Single ended 20 m undefined 7 -abit (b) (c) Single ended 20 m undefined 7 -abit (c) (c) (c)<td>data (a) PULL_PL Single ended 20 m undefined 7 data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PUT_PL Single ended 20 m undefined 7 -data (a) POT_PL Single ended 20 m undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (b)<pot_ar< td=""> Single ended 20 ms undefined 7 -data (b) POT_AR Single ended 20 ms undefined 7 -data (b) (c) (c) (c) (c) (c) (c) 10 m</pot_ar<></td><td>dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PUT_PL Single ended 20 m undefined 7 -db (a) POT_PL Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 10 m 10 m 10 m 10 m 10 m<!--</td--><td>data (a) PULL_PL Single ended 20 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7
-data (b) PUT_PL Single ended 20 ms undefined 7 -data <td< td=""><td>Lab. [a] PULL_PL Stright ended 20 Lab. [a] PULL_PL Stright ended 20 muddlined 7 Lab. [a] PU</td><td>dda (a) PULL_PL Single ended 20 -dda (a) PULL_PL Single ended 20 -dda (a) PULL_AR Single ended 20 -dda (a) PUT_AR Single ended 20 -dda (b) PUT_AR Single ended 20 -dda (c) PUT_AR PUT_AR PUT_AR</td></td<></td></td></td></td></td> | Lall Isia PULL_PL Straph ended 20 undefined 7 Lall INIT_PL Straph ended 20 undefined 7 Lall DUIN DUIN Straph ended 20 undefined 7 Lall DUIN DUIN Straph ended 20 undefined 7 Lall <

 | Lab. [a] [A]L., PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [a] PULL, PL. Single ended 20 undefined 7 Lab. [b] PULL, PL. Single ended 100 0 10 7 Lab. [b] PULL, PL. Single ended 100 0 10 10 10 10 10 10 10 10 10 10 10 </td <td>ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7</td> <td>Ladia Isia PULL_PA Straph ended 20 undefined 7 Ladia Isia POT_PA Straph ended 20 undefined 7 Ladia Isia POT_PA Straph ended 20 undefined 7 Ladia Isia POT_AR Straph ended 20 undefined 7</td> <td>Ladia Isia PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PUL_PA Strage endod 20 ms undefined 7 -dia 10 PUL_PA Strage endod 20 ms undefined 7 -dia PUL PUL PUL</td>

 | ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PULL_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (a) PUT_PL Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7 ubb (b) PUT_AR Strage ended 20 ms undefined 7

 | Ladia Isia PULL_PA Straph ended 20 undefined 7 Ladia Isia POT_PA Straph ended 20 undefined 7 Ladia Isia POT_PA Straph ended 20 undefined 7 Ladia Isia POT_AR Straph ended 20 undefined 7

 | Ladia Isia PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PULL_PA Strage endod 20 ms undefined 7 -dia 64 PUL_PA Strage endod 20 ms undefined 7 -dia 10 PUL_PA Strage endod 20 ms undefined 7 -dia PUL PUL PUL

 | addla (a) PULL_PL Single ended 20 m undefined 7 addla (a) PULL_PL Single ended 20 m undefined 7 -addla (a) PULL_PL Single ended 20 m undefined 7 -addla (a) POT_FPL Single ended 20 m undefined 7 -addla (a) POT_FPL Single ended 20 m undefined 7 -addla (a) POT_AR Single ended 20 m undefined 7 -addla (a) POT_AR Single ended 20 ms undefined 7 -addla (a) POT_AR Single ended 20 ms undefined 7 -addla (b) POT_AR Single ended 20 ms undefined 7 -addla (b) POT_AR Single ended 20 ms undefined 7 -addla (b) DUT02 B Floput 10 s 10 s 10 s <td>data (a) PULL_PL Single ended 20 m undefined 7 data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PUT_PL Single ended 20 m undefined 7 -data (a) POT_PL Single ended 20 m undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (b)<pot_ar< td=""> Single ended 20 ms undefined 7 -data (b) POT_AR Single ended 20 ms undefined 7 -data (b) (c) (c) (c) (c) (c) (c) 10 m</pot_ar<></td> <td>dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PUT_PL Single ended 20 m undefined 7 -db (a) POT_PL Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 10 m 10 m 10 m 10 m 10 m<!--</td--><td>db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULT_PL Single ended 2.0 m undefined 7 db (a) PUT_PL Single ended 2.0 m undefined 7 db (a) PUT_PL Single ended 2.0 m undefined 7 db (b) PUT_PL Single ended 2.0 m undefined 7 db (b) PUT_PL Single ended 2.0 m undefined 7 db (b) DUT Bit hout 10 m 10 m 10 m db (b) DUT Bit hout 10 m 10 m 10 m 10 m 10 m</td><td>Lab. [a] PULL_PL Single ended 20 Lab. [a] PULL_PL Single ended 20 undefined 7 Lab. [a] PUL_PL Single ended 100 m 10 s Lab. [a] PUL_PL Single ended 10 s 10 s 10 s</td><td>Lab. [a] PULL_PL Single endod 20 Lab. PULL_PL Single endod 20 undefined 7 Lab. PULL_PL Single endod 20 undefined 7 Lab. PULL_PL Single endod 20 mundefined 7 Lab. Divitit</td><td>Lab. [a] PULL, PL Straße endel 20 undefined 7 Lab. [a] PULL, PL Straße endel 20 ms undefined 7 Lab. [a] PULL, PL Straße endel 20 ms undefined 7 Lab. [a] PULT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7</td><td>Lab. [a] PULL, PL Stright ended 20 Lab. [a] PULL, PL
Stright ended 20 Multimed 7 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, AL Stright ended 20 Multimed 7 Lab. [a] PUT, AL Stright ended 20 multimed 7 Lab. [b] PUL, PL Stright ended 20 multimed 7 Lab. [b] PUT, AL Stright ended 10 multimed 7 Lab. Distright ended 10 multimed<</td><td>Lab. 1al PULL, PL Single ended 20 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 10 windling 7 Lab. 1al PULL, PL Sin</td><td>adia (a) PULL_PL Single ended 20 adia (a) PULL_PL Single ended 20 m undefined 7 adia (a) PULL_PL Single ended 20 m undefined 7 -abit (a) POT_PL Single ended 20 m undefined 7 -abit (a) POT_PL Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (b) POT_AR Single ended 20 m undefined 7 -abit (b) (c) Single ended 20 m undefined 7 -abit (c) (c) (c)<td>data (a) PULL_PL Single ended 20 m undefined 7 data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PUT_PL Single ended 20 m undefined 7 -data (a) POT_PL Single ended 20 m undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (b)<pot_ar< td=""> Single ended 20 ms undefined 7 -data (b) POT_AR Single ended 20 ms undefined 7 -data (b) (c) (c) (c) (c) (c) (c) 10 m</pot_ar<></td><td>dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PUT_PL Single ended 20 m undefined 7 -db (a) POT_PL Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 10 m 10 m 10 m 10 m 10 m<!--</td--><td>data (a) PULL_PL Single ended 20 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data <td< td=""><td>Lab. [a] PULL_PL Stright ended 20 Lab. [a] PULL_PL Stright ended 20 muddlined 7 Lab. [a] PU</td><td>dda (a) PULL_PL Single ended 20 -dda (a) PULL_PL Single ended 20 -dda (a) PULL_AR Single ended 20 -dda (a) PUT_AR Single ended 20 -dda (b) PUT_AR Single ended 20 -dda (c) PUT_AR PUT_AR PUT_AR</td></td<></td></td></td></td>
 | data (a) PULL_PL Single ended 20 m undefined 7 data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PUT_PL Single ended 20 m undefined 7 -data (a) POT_PL Single ended 20 m undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (b) <pot_ar< td=""> Single ended 20 ms undefined 7 -data (b) POT_AR Single ended 20 ms undefined 7 -data (b) (c) (c) (c) (c) (c) (c) 10 m</pot_ar<>
 | dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PUT_PL Single ended 20 m undefined 7 -db (a) POT_PL Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db
 (a) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 10 m 10 m 10 m 10 m 10 m </td <td>db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULT_PL Single ended 2.0 m undefined 7 db (a) PUT_PL Single ended 2.0 m undefined 7 db (a) PUT_PL Single ended 2.0 m undefined 7 db (b) PUT_PL Single ended 2.0 m undefined 7 db (b) PUT_PL Single ended 2.0 m undefined 7 db (b) DUT Bit hout 10 m 10 m 10 m db (b) DUT Bit hout 10 m 10 m 10 m 10 m 10 m</td> <td>Lab. [a] PULL_PL Single ended 20 Lab. [a] PULL_PL Single ended 20 undefined 7 Lab. [a] PUL_PL Single ended 100 m 10 s Lab. [a] PUL_PL Single ended 10 s 10 s 10 s</td> <td>Lab. [a] PULL_PL Single endod 20 Lab. PULL_PL Single endod 20 undefined 7 Lab. PULL_PL Single endod 20 undefined 7 Lab. PULL_PL Single endod 20 mundefined 7 Lab. Divitit</td> <td>Lab. [a] PULL, PL Straße endel 20 undefined 7 Lab. [a] PULL, PL Straße endel 20 ms undefined 7 Lab. [a] PULL, PL Straße endel 20 ms undefined 7 Lab. [a] PULT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7</td> <td>Lab. [a] PULL, PL Stright ended 20 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, AL Stright ended 20 Multimed 7 Lab. [a] PUT, AL Stright ended 20 multimed 7 Lab. [b] PUL, PL Stright ended 20 multimed 7 Lab. [b] PUT, AL Stright ended 10 multimed 7 Lab. Distright ended 10 multimed<</td> <td>Lab. 1al PULL, PL Single ended 20 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 10 windling 7 Lab. 1al PULL, PL Sin</td> <td>adia (a) PULL_PL Single ended 20 adia (a) PULL_PL Single ended 20 m undefined 7 adia (a) PULL_PL Single ended 20 m undefined 7 -abit (a) POT_PL Single ended 20 m undefined 7 -abit (a) POT_PL Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (b) POT_AR Single ended 20 m undefined 7 -abit (b) (c) Single ended 20 m undefined 7 -abit (c) (c) (c)<td>data (a) PULL_PL Single ended 20 m undefined 7 data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PUT_PL Single ended 20 m undefined 7 -data (a) POT_PL Single ended 20 m undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (b)<pot_ar< td=""> Single ended 20 ms undefined 7 -data (b) POT_AR Single ended 20 ms undefined 7 -data (b) (c) (c) (c) (c) (c) (c) 10 m</pot_ar<></td><td>dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PUT_PL Single ended 20 m undefined 7 -db (a) POT_PL Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 10 m 10 m 10 m 10 m 10 m<!--</td--><td>data (a) PULL_PL Single ended 20 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7
-data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data <td< td=""><td>Lab. [a] PULL_PL Stright ended 20 Lab. [a] PULL_PL Stright ended 20 muddlined 7 Lab. [a] PU</td><td>dda (a) PULL_PL Single ended 20 -dda (a) PULL_PL Single ended 20 -dda (a) PULL_AR Single ended 20 -dda (a) PUT_AR Single ended 20 -dda (b) PUT_AR Single ended 20 -dda (c) PUT_AR PUT_AR PUT_AR</td></td<></td></td></td> | db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULL_PL Single ended 2.0 m undefined 7 db (a) PULT_PL Single ended 2.0 m undefined 7 db (a) PUT_PL Single ended 2.0 m undefined 7 db (a) PUT_PL Single ended 2.0 m undefined 7 db (b) PUT_PL Single ended 2.0 m undefined 7 db (b) PUT_PL Single ended 2.0 m undefined 7 db (b) DUT Bit hout 10 m 10 m 10 m db (b) DUT Bit hout 10 m 10 m 10 m 10 m 10 m
 | Lab. [a] PULL_PL Single ended 20 Lab. [a] PULL_PL Single ended 20 undefined 7 Lab. [a] PUL_PL Single ended 100 m 10 s Lab. [a] PUL_PL Single ended 10 s 10 s 10 s

 | Lab. [a] PULL_PL Single endod 20 Lab. PULL_PL Single endod 20 undefined 7 Lab. PULL_PL Single endod 20 undefined 7 Lab. PULL_PL Single endod 20 mundefined 7 Lab. Divitit
 | Lab. [a] PULL, PL Straße endel 20 undefined 7 Lab. [a] PULL, PL Straße endel 20 ms undefined 7 Lab. [a] PULL, PL Straße endel 20 ms undefined 7 Lab. [a] PULT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, PL Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7 Lab. [a] PUT, JAR Straße endel 20 ms undefined 7
 | Lab. [a] PULL, PL Stright ended 20 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULL, PL Stright ended 20 Multimed 7 Lab. [a] PULT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, PL Stright ended 20 Multimed 7 Lab. [a] PUT, AL Stright ended 20 Multimed 7 Lab. [a] PUT, AL Stright ended 20 multimed 7 Lab. [b] PUL, PL Stright ended 20 multimed 7 Lab. [b] PUT, AL Stright ended 10 multimed 7 Lab. Distright ended 10 multimed<
 | Lab. 1al PULL, PL Single ended 20 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 Windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20 windling 7 Lab. 1al PULL, PL Single ended 20
 windling 7 Lab. 1al PULL, PL Single ended 10 windling 7 Lab. 1al PULL, PL Sin | adia (a) PULL_PL Single ended 20 adia (a) PULL_PL Single ended 20 m undefined 7 adia (a) PULL_PL Single ended 20 m undefined 7 -abit (a) POT_PL Single ended 20 m undefined 7 -abit (a) POT_PL Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (a) POT_AR Single ended 20 m undefined 7 -abit (b) POT_AR Single ended 20 m undefined 7 -abit (b) (c) Single ended 20 m undefined 7 -abit (c) (c) (c) <td>data (a) PULL_PL Single ended 20 m undefined 7 data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PUT_PL Single ended 20 m undefined 7 -data (a) POT_PL Single ended 20 m undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (b)<pot_ar< td=""> Single ended 20 ms undefined 7 -data (b) POT_AR Single ended 20 ms undefined 7 -data (b) (c) (c) (c) (c) (c) (c) 10 m</pot_ar<></td> <td>dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PUT_PL Single ended 20 m undefined 7
 -db (a) POT_PL Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 10 m 10 m 10 m 10 m 10 m<!--</td--><td>data (a) PULL_PL Single ended 20 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data <td< td=""><td>Lab. [a] PULL_PL Stright ended 20 Lab. [a] PULL_PL Stright ended 20 muddlined 7 Lab. [a] PU</td><td>dda (a) PULL_PL Single ended 20 -dda (a) PULL_PL Single ended 20 -dda (a) PULL_AR Single ended 20 -dda (a) PUT_AR Single ended 20 -dda (b) PUT_AR Single ended 20 -dda (c) PUT_AR PUT_AR PUT_AR</td></td<></td></td> | data (a) PULL_PL Single ended 20 m undefined 7 data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PULL_PL Single ended 20 m undefined 7 -data (a) PUT_PL Single ended 20 m undefined 7 -data (a) POT_PL Single ended 20 m undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (a) POT_AR Single ended 20 ms undefined 7 -data (b) <pot_ar< td=""> Single ended 20 ms undefined 7 -data (b) POT_AR Single ended 20 ms undefined 7 -data (b) (c) (c) (c) (c) (c) (c) 10 m</pot_ar<> | dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PULL_PL Single ended 20 m undefined 7 dda (a) PUT_PL Single ended 20 m undefined 7 -db (a) POT_PL Single ended 20 m undefined 7 -db (a) POT_AR Single ended 20 m
undefined 7 -db (a) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 20 m undefined 7 -db (b) POT_AR Single ended 10 m 10 m 10 m 10 m 10 m </td <td>data (a) PULL_PL Single ended 20 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data <td< td=""><td>Lab. [a] PULL_PL Stright ended 20 Lab. [a] PULL_PL Stright ended 20 muddlined 7 Lab. [a] PU</td><td>dda (a) PULL_PL Single ended 20 -dda (a) PULL_PL Single ended 20 -dda (a) PULL_AR Single ended 20 -dda (a) PUT_AR Single ended 20 -dda (b) PUT_AR Single ended 20 -dda (c) PUT_AR PUT_AR PUT_AR</td></td<></td> | data (a) PULL_PL Single ended 20 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PULL_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (a) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data (b) PUT_PL Single ended 20 ms undefined 7 -data <td< td=""><td>Lab. [a] PULL_PL Stright ended 20 Lab. [a] PULL_PL Stright ended 20 muddlined 7 Lab. [a] PU</td><td>dda (a) PULL_PL Single ended 20 -dda (a) PULL_PL Single ended 20 -dda (a) PULL_AR Single ended 20 -dda (a) PUT_AR Single ended 20 -dda (b) PUT_AR Single ended 20 -dda (c) PUT_AR PUT_AR PUT_AR</td></td<> | Lab. [a] PULL_PL Stright ended 20 Lab. [a] PULL_PL Stright ended 20 muddlined 7 Lab. [a] PU | dda (a) PULL_PL Single ended 20 -dda (a) PULL_PL Single ended 20 -dda (a) PULL_AR Single ended 20 -dda (a) PUT_AR Single ended 20 -dda (b) PUT_AR Single ended 20 -dda (c) PUT_AR PUT_AR PUT_AR |
| Lab. 18 PULL_MI Stright ended 2.0 ms underined 7 Lab. 10 PULL_MI Stright ended 2.0 ms underined 7 Lab. 10 PULL_MI Stright ended 2.0 ms underined 7 Lab. 10 PUT_FL Stright ended 2.0 ms underined 7 Lab. 10 PUT_FL Stright ended 2.0 ms underined 7 Lab. 10 PUT_AL Stright ended 2.0 ms underined 7 Lab. 10 PUT_AL Stright ended 2.0 ms underined 7 Lab. Dot Distright Distright Stright ended 2.0 ms underined 7 LB. Dot Distright Distright Bit Input 10.0 ms 10.s 10.s LB. Dot Distright Distright Bit Input 10.s 10.s 10.s LB. Dot Distright Distright Bit Input 10.s 10.s<

 | "utility" (a) PULL_AII Strugt mode 2.0 mi undefined 7 "utility" (a) POT_FA Strugt mode 2.0 mi undefined 7 "utility" (a) POT_FA Strugt mode 2.0 mi undefined 7 "utility" (a) POT_FA Strugt mode 2.0 mi undefined 7 "utility" (a) POT_FA Strugt mode 2.0 mi undefined 7 "utility" (a) POT_FA Strugt mode 2.0 mi undefined 7 "utility" (a) POT_FA Strugt mode 2.0 mi undefined 7 "utility" (a) POT_FA Strugt mode 2.0 mi undefined 7 "utility" (a) (b) Strugt mode 2.0 mi undefined 7 "utility" (a) (b) (b) (b) (b) 0.0 mi 10 s EILID DOU-Z EV DU-U-Z EV DU-U-Z<

 | db (a) PULL_AM Single ended 2.0 mi undefined 7 db (a) PULL_AL Single ended 2.0 mi undefined 7
 db (a) PULL_AL Single ended 2.0 mi undefined 7 db (a) PUL_PA Single ended 2.0 mi undefined 7 db (a) PUL_PA Single ended 2.0 mi undefined 7 db (a) PUL_PA Single ended 2.0 mi undefined 7 db (a) PUL_PA Single ended 2.0 mi undefined 7 db (a) PUL_PA Single ended 2.0 mi undefined 7 db (b) (b) (b) (b) (b) 10.0 mi 10 mi db (b) (b) (b) (b) (b) (b) 10.0 mi db (b) (b) (b) (b) (b) (b) 10.0 mi

 | data (e) PULL_APIL Strage encode 2.0 m. undefined 7 db. (e) POT_FR Strage encode 2.0 m. undefined 7 -db. (e) POT_FR Strage encode 2.0 m. undefined 7 -db. (e) POT_FR Strage encode 2.0 m. undefined 7 -db. (e) POT_AL Strage encode 2.0 m. undefined 7 -db. (e) POT_AL Strage encode 2.0 m. undefined 7 -db. (e) POT_AL Strage encode 2.0 m. undefined 7 -db. (f) POT_AL Strage encode 2.0 m. undefined 7 -db. (f) POT_AL Strage encode 2.0 m. undefined 7 -db. USD/ONCH Net read Strage encode 2.0 m. 10 m. -db. USD/ONCH Net read Strage encode 10 m. -fl. <t< td=""><td>Lab. Init Pull_Ant Straphe model 20 ms undefined 7 Lab. A PULL_Ant Straphe model 20 ms undefined 7 Lab. A A PULL_Ant Straphe model 20 ms undefined 7 Lab. A B PULL_Ant Straphe model 20 ms undefined 7 Lab. B PULL_Ant Straphe model 20 ms undefined 7 Lab. Init PULL_Ant Straphe model 20 ms undefined 7 Lab. Init PULL_Ant Straphe model 20 ms undefined 7 Lab. Init PULL_Ant Straphe model 20 ms undefined 7 Lab. Divit DIVIT B Straphe model 20 ms undefined 7 LB. Divit DIVIT DIVIT B Straphemodel 20 ms Lab 10 s LB. Divit DIVIT</td><td>Lab. (a) PULL_AM Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a)
 POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 100 ms 10 s Lab. (b) DUL DUL DUL DUL DUL 10 s <</td><td>Hath (a) POLL_API Strigge ended 2 0 ms undefined 7 -#Bb (a) POLL_API Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 10 ms 10 ms 10 ms -#Bb Div Div Bit POL_FPI Bit 10 ms</td><td>Lab. 68 PULL_AM Striphender 20 underned 7 Lab. 64 POT_PL Striphender 20 ms underned 7 Lab. 64 POT_PL Striphended 20 ms underned 7 Lab. 64 POT_AL Striphended 20 ms underned 7 Lab. 040 pot_AL Striphended 20 ms 10 s 10 s Lab. 00 pot_AL</td><td>Lalla Isia Pull_LAM Strophe model 20 ms undefined 7 Bab Isia PULL_AM Strophe model 20 ms undefined 7 Bab Isia PULL_AM Strophe model 20 ms undefined 7 Bab Isia POT_FL Strophe model 20 ms undefined 7 Bab Isia POT_FL Strophe model 20 ms undefined 7 Bab Isia POT_FL Strophe model 20 ms undefined 7 Bab Isia POT_FL Strophe model 20 ms undefined 7 Bab Isia POT_FL Strophe model 20 ms undefined 7 Bab POLONE Dot polone Strophe model 20 ms undefined 7 Bab POLONE Dot polone Bot polone To strophe 10 s Bab POLONE Dot polone Bot polone Bot polone 10 s <td>Lab. [4] [4] PULL_AM Straphenodo 2 Lab. [4] PULL_AM Straphenodo 20 undefined 7 Lab. [4] PULL</td><td>Ladia Isia PULL_Ant Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 14 POT AL Strage encode 2 Dm undefined 7 -dia 14 POT AL Strage encode 2 Dm undefined 7 -dia 010 DMD DNM B Trade Strage encode 10 s 10 s L1 Depair for the mode PO Depair PO 10 s 10 s -HIL DOT DDN DEPA EX for d EX for d 10 s 10 s 10 s</td><td>Ladia Isia PULL_Ant Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_AL Straght endod 2 Dm undefined 7 -dia (b) POT_AL Straght endod 2 Dm undefined 7 -dia (b) DOT B Input Straght endod 10 s 10 s L1 Did Din1 DDIN0 B Input Filterput 10 s 10 s -HIL DOT B Input Filterput Filterput Filterput Filterput</td><td>Table Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia POT FL Single endod 20 ms underned 7 Bas Isia POT FL Single endod 20 ms underned 7 Bas Isia POT JL Single endod 20 ms underned 7 Bas POT JL Single endod 20 ms underned 7 Bas POT JL Dividition Dividition Single endod 20 ms underned 7 Bas POL Dividition Dividition Single endod 20 ms underned 7 Bas Dividition Dividition Dividion Dividit</td><td>Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PUT_FL Single ended 20 ms undefined 7 -Lab. Isia PUT_AIL Single ended 20 ms undefined 7 -Lab. Isia PUT_AIL Single ended 20 ms undefined 7 -Lab. Isia PUT_AIL Single ended 20 ms undefined 7 -Lab. PUT_AIL Single ended 20 ms undefined 7 -Lab. PUT_AIL Single ended 20 ms undefined 7 -Lab. PUT_AIL PUT_AIL Single ended 100 ms 10 ms L1.</td><td>mLB (ii) PULL_AII Single ended 20 m undefined 7 -dB (ii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7</td><td>mLb (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0
m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 100.0 m. 10 s 7 -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. DULL_API DULL_API Single ended 100.0 m. 10 s -Ab. <t< td=""><td>db. (a) PUL_API Strippended 2.0 m. undefined 7 db. (a) POT_PL Strippended 2.0 m. undefined 7 db. (a) POT_AL Strippended 2.0 m. undefined 7 db. (b) POT_AL Bit Pot 10.0 m. m. To 10 db. (b) (b) (b) Bit Pot 10.0 m. m. To db. (b)</td><td>Lab. 61 PUL_A Drops endod 2.0 underned 7 Lab. 61 POT_FL Styde endod 2.0 undefined 7 Lab. 62 Pot_FL Styde endod 2.0 undefined 7 Lab. 60.0 Pot_FL Styde endod 2.0 undefined 7 Lab. 50.0 Pot_FL Styde endod 10.0 10.0 10.0 LB. Dot_FL</td><td>Lab. (a) PULL_ARL Single model 20 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 7 Lab. (a) POT_FR Single model 20 audined 7 Lab. (a) POT_FR Single model 20 audined 7 Lab. (b) POT_AL Single model 20 audined 7 Lab. (b) POT_AL Single model 20 audined 7 Lab. (b) POT_AL Single model 20 ms undefined 7 Lab. (b) (b) POT_AL Single model 20 ms undefined 7 Lab. <t< td=""><td>Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [b] PULL, AN Strage ended 100 ms 10 s Lab. [b] PULL, AN Strage ended 10 s 10 s Lab.</td><td>Lab. [4] PULL, AN Strophe model 20 ms undefined 7 Lab. [4] PULL, AN Strophe model 20 ms undefined 7 Lab. [4] PULL, PLA Strophe model 20 ms undefined 7 Lab. [4] PULL, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [6] [6] [6] [6] [6] [6]</td><td>Lab. [e] PULL, AN Strophe model 20 ms undefined 7 Lab. [e] PULL, AN Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7</td><td>Lab. (a) PULL_AII Single ended 20 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (b) PULL_AI Single ended 20 ms undefined 7 -Lab. (b) (b) (b) (b) (b) (b) 7 7</td><td>mLB (ii) PULL_AII Single ended 20 m undefined 7 -dB (ii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7</td><td>mLb (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 100.0 m. 10 s 7 -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. DULL_API DULL_API Single ended 100.0 m. 10 s -Ab. <t< td=""><td>Lata (a) PULL_API Straph ended 20
-db. (a) POT_FR Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (b) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 ms<uddefined< td=""> 7 -db. (c) POT_AL Straph ended 20 ms<uddefined< td=""> 7 -db. POT_AL Bit P</uddefined<></uddefined<></td><td>Lab. (a) (b) L. An. Strophended 2 0. undefined 7 Lab. (a) POT_FR Strophended 2.0 ms undefined 7 Lab. (a) POT_AL Strophended 2.0 ms undefined 7 Lab. (b) POT_AL Strophended 10 ms 10 ms 10 ms Lab. DOL Tobus DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DE Input Lab. DOL TOL DOUTO2 DE Input Hout Hout Hout Hout Hout Hout Hout Hout Hout Hout</td><td>data (a) (b)(L,A)(L,A) Straple wrode 20 (a)(b)(b)(b)(c) 7 -Da (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) The (DT)(F) The (DT)(F) -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) F) F) F) -db (DT)(DT) (DT)(F)</td></t<></td></t<></td></t<></td></td></t<> | Lab. Init Pull_Ant Straphe model 20 ms undefined 7 Lab. A PULL_Ant Straphe model 20 ms undefined 7 Lab. A A PULL_Ant Straphe model 20 ms undefined 7 Lab. A B PULL_Ant Straphe model 20 ms undefined 7 Lab. B PULL_Ant Straphe model 20 ms undefined 7 Lab. Init PULL_Ant Straphe model 20 ms undefined 7 Lab. Init PULL_Ant Straphe model 20 ms undefined 7 Lab. Init PULL_Ant Straphe model 20 ms undefined 7 Lab. Divit DIVIT B Straphe model 20 ms undefined 7 LB. Divit DIVIT DIVIT B Straphemodel 20 ms Lab 10 s LB. Divit DIVIT

 | Lab. (a) PULL_AM Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 20 ms undefined 7 Lab. (a) POLT_FA Strage ended 100 ms 10 s Lab. (b) DUL DUL DUL DUL DUL 10 s <

 | Hath (a) POLL_API Strigge ended 2 0 ms undefined 7 -#Bb (a) POLL_API Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 2 0 ms undefined 7 -#Bb (a) POL_FPI Strigge ended 10 ms 10 ms 10 ms -#Bb Div Div Bit POL_FPI Bit 10 ms

 | Lab. 68 PULL_AM Striphender 20 underned 7 Lab. 64 POT_PL Striphender 20 ms underned 7 Lab. 64 POT_PL Striphended 20 ms underned 7 Lab. 64 POT_AL Striphended 20 ms underned 7 Lab. 040 pot_AL Striphended 20 ms 10 s 10 s Lab. 00 pot_AL

 | Lalla Isia Pull_LAM Strophe model 20 ms undefined 7 Bab Isia PULL_AM Strophe model 20 ms undefined 7 Bab Isia PULL_AM Strophe model 20 ms undefined 7 Bab Isia POT_FL Strophe model 20 ms undefined 7 Bab Isia POT_FL Strophe model 20 ms undefined 7 Bab Isia POT_FL Strophe model 20 ms undefined 7 Bab Isia POT_FL Strophe model 20 ms undefined 7 Bab Isia POT_FL Strophe model 20 ms undefined 7 Bab POLONE Dot polone Strophe model 20 ms undefined 7 Bab POLONE Dot polone Bot polone To strophe 10 s Bab POLONE Dot polone Bot polone Bot polone 10 s <td>Lab. [4] [4] PULL_AM Straphenodo 2 Lab. [4] PULL_AM Straphenodo 20 undefined 7 Lab. [4] PULL</td> <td>Ladia Isia PULL_Ant Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 14 POT AL Strage encode 2 Dm undefined 7 -dia 14 POT AL Strage encode 2 Dm undefined 7 -dia 010 DMD DNM B Trade Strage encode 10 s 10 s L1 Depair for the mode PO Depair PO 10 s 10 s -HIL DOT DDN DEPA EX for d EX for d 10 s 10 s 10 s</td> <td>Ladia Isia PULL_Ant Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_AL Straght endod 2 Dm undefined 7 -dia (b) POT_AL Straght endod 2 Dm undefined 7 -dia (b) DOT B Input Straght endod 10 s 10 s L1 Did Din1 DDIN0 B Input Filterput 10 s 10 s -HIL DOT B Input Filterput Filterput Filterput Filterput</td> <td>Table Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia POT FL Single endod 20 ms underned 7 Bas Isia POT FL Single endod 20 ms underned 7 Bas Isia POT JL Single endod 20 ms underned 7 Bas POT JL Single endod 20 ms underned 7 Bas POT JL Dividition Dividition Single endod 20 ms underned 7 Bas POL Dividition Dividition Single endod 20 ms underned 7 Bas Dividition Dividition Dividion Dividit</td> <td>Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PUT_FL Single ended 20 ms undefined 7 -Lab. Isia PUT_AIL Single ended 20 ms undefined 7 -Lab. Isia PUT_AIL Single ended 20 ms undefined 7 -Lab. Isia PUT_AIL Single ended 20 ms undefined 7 -Lab. PUT_AIL Single ended 20 ms undefined 7 -Lab. PUT_AIL Single ended 20 ms undefined 7 -Lab. PUT_AIL PUT_AIL Single ended 100 ms 10 ms L1.</td> <td>mLB (ii) PULL_AII Single ended 20 m undefined 7 -dB (ii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7</td> <td>mLb (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7
 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 100.0 m. 10 s 7 -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. DULL_API DULL_API Single ended 100.0 m. 10 s -Ab. <t< td=""><td>db. (a) PUL_API Strippended 2.0 m. undefined 7 db. (a) POT_PL Strippended 2.0 m. undefined 7 db. (a) POT_AL Strippended 2.0 m. undefined 7 db. (b) POT_AL Bit Pot 10.0 m. m. To 10 db. (b) (b) (b) Bit Pot 10.0 m. m. To db. (b)</td><td>Lab. 61 PUL_A Drops endod 2.0 underned 7 Lab. 61 POT_FL Styde endod 2.0 undefined 7 Lab. 62 Pot_FL Styde endod 2.0 undefined 7 Lab. 60.0 Pot_FL Styde endod 2.0 undefined 7 Lab. 50.0 Pot_FL Styde endod 10.0 10.0 10.0 LB. Dot_FL</td><td>Lab. (a) PULL_ARL Single model 20 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 7 Lab. (a) POT_FR Single model 20 audined 7 Lab. (a) POT_FR Single model 20 audined 7 Lab. (b) POT_AL Single model 20 audined 7 Lab. (b) POT_AL Single model 20 audined 7 Lab. (b) POT_AL Single model 20 ms undefined 7 Lab. (b) (b) POT_AL Single model 20 ms undefined 7 Lab. <t< td=""><td>Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [b] PULL, AN Strage ended 100 ms 10 s Lab. [b] PULL, AN Strage ended 10 s 10 s Lab.</td><td>Lab. [4] PULL, AN Strophe model 20 ms undefined 7 Lab. [4] PULL, AN Strophe model 20 ms undefined 7 Lab. [4] PULL, PLA Strophe model 20 ms undefined 7 Lab. [4] PULL, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [6] [6] [6] [6] [6] [6]</td><td>Lab. [e] PULL, AN Strophe model 20 ms undefined 7 Lab. [e] PULL, AN Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7</td><td>Lab. (a) PULL_AII Single ended 20 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (b) PULL_AI Single ended 20 ms undefined 7 -Lab. (b) (b) (b) (b) (b) (b) 7 7</td><td>mLB (ii) PULL_AII Single ended 20 m undefined 7 -dB (ii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7</td><td>mLb (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 100.0 m. 10 s 7 -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. DULL_API DULL_API Single ended 100.0 m. 10 s -Ab. <t< td=""><td>Lata (a) PULL_API Straph ended 20 -db. (a) POT_FR
 Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (b) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 ms<uddefined< td=""> 7 -db. (c) POT_AL Straph ended 20 ms<uddefined< td=""> 7 -db. POT_AL Bit P</uddefined<></uddefined<></td><td>Lab. (a) (b) L. An. Strophended 2 0. undefined 7 Lab. (a) POT_FR Strophended 2.0 ms undefined 7 Lab. (a) POT_AL Strophended 2.0 ms undefined 7 Lab. (b) POT_AL Strophended 10 ms 10 ms 10 ms Lab. DOL Tobus DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DE Input Lab. DOL TOL DOUTO2 DE Input Hout Hout Hout Hout Hout Hout Hout Hout Hout Hout</td><td>data (a) (b)(L,A)(L,A) Straple wrode 20 (a)(b)(b)(b)(c) 7 -Da (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) The (DT)(F) The (DT)(F) -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) F) F) F) -db (DT)(DT) (DT)(F)</td></t<></td></t<></td></t<></td> | Lab. [4] [4] PULL_AM Straphenodo 2 Lab. [4] PULL_AM Straphenodo 20 undefined 7 Lab. [4] PULL

 | Ladia Isia PULL_Ant Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 64 POT FR Strage encode 2 Dm undefined 7 -dia 14 POT AL Strage encode 2 Dm undefined 7 -dia 14 POT AL Strage encode 2 Dm undefined 7 -dia 010 DMD DNM B Trade Strage encode 10 s 10 s L1 Depair for the mode PO Depair PO 10 s 10 s -HIL DOT DDN DEPA EX for d EX for d 10 s 10 s 10 s

 | Ladia Isia PULL_Ant Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_PL Straght endod 2 Dm undefined 7 -dia (a) POT_AL Straght endod 2 Dm undefined 7 -dia (b) POT_AL Straght endod
2 Dm undefined 7 -dia (b) DOT B Input Straght endod 10 s 10 s L1 Did Din1 DDIN0 B Input Filterput 10 s 10 s -HIL DOT B Input Filterput Filterput Filterput Filterput
 | Table
Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia PULL_Ant Single endod 20 ms underned 7 Bas Isia POT FL Single endod 20 ms underned 7 Bas Isia POT FL Single endod 20 ms underned 7 Bas Isia POT JL Single endod 20 ms underned 7 Bas POT JL Single endod 20 ms underned 7 Bas POT JL Dividition Dividition Single endod 20 ms underned 7 Bas POL Dividition Dividition Single endod 20 ms underned 7 Bas Dividition Dividition Dividion Dividit

 | Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PULL_AIT Single ended 20 ms undefined 7 -Lab. Isia PUT_FL Single ended 20 ms undefined 7 -Lab. Isia PUT_AIL Single ended 20 ms undefined 7 -Lab. Isia PUT_AIL Single ended 20 ms undefined 7 -Lab. Isia PUT_AIL Single ended 20 ms undefined 7 -Lab. PUT_AIL Single ended 20 ms undefined 7 -Lab. PUT_AIL Single ended 20 ms undefined 7 -Lab. PUT_AIL PUT_AIL Single ended 100 ms 10 ms L1.

 | mLB (ii) PULL_AII Single ended 20 m undefined 7 -dB (ii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7

 | mLb (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 100.0 m. 10 s 7 -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. DULL_API DULL_API Single ended 100.0 m. 10 s -Ab. <t< td=""><td>db. (a) PUL_API Strippended 2.0 m. undefined 7 db. (a) POT_PL Strippended 2.0 m. undefined 7 db. (a) POT_AL Strippended 2.0 m. undefined 7 db. (b) POT_AL Bit Pot 10.0 m. m. To 10 db. (b) (b) (b) Bit Pot 10.0 m. m. To db. (b)</td><td>Lab. 61 PUL_A Drops endod 2.0 underned 7 Lab. 61 POT_FL Styde endod 2.0 undefined 7 Lab. 62 Pot_FL Styde endod 2.0 undefined 7 Lab. 60.0 Pot_FL Styde endod 2.0 undefined 7 Lab. 50.0 Pot_FL Styde endod 10.0 10.0 10.0 LB. Dot_FL</td><td>Lab. (a) PULL_ARL Single model 20 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 7 Lab. (a) POT_FR Single model 20 audined 7 Lab. (a) POT_FR Single model 20 audined 7 Lab. (b) POT_AL Single model 20 audined 7 Lab. (b) POT_AL Single model 20 audined 7 Lab. (b) POT_AL Single model 20 ms undefined 7 Lab. (b) (b) POT_AL Single model 20 ms undefined 7 Lab. <t< td=""><td>Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [b] PULL, AN Strage ended 100 ms 10 s Lab. [b] PULL, AN Strage ended 10 s 10 s Lab.</td><td>Lab. [4] PULL, AN Strophe model 20 ms undefined 7 Lab. [4] PULL, AN Strophe model 20 ms undefined 7 Lab. [4] PULL, PLA Strophe model 20 ms undefined 7 Lab. [4] PULL, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [6] [6] [6] [6] [6] [6]</td><td>Lab. [e] PULL, AN Strophe model 20 ms undefined 7 Lab. [e] PULL, AN Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7</td><td>Lab. (a) PULL_AII Single ended 20 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (b) PULL_AI Single ended 20 ms undefined 7 -Lab. (b) (b) (b) (b) (b) (b) 7 7</td><td>mLB (ii) PULL_AII Single ended 20 m undefined 7 -dB (ii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7</td><td>mLb (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined
 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 100.0 m. 10 s 7 -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. DULL_API DULL_API Single ended 100.0 m. 10 s -Ab. <t< td=""><td>Lata (a) PULL_API Straph ended 20 -db. (a) POT_FR Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (b) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 ms<uddefined< td=""> 7 -db. (c) POT_AL Straph ended 20 ms<uddefined< td=""> 7 -db. POT_AL Bit P</uddefined<></uddefined<></td><td>Lab. (a) (b) L. An. Strophended 2 0. undefined 7 Lab. (a) POT_FR Strophended 2.0 ms undefined 7 Lab. (a) POT_AL Strophended 2.0 ms undefined 7 Lab. (b) POT_AL Strophended 10 ms 10 ms 10 ms Lab. DOL Tobus DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DE Input Lab. DOL TOL DOUTO2 DE Input Hout Hout Hout Hout Hout Hout Hout Hout Hout Hout</td><td>data (a) (b)(L,A)(L,A) Straple wrode 20 (a)(b)(b)(b)(c) 7 -Da (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) The (DT)(F) The (DT)(F) -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) F) F) F) -db (DT)(DT) (DT)(F)</td></t<></td></t<></td></t<> | db. (a) PUL_API Strippended 2.0 m. undefined 7 db. (a) POT_PL Strippended 2.0 m. undefined 7 db. (a) POT_AL Strippended 2.0 m. undefined 7 db. (b) POT_AL Bit Pot 10.0 m. m. To 10 db. (b) (b) (b) Bit Pot 10.0 m. m. To db. (b)
 | Lab. 61 PUL_A Drops endod 2.0 underned 7 Lab. 61 POT_FL Styde endod 2.0 undefined 7 Lab. 62 Pot_FL Styde endod 2.0 undefined 7 Lab. 60.0 Pot_FL Styde endod 2.0 undefined 7 Lab. 50.0 Pot_FL Styde endod 10.0 10.0 10.0 LB. Dot_FL

 | Lab. (a) PULL_ARL Single model 20 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 2 Lab. (a) POT_FR Single model 20 audined 7 Lab. (a) POT_FR Single model 20 audined 7 Lab. (a) POT_FR Single model 20 audined 7 Lab. (b) POT_AL Single model 20 audined 7 Lab. (b) POT_AL Single model 20 audined 7 Lab. (b) POT_AL Single model 20 ms undefined 7 Lab. (b) (b) POT_AL Single model 20 ms undefined 7 Lab. <t< td=""><td>Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [b] PULL, AN Strage ended 100 ms 10 s Lab. [b] PULL, AN Strage ended 10 s 10 s Lab.</td><td>Lab. [4] PULL, AN Strophe model 20 ms undefined 7 Lab. [4] PULL, AN Strophe model 20 ms undefined 7 Lab. [4] PULL, PLA Strophe model 20 ms undefined 7 Lab. [4] PULL, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [6] [6] [6] [6] [6] [6]</td><td>Lab. [e] PULL, AN Strophe model 20 ms undefined 7 Lab. [e] PULL, AN Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7</td><td>Lab. (a) PULL_AII Single ended 20 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (b) PULL_AI Single ended 20 ms undefined 7 -Lab. (b) (b) (b) (b) (b) (b) 7 7</td><td>mLB (ii) PULL_AII Single ended 20 m undefined 7 -dB (ii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7
-dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7</td><td>mLb (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 100.0 m. 10 s 7 -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. DULL_API DULL_API Single ended 100.0 m. 10 s -Ab. <t< td=""><td>Lata (a) PULL_API Straph ended 20 -db. (a) POT_FR Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (b) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 ms<uddefined< td=""> 7 -db. (c) POT_AL Straph ended 20 ms<uddefined< td=""> 7 -db. POT_AL Bit P</uddefined<></uddefined<></td><td>Lab. (a) (b) L. An. Strophended 2 0. undefined 7 Lab. (a) POT_FR Strophended 2.0 ms undefined 7 Lab. (a) POT_AL Strophended 2.0 ms undefined 7 Lab. (b) POT_AL Strophended 10 ms 10 ms 10 ms Lab. DOL Tobus DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DE Input Lab. DOL TOL DOUTO2 DE Input Hout Hout Hout Hout Hout Hout Hout Hout Hout Hout</td><td>data (a) (b)(L,A)(L,A) Straple wrode 20 (a)(b)(b)(b)(c) 7 -Da (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) The (DT)(F) The (DT)(F) -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) F) F) F) -db (DT)(DT) (DT)(F)</td></t<></td></t<> | Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, PL Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [a] PULL, AN Strage ended 20 ms undefined 7 Lab. [b] PULL, AN Strage ended 100 ms 10 s Lab. [b] PULL, AN Strage ended 10 s 10 s Lab.
 | Lab. [4] PULL, AN Strophe model 20 ms undefined 7 Lab. [4] PULL, AN Strophe model 20 ms undefined 7 Lab. [4] PULL, PLA Strophe model 20 ms undefined 7 Lab. [4] PULL, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT, PLA Strophe model 20 ms undefined 7 Lab. [4] PUT,
PLA Strophe model 20 ms undefined 7 Lab. [6] [6] [6] [6] [6] [6]
 | Lab. [e] PULL, AN Strophe model 20 ms undefined 7 Lab. [e] PULL, AN Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 Lab. [e] PULPL Strophe model 20 ms undefined 7 | Lab. (a) PULL_AII Single ended 20 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (a) PULL_AI Single ended 20 ms undefined 7 -Lab. (b) PULL_AI Single ended 20 ms undefined 7 -Lab. (b) (b) (b) (b) (b) (b)
 7 7 | mLB (ii) PULL_AII Single ended 20 m undefined 7 -dB (ii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7 -dB (iii) PULL_AI Single ended 20 m undefined 7
 | mLb (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 2.0 m. undefined 7 -Ab. (e) PULL_API Single ended 100.0 m. 10 s 7 -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. (e) DULL_API Single ended 100.0 m. 10 s -Ab. DULL_API DULL_API Single ended 100.0 m. 10 s -Ab. <t< td=""><td>Lata (a) PULL_API Straph ended 20 -db. (a) POT_FR Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (b) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 ms<uddefined< td=""> 7 -db. (c) POT_AL Straph ended 20 ms<uddefined< td=""> 7 -db. POT_AL Bit P</uddefined<></uddefined<></td><td>Lab. (a) (b) L. An. Strophended 2 0. undefined 7 Lab. (a) POT_FR Strophended 2.0 ms undefined 7 Lab. (a) POT_AL Strophended 2.0 ms undefined 7 Lab. (b) POT_AL Strophended 10 ms 10 ms 10 ms Lab. DOL Tobus DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DE Input Lab. DOL TOL DOUTO2 DE Input Hout Hout Hout Hout Hout Hout Hout Hout Hout Hout</td><td>data (a) (b)(L,A)(L,A) Straple wrode 20 (a)(b)(b)(b)(c) 7 -Da (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) The (DT)(F) The (DT)(F) -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) F) F) F) -db (DT)(DT) (DT)(F)</td></t<> | Lata (a) PULL_API Straph ended 20 -db. (a) POT_FR Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (a) POT_AL Straph ended 20 suddefined 7 -db. (b) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 suddefined 7 -db. (c) POT_AL Straph ended 20 ms <uddefined< td=""> 7 -db. (c) POT_AL Straph ended 20 ms<uddefined< td=""> 7 -db. POT_AL Bit P</uddefined<></uddefined<> | Lab. (a)
(b) L. An. Strophended 2 0. undefined 7 Lab. (a) POT_FR Strophended 2.0 ms undefined 7 Lab. (a) POT_AL Strophended 2.0 ms undefined 7 Lab. (b) POT_AL Strophended 10 ms 10 ms 10 ms Lab. DOL Tobus DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DOUTO2 DE Input Lab. DOL TOL DOUTO2 DE Input Hout | data (a) (b)(L,A)(L,A) Straple wrode 20 (a)(b)(b)(b)(c) 7 -Da (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (a) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) Straple wrode 20 is: undefined 7 -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) The (DT)(F) The (DT)(F) -db (b) (DT)(F) (b) (DT)(F) (b) (DT)(F) F) F) F) -db (DT)(DT) (DT)(F) |
|

 | -udia (a) PUL_24, Surgit ended 2 / m. vectorized 7 -udia (a) POT_24, Surgit ended 2 / m. vectorized 7 -udia (a) POT_24, Surgit ended 2 / m. vectorized 7 -udia (a) POT_24, Surgit ended 2 / m. vectorized 7 -udia (a) POT_24, Surgit ended 2 / m. vectorized 7 -udia (a) POT_24, Surgit ended 2 / m. vectorized 7 -udia (a) POT_24, Surgit ended 2 / m. vectorized 7 -udia (a) POT_24, Surgit ended 2 / m. vectorized 7 -udia (b) Potorized B / surgit ended 2 / m. vectorized 7 -udia (b) (b) Potorized B / surgit ended 2 / m. 10 / m. 10 / m. -udia (b) (b) Potorized B / s

 | db (a) (b) (b) (c)

 | Lab. (a) PUL_AL Single ended 21 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined 7 Lab. (a) POT_PR Single ended 20 ms undefined 7 Lab. (b) POT_PR Single ended 20 ms undefined 7 Lab. (b) POT_PR Single ended 20 ms undefined 7 Lab. (b) Diff pot_f Single ended 100 ms 10 s 10 LBL Diff pot_f Diff pot_f Diff pot_f Diff pot_f 10 s 10 LBL Diff pot_f Diff pot_f Diff pot_f Diff pot_f 10 10

 | Lab. [a] POL_PL Stright ended 20 ms undefined 7 -ab. [a] POT_PL Stright ended 20 ms undefined 7 -ab. [a] POT_PL Stright ended 20 ms undefined 7 -ab. [a] POT_PL Stright ended 20 ms undefined 7 -ab. [a] POT_PL Stright ended 20 ms undefined 7 -ab. [a] POT_AL Stright ended 20 ms undefined 7 -ab. [a] POT_AL Stright ended 20 ms undefined 7 -ab. [a] POT_AL Stright ended 20 ms undefined 7 -bb.

 | Lab. [a] Null_par. Songle ended. 2 / 0. undefined. 7 Lab. [a] POT_PR Songle ended. 2.0 m. undefined. 7 Lab. [a] POT_PR Songle ended. 2.0 m. undefined. 7 Lab. [a] POT_PR Songle ended. 2.0 m. undefined. 7 Lab. [a] POT_PR Songle ended. 2.0 m. undefined. 7 Lab. [a] POT_PR Songle ended. 2.0 m. undefined. 7 Lab. [a] POT_PR Songle ended. 2.0 m. undefined. 7 Lab. [a] POT_PR Songle ended. 2.0 m. undefined. 7 Lab. [a] POT_PR Songle ended. 10.0 m. 10.0 m. 10.0 m. Lab. [b] POT_PR Songle ended. 10.0 m. 10.0 m. 10.0 m. Lab. [b] [b] POT_PR Songle ended. 10.0 m.

 | Lab. (a) POLL AL Stright ended 20 undefined 7 Lab. (a) POLL PL Stright ended 20 au condenied 7 Lab. (a) POLL PL Stright ended 20 au condenied 7 Lab. (a) POLL PL Stright ended 20 au condenied 7 Lab. (a) POLL PL Stright ended 20 au condenied 7 Lab. (a) POLL PL Stright ended 20 au condenied 7 Lab. (a) POLL PL Stright ended 20 au condenied 7 Lab. (a) POLL PL Stright ended 20 au condenied 7 Lab. (a) POLL PL Stright ended 20 au condenied 7 Lab. (a) POLL PL Stright ended 100 0 0 0 Lab. (b) POLL PL Stright ended 100 0 0

 | Lab. [a] [VolAL Single ended 21 Lab. [a] POI_PI Single ended 20 ms undified 7 Lab. [a] POI_PI Single ended 20 ms undified 7 Lab. [a] POI_PI Single ended 20 ms undified 7 Lab. [a] POI_PI Single ended 20 ms undified 7 Lab. [a] POI_PI Single ended 20 ms undified 7 Lab. [b] POI_AL Single ended 20 ms undified 7 Lab. [b] POI_AL Single ended 100 ms 10 ms 10 ms LAB. Option DNOT Bit Input Hight Input 10 ms 10 ms LAB. Option DNOT Bit Input Hight Input

 | Lall [a] [b] [b] [c] [c] <td>Lab. [a] [V1L_AL Single ended 2 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [b] POT_A Single ended 100 ms 10 s 10 s Lab. [b] Divid Bit not. 10 s 10 s 10 s Lab. [b] Divid Bit not. 10 s 10 s 10 s Lab. [b] Divid Divid Bit not. 10 s 10 s Lab. [b] Divid Di</td> <td>Ladia (a) PULL_AL Stright ended 2 Ladia (a) POT_FL Stright ended 2.0 ms undefined 7 Ladia (a) POT_FL Stright ended 2.0 ms undefined 7 Ladia (a) POT_FL Stright ended 2.0 ms undefined 7 Ladia (a) POT_FL Stright ended 2.0 ms undefined 7 Ladia (a) POT_FL Stright ended 2.0 ms undefined 7 Ladia (b) POT_AL Stright ended 2.0 ms undefined 7 Ladia (c) Dotal track for targets Stright ended 10.0 ms 10.s FAIL DOLG Dotal track for targets Dotal track for targets Dotal track for targets 10.0 ms 10.s FAIL DOLG Dotal track for targets Dotal track for targets Dotal track for targets 10.0 ms 10.0 ms FAIL DOLG Dotal targets Dotal targets Dotal targets Dotal targets</td> <td>Ladia (a) PULL_AL Stright ended 2 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (b) POTA Stright ended 2.0 ms undefined 7 Ladia (b) POTA Stright ended 100 ms 10 s 10 s Ladia (c) DOTO2 BR Upuput 10 s 10 s 10 s LAB (c) DOTO2 BR Upuput 10 s 10 s 10 s LAB (c) DOTO2 BR Upuput 10 s 10 s 10 s LAB DOS UDD1</td> <td>Ladia (a) PULL_AL Stright ended 2 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_AL Stright ended 2.0 ms undefined 7 Ladia (a) POT_AL Stright ended 2.0 ms undefined 7 Ladia (b) POT_AL Stright ended 2.0 ms undefined 7 Ladia (b) POT_AL Stright ended 1000 ms 100 10 Ladia (c) DOUTO2 BR (rout 1000 ms 100 10 LADIA DOUTO2 BR (rout BR (rout 100 10 LADIA DOUTO2 BR (rout 100 10 10 LADIA DOUTO2 BR (rout 10</td> <td>Lath 1a
 PDI_Lath Strophenoids 21 Lath 1a PDI_Lath Strophenoids 21 Lath 1a PDI_Lath Strophenoids 21 Lath 1a PDI_Lath Strophenoids 20 Lath 1b PDI_Lath Strophenoids 20 Lath 1b PDI_Lath Strophenoids 20 Lath PDI_Lath Strophenoids 20 undefined 7 Lath PDI_Lath Strophenoids 20 undefined 7 Lath PDI_Lath Strophenoids 20 undefined 7 Lath PDIUL PDIUL PDIUL PDIUL PDIUL PDIUL PDIU</td> <td>Ladia (a) (b) (c) (c)<!--</td--><td></td><td>ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (b) DOINT Bit Input 1.0 m. 10 m. 10 m. Ubb (b) DOINT Bit Input 1.0 m. 10 m. 10 m. Ubb (b) DOINT Bit Input 1.0 m. 10 m. 10 m. Ubb (b) DOINT Bit Input 1.0 m. 10 m. UBb <td< td=""><td>Lab. [a] [b] [b]<td>Laik [a] [AUL_AL Single endod 2 Laik [a] [PD1_Phi Single endod 20 Laik [a] [PD1_Phi Single endod 10 is Laik [a] [PD1_Phi Single endod 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [Bit input [Bit input [Bit input<td>Lab. Iai Val.Lat. Stright ended 20 and under additional 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 100 nm<</td> 10 = Lab. Dot Dot Dot DOUTOL Bk logut 20 10 = 10 =<td>Lab. Init Init Stright ended 2 / 1 Lab. Init POT_R Stright ended 2 / 1 Lab. Init POT_AR Stright ended 2 / 1 Lab. Init POT_AR Stright ended 10 m Lab. Init POT_AR Stright ended 10 m Lab. Dot Dot Dot 10 m Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Pot Pot <</td><td>Lab. Lei Null_at Single model 21 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_AR Single model 100 7 100 Lab. Dot DOTO Bi lood 100 8 100 8 100</td><td>Latt Lat Party Latt Single encled 2 Dim Undefined 7 Latt Diptal input / output Single encled 2 Dim Undefined 7 Latt Diptal input / output Diptal input / output Single encled 100 min 10 min Latt Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input / output Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input</td><td>Ladia (a) (b) (c) (c)<!--</td--><td></td><td>Latla Isia PUL_AL Single ended 2 Dim Underside 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right Bit Right Dim Dim Dim Dim Latla Dim Dim</td><td>Lab. [a] [b] [b]<td>Lab. (e) PULL_AL Single ended 20 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 100 ms 10 s 7 -Ab. (b) DINT Bit finuk 20 ms 10 s 10 s -Ab. (b) DINT Bit finuk 100 ms 10 s 10 s -Ab. (b) DINT Bit finuk 10 s 10 s 10 s -Ab. (b) DINT</td></td></td></td></td></td<></td></td>
 | Lab. [a] [V1L_AL Single ended 2 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [a] POT_PL Single ended 20 ms undified 7 Lab. [b] POT_A Single ended 100 ms 10 s 10 s Lab. [b] Divid Bit not. 10 s 10 s 10 s Lab. [b] Divid Bit not. 10 s 10 s 10 s Lab. [b] Divid Divid Bit not. 10 s 10 s Lab. [b] Divid Di

 | Ladia (a) PULL_AL Stright ended 2 Ladia (a) POT_FL Stright ended 2.0 ms undefined 7 Ladia (a) POT_FL Stright ended 2.0 ms undefined 7 Ladia (a) POT_FL Stright ended 2.0 ms undefined 7 Ladia (a) POT_FL Stright ended 2.0 ms undefined 7 Ladia (a) POT_FL Stright ended 2.0 ms undefined 7 Ladia (b) POT_AL Stright ended 2.0 ms undefined 7 Ladia (c) Dotal track for targets Stright ended 10.0 ms 10.s FAIL DOLG Dotal track for targets Dotal track for targets Dotal track for targets 10.0 ms 10.s FAIL DOLG Dotal track for targets Dotal track for targets Dotal track for targets 10.0 ms 10.0 ms FAIL DOLG Dotal targets Dotal targets Dotal targets Dotal targets

 | Ladia (a) PULL_AL Stright ended 2 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (b) POTA Stright ended 2.0 ms undefined 7 Ladia (b) POTA Stright ended 100 ms 10 s 10 s Ladia (c) DOTO2 BR Upuput 10 s 10 s 10 s LAB (c) DOTO2 BR Upuput 10 s 10 s 10 s LAB (c) DOTO2 BR Upuput 10 s 10 s 10 s LAB DOS UDD1

 | Ladia (a) PULL_AL Stright ended 2 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_PL Stright ended 2.0 ms undefined 7 Ladia (a) POT_AL Stright ended 2.0 ms undefined 7 Ladia (a) POT_AL Stright ended 2.0 ms undefined 7 Ladia (b) POT_AL Stright ended 2.0 ms undefined 7 Ladia (b) POT_AL Stright ended 1000 ms 100 10 Ladia (c) DOUTO2 BR (rout 1000 ms 100 10 LADIA DOUTO2 BR (rout BR (rout 100 10 LADIA DOUTO2 BR (rout 100 10 10
 LADIA DOUTO2 BR (rout 10

 | Lath 1a PDI_Lath Strophenoids 21 Lath 1a PDI_Lath Strophenoids 21 Lath 1a PDI_Lath Strophenoids 21 Lath 1a PDI_Lath Strophenoids 20 Lath 1b PDI_Lath Strophenoids 20 Lath 1b PDI_Lath Strophenoids 20 Lath PDI_Lath Strophenoids 20 undefined 7 Lath PDI_Lath Strophenoids 20 undefined 7 Lath PDI_Lath Strophenoids 20 undefined 7 Lath PDIUL PDIUL PDIUL PDIUL PDIUL PDIUL PDIU

 | Ladia (a) (b) (c) (c) </td <td></td> <td>ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (b) DOINT Bit Input 1.0 m. 10 m. 10 m. Ubb (b) DOINT Bit Input 1.0 m. 10 m. 10 m. Ubb (b) DOINT Bit Input 1.0 m. 10 m. 10 m. Ubb (b) DOINT Bit Input 1.0 m. 10 m. UBb <td< td=""><td>Lab. [a] [b] [b]<td>Laik [a] [AUL_AL Single endod 2 Laik [a] [PD1_Phi Single endod 20 Laik [a] [PD1_Phi Single endod 10 is Laik [a] [PD1_Phi Single endod 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [Bit input [Bit input [Bit input<td>Lab. Iai Val.Lat. Stright ended 20 and under additional 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 100 nm<</td> 10 = Lab. Dot Dot Dot DOUTOL Bk logut 20 10 = 10 =<td>Lab. Init Init Stright ended 2 / 1 Lab. Init POT_R Stright ended 2 / 1 Lab. Init POT_AR Stright ended 2 / 1 Lab. Init POT_AR Stright ended 10 m Lab. Init POT_AR Stright ended 10 m Lab. Dot Dot Dot 10 m Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Pot Pot <</td><td>Lab. Lei Null_at Single model 21 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_AR Single model 100 7 100 Lab. Dot DOTO Bi lood 100 8 100 8 100</td><td>Latt Lat Party Latt Single encled 2 Dim Undefined 7 Latt Diptal input / output Single encled 2 Dim Undefined 7 Latt Diptal input / output Diptal input / output Single encled 100 min 10 min Latt Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input / output Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input</td><td>Ladia (a) (b) (c) (c)<!--</td--><td></td><td>Latla Isia PUL_AL Single ended 2 Dim Underside 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right Bit Right Dim Dim Dim Dim Latla Dim Dim</td><td>Lab. [a] [b] [b]<td>Lab. (e) PULL_AL Single ended 20 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 100 ms 10 s 7 -Ab. (b) DINT Bit finuk 20 ms 10 s 10 s -Ab. (b) DINT Bit finuk 100 ms 10 s 10 s -Ab. (b) DINT Bit finuk 10 s 10 s 10 s -Ab. (b) DINT</td></td></td></td></td></td<></td>
 |
 | ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (a) POIL_AL Single model 2.0 m. undefined 7 ubb (b) DOINT Bit Input 1.0 m. 10 m. 10 m. Ubb (b) DOINT Bit Input 1.0 m. 10 m. 10 m. Ubb (b) DOINT Bit Input 1.0 m. 10 m. 10 m. Ubb (b) DOINT Bit Input 1.0 m. 10 m. UBb <td< td=""><td>Lab. [a] [b] [b]<td>Laik [a] [AUL_AL Single endod 2 Laik [a] [PD1_Phi Single endod 20 Laik [a] [PD1_Phi Single endod 10 is Laik [a] [PD1_Phi Single endod 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [Bit input [Bit input [Bit input<td>Lab. Iai Val.Lat. Stright ended 20 and under additional 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland
 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 100 nm<</td> 10 = Lab. Dot Dot Dot DOUTOL Bk logut 20 10 = 10 =<td>Lab. Init Init Stright ended 2 / 1 Lab. Init POT_R Stright ended 2 / 1 Lab. Init POT_AR Stright ended 2 / 1 Lab. Init POT_AR Stright ended 10 m Lab. Init POT_AR Stright ended 10 m Lab. Dot Dot Dot 10 m Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Pot Pot <</td><td>Lab. Lei Null_at Single model 21 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_AR Single model 100 7 100 Lab. Dot DOTO Bi lood 100 8 100 8 100</td><td>Latt Lat Party Latt Single encled 2 Dim Undefined 7 Latt Diptal input / output Single encled 2 Dim Undefined 7 Latt Diptal input / output Diptal input / output Single encled 100 min 10 min Latt Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input / output Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input</td><td>Ladia (a) (b) (c) (c)<!--</td--><td></td><td>Latla Isia PUL_AL Single ended 2 Dim Underside 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right Bit Right Dim Dim Dim Dim Latla Dim Dim</td><td>Lab. [a] [b] [b]<td>Lab. (e) PULL_AL Single ended 20 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 100 ms 10 s 7 -Ab. (b) DINT Bit finuk 20 ms 10 s 10 s -Ab. (b) DINT Bit finuk 100 ms 10 s 10 s -Ab. (b) DINT Bit finuk 10 s 10 s 10 s -Ab. (b) DINT</td></td></td></td></td></td<> | Lab. [a] [b] [b] <td>Laik [a] [AUL_AL Single endod 2 Laik [a] [PD1_Phi Single endod 20 Laik [a] [PD1_Phi Single endod 10 is Laik [a] [PD1_Phi Single endod 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [Bit input [Bit input [Bit input<td>Lab. Iai Val.Lat. Stright ended 20 and under additional 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai
POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 100 nm<</td> 10 = Lab. Dot Dot Dot DOUTOL Bk logut 20 10 = 10 =<td>Lab. Init Init Stright ended 2 / 1 Lab. Init POT_R Stright ended 2 / 1 Lab. Init POT_AR Stright ended 2 / 1 Lab. Init POT_AR Stright ended 10 m Lab. Init POT_AR Stright ended 10 m Lab. Dot Dot Dot 10 m Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Pot Pot <</td><td>Lab. Lei Null_at Single model 21 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_AR Single model 100 7 100 Lab. Dot DOTO Bi lood 100 8 100 8 100</td><td>Latt Lat Party Latt Single encled 2 Dim Undefined 7 Latt Diptal input / output Single encled 2 Dim Undefined 7 Latt Diptal input / output Diptal input / output Single encled 100 min 10 min Latt Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input / output Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input</td><td>Ladia (a) (b) (c) (c)<!--</td--><td></td><td>Latla Isia PUL_AL Single ended 2 Dim Underside 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right Bit Right Dim Dim Dim Dim Latla Dim Dim</td><td>Lab. [a] [b] [b]<td>Lab. (e) PULL_AL Single ended 20 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 100 ms 10 s 7 -Ab. (b) DINT Bit finuk 20 ms 10 s 10 s -Ab. (b) DINT Bit finuk 100 ms 10 s 10 s -Ab. (b) DINT Bit finuk 10 s 10 s 10 s -Ab. (b) DINT</td></td></td></td> | Laik [a] [AUL_AL Single endod 2 Laik [a] [PD1_Phi Single endod 20 Laik [a] [PD1_Phi Single endod 10 is Laik [a] [PD1_Phi Single endod 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [a] [PD1_Phi [Bit input 10 is Laik [Bit input [Bit input [Bit input <td>Lab. Iai Val.Lat. Stright ended 20 and under additional 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR
Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 100 nm<</td> 10 = Lab. Dot Dot Dot DOUTOL Bk logut 20 10 = 10 = <td>Lab. Init Init Stright ended 2 / 1 Lab. Init POT_R Stright ended 2 / 1 Lab. Init POT_AR Stright ended 2 / 1 Lab. Init POT_AR Stright ended 10 m Lab. Init POT_AR Stright ended 10 m Lab. Dot Dot Dot 10 m Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Pot Pot <</td> <td>Lab. Lei Null_at Single model 21 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_AR Single model 100 7 100 Lab. Dot DOTO Bi lood 100 8 100 8 100</td> <td>Latt Lat Party Latt Single encled 2 Dim Undefined 7 Latt Diptal input / output Single encled 2 Dim Undefined 7 Latt Diptal input / output Diptal input / output Single encled 100 min 10 min Latt Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input / output Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input</td> <td>Ladia (a) (b) (c) (c)<!--</td--><td></td><td>Latla Isia PUL_AL Single ended 2 Dim Underside 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right Bit Right Dim Dim Dim Dim Latla Dim Dim</td><td>Lab. [a] [b] [b]<td>Lab. (e) PULL_AL Single ended 20 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 100 ms 10 s 7 -Ab. (b) DINT Bit finuk 20 ms 10 s 10 s -Ab. (b) DINT Bit finuk 100 ms 10 s 10 s -Ab. (b) DINT Bit finuk 10 s 10 s 10 s -Ab. (b) DINT</td></td></td> | Lab. Iai Val.Lat. Stright ended 20 and under additional 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 20 and underland 7 Lab. Iai POT_FR Stright ended 100 nm<

 | Lab. Init Init Stright ended 2 / 1 Lab. Init POT_R Stright ended 2 / 1 Lab. Init POT_AR Stright ended 2 / 1 Lab. Init POT_AR Stright ended 10 m Lab. Init POT_AR Stright ended 10 m Lab. Dot Dot Dot 10 m Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Dot Pot Lab. Dot Dot Pot Pot <
 | Lab. Lei Null_at Single model 21 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_FL Single model 20 audinade 7 Lab. Lei POT_AR Single model 100 7 100 Lab. Dot DOTO Bi lood 100 8 100 8 100 | Latt Lat Party Latt Single encled 2 Dim Undefined 7 Latt Diptal input / output Single encled 2 Dim Undefined 7 Latt Diptal input / output Diptal input / output Single encled 100 min 10 min
 Latt Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input / output Diptal input / output Latt Diptal input / output Diptal input / output Bit input Diptal input | Ladia (a) (b) (c) (c) </td <td></td> <td>Latla Isia PUL_AL Single ended 2 Dim Underside 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right Bit Right Dim Dim Dim Dim Latla Dim Dim</td> <td>Lab. [a] [b] [b]<td>Lab. (e) PULL_AL Single ended 20 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 100 ms 10 s 7 -Ab. (b) DINT Bit finuk 20 ms 10 s 10 s -Ab. (b) DINT Bit finuk 100 ms 10 s 10 s -Ab. (b) DINT Bit finuk 10 s 10 s 10 s -Ab. (b) DINT</td></td>
 | | Latla Isia PUL_AL Single ended 2 Dim Underside 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Isingle ended 2 Dim Single ended 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right 2 Dim Single ended 7 Latla Dim Dim Bit Right Bit Right Dim Dim Dim Dim Latla Dim Dim | Lab. [a] [b] [b] <td>Lab. (e) PULL_AL Single ended 20 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 100 ms 10 s 7 -Ab. (b) DINT Bit finuk 20 ms 10 s 10 s -Ab. (b) DINT Bit finuk 100 ms 10 s 10 s
 -Ab. (b) DINT Bit finuk 10 s 10 s 10 s -Ab. (b) DINT</td> | Lab. (e) PULL_AL Single ended 20 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_PR Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 20 ms undefined 7 -Ab. (e) POT_AB Single ended 100 ms 10 s 7 -Ab. (b) DINT Bit finuk 20 ms 10 s 10 s -Ab. (b) DINT Bit finuk 100 ms 10 s 10 s -Ab. (b) DINT Bit finuk 10 s 10 s 10 s -Ab. (b) DINT |
| LB Idl Idl Vision Stripte model 2.0 ms underlined 7 LB Idl POT_PAL Stripte model 2.0 ms underlined 7 LB Idl POT_PAL Stripte model 2.0 ms underlined 7 LB Idl POT_AL Stripte model 2.0 ms underlined 7 LB Distripte model Idl Stripte model 2.0 ms undefined 7 LB Distripte model Distripte model Introduction 7 7 LB Distripte model Distripte model Introduction 7 LB Distripte model Bit legal Bit legal 8 LD Distripte model Distripte model Bit legal 8 LD </td <td>-ulb (a) P01_P1 Single model 2.0 mi undefined 7 -ubb (a) P01_P1 Single model 2.0 mi undefined 7 -ubb (a) P01_P4 Single model 2.0 mi undefined 7 -ubb (a) P01_P4 Single model 2.0 mi undefined 7 -ubb (a) P01_P4 Single model 2.0 mi undefined 7 -ubb (a) Single model 0.0 mi undefined 7 -ubb (a) Single model 10.0 mi 10.0 mi 10.0 mi -flat D01/D1 D01/D2 Bit fungut 10.0 mi 10.0 mi -flat D01/D1 Bit fungut Bit fungut 10.0 mi 10.0 mi 10.0 mi 10.0 mi -flat D01/D1 Bit fungut Bit fungut 10.0 mi 10.0 mi 10.0 mi 10.0 mi -flat D01/D1 Bit fungut Bit fungut 10.0 mi 10.0 mi <td< td=""><td>db. (a) (b) (c) (c)</td></td<><td></td><td>Lab. [a] <th]< th=""> [a]</th]<></td><td>Lab. [4] P01_P1 Single ended 2.0 ms undefined 7 Lab. [4] P01_P1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 10.0 ms 10.0 ms</td><td>-abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 10.0 ms 10 ms -abb (b) P01_PNL P01_PNL P01_PNL P01_PNL -abb (b) P01_PNL P01_PNL P01_PNL</td><td>Lab. (a) (a) (b) (c) (c)<td>Lab. (a) (a) (b) (c) (c)<td>Lab. (a) (a) (a) (b) (c) (c)<td>Table Iai PUID_FPT Straphenetide 2 Unit Understand 1 </td><td>Table Iai PUID_FPT Straphenetide 2 Units United integers 1 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable </td><td>Table Isia Pion Junit Surgitie ended 2 Units Understand 7 Cable Isia Pion Junit Surgitie ended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 7 Cable Disord Disord Disord 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 100 Dim survicifierended 7 Fable Disord Disord Disord Disord Disord 100 Dim survicifierended 100 Dim</td><td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Allb. (a) (b) (c) (c)<!--</td--><td>Lab. [4] [40] P01_P1L Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 100 ms 10 ms 10 ms Lab. [4] P01_PL Bit lock 100 ms 10 ms 10 ms LBL [4] P01/12 Bit lock 10 ms 10 ms 10 ms LBL [4] [4] P01/12 Bit lock 10 ms 10 ms LBL [4] [4] [4] [4] [4] [4] [4] LBL [4] [4] [4] [4] [4] [4] [4]</td><td>Lab. [a] [b] [b] [c] [c]<td>Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 100 ms 10 s 10 s Lab. [b] [b] [b] P01 Bit locut 10 ms 10 s Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b</td><td>Lab. [4]<td>Lab. [4]
 [4] [4]<td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td></td></td></td></td></td></td></td></td></td></td></td> | -ulb (a) P01_P1 Single model 2.0 mi undefined 7 -ubb (a) P01_P1 Single model 2.0 mi undefined 7 -ubb (a) P01_P4 Single model 2.0 mi undefined 7 -ubb (a) P01_P4 Single model 2.0 mi undefined 7 -ubb (a) P01_P4 Single model 2.0 mi undefined 7 -ubb (a) Single model 0.0 mi undefined 7 -ubb (a) Single model 10.0 mi 10.0 mi 10.0 mi -flat D01/D1 D01/D2 Bit fungut 10.0 mi 10.0 mi -flat D01/D1 Bit fungut Bit fungut 10.0 mi 10.0 mi 10.0 mi 10.0 mi -flat D01/D1 Bit fungut Bit fungut 10.0 mi 10.0 mi 10.0 mi 10.0 mi -flat D01/D1 Bit fungut Bit fungut 10.0 mi 10.0 mi <td< td=""><td>db. (a) (b) (c) (c)</td></td<> <td></td> <td>Lab. [a] <th]< th=""> [a]</th]<></td> <td>Lab. [4] P01_P1 Single ended 2.0 ms undefined 7 Lab. [4] P01_P1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 10.0 ms 10.0 ms</td> <td>-abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 10.0 ms 10 ms -abb (b) P01_PNL P01_PNL P01_PNL P01_PNL -abb (b) P01_PNL P01_PNL P01_PNL</td> <td>Lab. (a) (a) (b) (c) (c)<td>Lab. (a) (a) (b) (c) (c)<td>Lab. (a) (a) (a) (b) (c) (c)<td>Table Iai PUID_FPT Straphenetide 2 Unit Understand 1 </td><td>Table Iai PUID_FPT Straphenetide 2 Units United integers 1 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable </td><td>Table Isia Pion Junit Surgitie ended 2 Units Understand 7 Cable Isia Pion Junit Surgitie ended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 7 Cable Disord Disord Disord 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 100 Dim survicifierended 7 Fable Disord Disord Disord Disord Disord 100 Dim survicifierended 100 Dim</td><td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Allb. (a) (b) (c) (c)<!--</td--><td>Lab. [4] [40] P01_P1L Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 100 ms 10 ms 10 ms Lab. [4] P01_PL Bit lock 100 ms 10 ms 10 ms LBL [4] P01/12 Bit lock 10 ms 10 ms 10 ms LBL [4] [4] P01/12 Bit lock 10 ms 10 ms LBL [4]
 [4] [4] [4] [4] [4] [4] LBL [4] [4] [4] [4] [4] [4] [4]</td><td>Lab. [a] [b] [b] [c] [c]<td>Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 100 ms 10 s 10 s Lab. [b] [b] [b] P01 Bit locut 10 ms 10 s Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b</td><td>Lab. [4]<td>Lab. [4]<td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td></td></td></td></td></td></td></td></td></td></td>
 | db. (a) (b) (c)

 |

 | Lab. [a] [a] <th]< th=""> [a]</th]<>

 | Lab. [4] P01_P1 Single ended 2.0 ms undefined 7 Lab. [4] P01_P1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 2.0 ms undefined 7 Lab. [4] P01_D1 Single ended 10.0 ms

 | -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 2.0 ms undefined ? -abb (a) P01_PNL Single ended 10.0 ms 10 ms -abb (b) P01_PNL P01_PNL P01_PNL P01_PNL -abb (b) P01_PNL P01_PNL P01_PNL

 | Lab. (a) (a) (b) (c) (c) <td>Lab. (a) (a) (b) (c) (c)<td>Lab. (a) (a) (a) (b) (c) (c)<td>Table Iai PUID_FPT Straphenetide 2 Unit Understand 1 </td><td>Table Iai PUID_FPT Straphenetide 2 Units United integers 1 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable </td><td>Table Isia Pion Junit Surgitie ended 2 Units Understand 7 Cable Isia Pion Junit Surgitie ended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 7 Cable Disord Disord Disord 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 100 Dim survicifierended 7 Fable Disord Disord Disord Disord Disord 100 Dim survicifierended 100 Dim</td><td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Allb. (a) (b) (c) (c)<!--</td--><td>Lab. [4] [40] P01_P1L Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 100 ms 10 ms 10 ms Lab. [4] P01_PL Bit lock 100 ms 10 ms 10 ms LBL [4] P01/12 Bit lock 10 ms 10 ms 10 ms LBL [4] [4] P01/12 Bit lock 10 ms 10 ms LBL [4] [4] [4] [4] [4] [4] [4] LBL [4] [4] [4] [4] [4] [4] [4]</td><td>Lab. [a] [b] [b] [c] [c]<td>Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 100 ms 10 s 10 s Lab. [b] [b] [b] P01 Bit locut 10 ms 10 s Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b</td><td>Lab. [4]<td>Lab. [4]<td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td></td></td></td></td></td></td></td></td></td>
 | Lab. (a) (a) (b) (c) (c) <td>Lab. (a) (a) (a) (b) (c) (c)<td>Table Iai PUID_FPT Straphenetide 2 Unit Understand 1 </td><td>Table Iai PUID_FPT Straphenetide 2 Units United integers 1 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable </td><td>Table Isia Pion Junit Surgitie ended 2 Units Understand 7 Cable Isia Pion Junit Surgitie ended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 7 Cable Disord Disord Disord 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 100 Dim survicifierended 7 Fable Disord Disord Disord Disord Disord 100 Dim survicifierended 100 Dim</td><td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Allb. (a) (b) (c) (c)<!--</td--><td>Lab. [4] [40] P01_P1L Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 100 ms 10 ms 10 ms Lab. [4] P01_PL Bit lock 100 ms 10 ms 10 ms LBL [4] P01/12 Bit lock 10 ms 10 ms 10 ms LBL [4] [4] P01/12 Bit lock 10 ms 10 ms LBL [4] [4] [4] [4] [4] [4] [4] LBL [4] [4] [4] [4] [4] [4] [4]</td><td>Lab. [a] [b] [b] [c] [c]<td>Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 100 ms 10 s 10 s Lab. [b] [b] [b] P01 Bit locut 10 ms 10 s Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b</td><td>Lab. [4]<td>Lab. [4]<td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c)
(c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td></td></td></td></td></td></td></td></td> | Lab. (a) (a) (a) (b) (c) (c) <td>Table Iai PUID_FPT Straphenetide 2 Unit Understand 1 </td> <td>Table Iai PUID_FPT Straphenetide 2 Units United integers 1 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable </td> <td>Table Isia Pion Junit Surgitie ended 2 Units Understand 7 Cable Isia Pion Junit Surgitie ended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 7 Cable Disord Disord Disord 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 100 Dim survicifierended 7 Fable Disord Disord Disord Disord Disord 100 Dim survicifierended 100 Dim</td> <td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Allb. (a) (b) (c) (c)<!--</td--><td>Lab. [4] [40] P01_P1L Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 100 ms 10 ms 10 ms Lab. [4] P01_PL Bit lock 100 ms 10 ms 10 ms LBL [4] P01/12 Bit lock 10 ms 10 ms 10 ms LBL [4] [4] P01/12 Bit lock 10 ms 10 ms LBL [4] [4] [4] [4] [4] [4] [4] LBL [4] [4] [4] [4] [4] [4] [4]</td><td>Lab. [a] [b] [b] [c] [c]<td>Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 100 ms 10 s 10 s Lab. [b] [b] [b] P01 Bit locut 10 ms 10 s Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b</td><td>Lab. [4]<td>Lab. [4]<td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td></td></td></td></td></td></td></td>
 | Table Iai PUID_FPT Straphenetide 2 Unit Understand 1

 | Table Iai PUID_FPT Straphenetide 2 Units United integers 1 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable Iai POIT_AL Straphenetide 2 0 ms undefined 7 Cable

 | Table Isia Pion Junit Surgitie ended 2 Units Understand 7 Cable Isia Pion Junit Surgitie ended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 2 Dim survicifiered 7 Cable Isia Pion Junit Surgitierended 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 7 Cable Disord Disord Disord 100 Dim survicifierended 7 Cable Disord Disord Disord Disord 100 Dim survicifierended 7 Fable Disord Disord Disord Disord Disord 100 Dim survicifierended 100 Dim

 | Lab. (a) (b) (c) (c) <td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Allb. (a) (b) (c) (c)<!--</td--><td>Lab. [4] [40] P01_P1L Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single
endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 100 ms 10 ms 10 ms Lab. [4] P01_PL Bit lock 100 ms 10 ms 10 ms LBL [4] P01/12 Bit lock 10 ms 10 ms 10 ms LBL [4] [4] P01/12 Bit lock 10 ms 10 ms LBL [4] [4] [4] [4] [4] [4] [4] LBL [4] [4] [4] [4] [4] [4] [4]</td><td>Lab. [a] [b] [b] [c] [c]<td>Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 100 ms 10 s 10 s Lab. [b] [b] [b] P01 Bit locut 10 ms 10 s Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b</td><td>Lab. [4]<td>Lab. [4]<td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td></td></td></td></td></td></td> | Lab. (a) (b) (b) (c) (c) <td>Lab. (a) (b) (c) (c)<td>Allb. (a) (b) (c) (c)<!--</td--><td>Lab. [4] [40] P01_P1L Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 100 ms 10 ms 10 ms Lab. [4] P01_PL Bit lock 100 ms 10 ms 10 ms LBL [4] P01/12 Bit lock 10 ms 10 ms 10 ms LBL [4] [4] P01/12 Bit lock 10 ms 10 ms LBL [4] [4] [4] [4] [4] [4] [4] LBL [4] [4] [4] [4] [4]
[4] [4]</td><td>Lab. [a] [b] [b] [c] [c]<td>Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 100 ms 10 s 10 s Lab. [b] [b] [b] P01 Bit locut 10 ms 10 s Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b</td><td>Lab. [4]<td>Lab. [4]<td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td></td></td></td></td></td> | Lab. (a) (b) (c) (c) <td>Allb. (a) (b) (c) (c)<!--</td--><td>Lab. [4] [40] P01_P1L Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 100 ms 10 ms 10 ms Lab. [4] P01_PL Bit lock 100 ms 10 ms 10 ms LBL [4] P01/12 Bit lock 10 ms 10 ms 10 ms LBL [4] [4] P01/12 Bit lock 10 ms 10 ms LBL [4] [4] [4] [4] [4] [4] [4] LBL [4] [4] [4] [4] [4] [4] [4]</td><td>Lab. [a] [b] [b] [c] [c]<td>Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 100 ms 10 s 10 s Lab. [b] [b] [b] P01 Bit locut 10 ms 10 s Lab. [b] [b] [b] [b] [b]
[b] [b] Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b</td><td>Lab. [4]<td>Lab. [4]<td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td></td></td></td></td> | Allb. (a) (b) (c) (c) </td <td>Lab. [4] [40] P01_P1L Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 100 ms 10 ms 10 ms Lab. [4] P01_PL Bit lock 100 ms 10 ms 10 ms LBL [4] P01/12 Bit lock 10 ms 10 ms 10 ms LBL [4] [4] P01/12 Bit lock 10 ms 10 ms LBL [4] [4] [4] [4] [4] [4] [4] LBL [4] [4] [4] [4] [4] [4] [4]</td> <td>Lab. [a] [b] [b] [c] [c]<td>Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 100 ms 10 s 10 s Lab. [b] [b] [b] P01 Bit locut 10 ms 10 s Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b</td><td>Lab. [4]<td>Lab. [4]<td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td></td></td></td>
 | Lab. [4] [40] P01_P1L Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 20 ms undefined 7 Lab. [4] P01_PL Single endod 100 ms 10 ms 10 ms Lab. [4] P01_PL Bit lock 100 ms 10 ms 10 ms LBL [4] P01/12 Bit lock 10 ms 10 ms 10 ms LBL [4] [4] P01/12 Bit lock 10 ms 10 ms LBL [4] [4] [4] [4] [4] [4] [4] LBL [4] [4] [4] [4] [4] [4] [4]
 | Lab. [a] [b] [b] [c] [c] <td>Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 100 ms 10 s 10 s Lab. [b] [b] [b] P01 Bit locut 10 ms 10 s Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b</td> <td>Lab. [4]
[4] [4] [4]<td>Lab. [4]<td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td></td></td> | Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [a] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 2.0 ms undefined 7 Lab. [b] P01_P1 Single ended 100 ms 10 s 10 s Lab. [b] [b] [b] P01 Bit locut 10 ms 10 s Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b] Lab. [b] [b] [b] [b] [b] [b] [b
 | Lab. [4] <td>Lab. [4]<td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td></td>
 | Lab. [4] <td>Lam (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td></td> | Lam (a) (b) (c) (c) <td>Lab. (a) (b) (b) (c) (c)<td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td></td> | Lab. (a) (b) (b) (c) (c) <td>Lab. (a) (b) (c) (c)<td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td></td> | Lab. (a) (b)
 (c) (c) <td>Lab. (a) (b) (b) (c) (c)<td>India [a] [b] [b]<!--</td--><td></td></td></td> | Lab. (a) (b) (b) (c) (c) <td>India [a] [b] [b]<!--</td--><td></td></td> | India [a] [b] [b] </td <td></td> | |
| Image: Statution of the statute Image: Statute Image: Statute Image: Statute Image: Statute </td <td></td> <td>Bit 14 1011/16 310generation 20 min 400000000 7 Bit 16 P017_AL Single-model 20 min 4000000000 7 Bit 16 P017_AL Single-model 20 min 40000000000000 7 Bit 00 min D0 min 10 min 10 min 10 min 10 min Bit 00 min D0 min D0 min 10 min 10 min 10 min Bit 00 min D0 min D0 min 10 min 10 min 10 min Bit 00 min D0 min Bit flops/ 10 min 10 min Bit 00 min D0 min Bit flops/ 10 min 10 min Bit 00 min D0 min Bit flops/ 10 min 10 min Bit 00 min D0 min Bit flops/ 10 min 10 min Bit 00 min D0 min Bit flops/ 10 min 10 min Bit 00 min Bit flops/ Bit flops/</td> <td>Table (ii) (iii) Stright ended 2 Unit Units 7 Construct (iii) (iiii) Stright ended 2 O m undefined 7 Construct (iiii) (iiii) Stright ended 2 O m undefined 7 Construct (iiii) Stright ended 2 O m undefined 7 Construct (iiii) Stright ended 10 is 10 is 10 is FAL Dent Dent Dent Dent Dent </td> <td>-AB. [4] P01_PL Single erobit 2.0 ms Underside 7 -AB. [4] P01_PL Single erobit 2.0 ms Underside 7 -AB. [4] P01_PL Single erobit 2.0 ms Underside 7 -AB. [4] P01_PL Single erobit 2.0 ms Underside 7 -AB. Disk lends/ </td> <td>Lab. 14 POT_FAR Strapheneode 2.0 Interviewei 7 Lab. 16 POT_FAR Strapheneode 2.0 Interviewei 7 Lab. 10 POT_FAR Strapheneode 2.0 Interviewei 7 Lab. 2.0 Interviewei Strapheneode 2.0 Interviewei 7 Lab. Detail interviewei Strapheneode 10.0 Interviewei 7 HAB. Detail Detail Detail Detail 10.0 8 HAB. Detail Detail Detail Detail 10.0 8 HAB. Detail Detail Detail Detail 10.0 8 HAB. Detail Detail Detail Detail 10.0 10.0 HAB. Detail Detail Detail 10.0 10.0 10.0 HAB. Detail Detail Detail 10.0 10.0 10.0 HAB. Detail</td> <td></td> <td>Lab. (a) (b) (c) (c)<td>Lab. Leit Leit Sangle encode 2.0 ms Galance 7 Lab. Leit PDT_AL Sangle encode 2.0 ms undefined 7 Lab. Leit PDT_AL Sangle encode 2.0 ms undefined 7 Lab. Leit PDT_AL Sangle encode 2.0 ms undefined 7 Lab. Lob Calanced - Lobard Sangle encode 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DOVID Bit Input 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DOVID Bit Input 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DDVID DEVID DEVID DEVID 10.0 ms 10.0 ms HAB. DPLAP DEVID DEVID DEVID 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DEVID DEVID 10.0 ms 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DEVID DEVID</td><td>Table (a) (a)<!--</td--><td>Table Isia Pol Tr. Straght ended 2.0 mm Understand 7 Cable Isia POl Tr. Straght ended 2.0 mm undefined 7 Cable Isia POl Tr. Straght ended 2.0 mm undefined 7 Cable Isia Pol Tr. Straght ended 2.0 mm undefined 7 Cable Isia Pol Pol Tr. Straght ended 10 mm 10 mm 10 mm FAIL Pol Pol Tr. Straght ended 10 mm 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Pol Pol Pol Pol Pol Pol Pol</td><td>Table Isia Pol / Tk Single mode 2 Um Generation 7 Table Isia POl / Tk Single mode 2 0 m undefined 7 Table Isia POl / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 10 0 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Pol / Tk 10 0 10 0 10 0 10 0 10 0 10 0 <</td><td>Table Isia Pol / PL Single enclose 2.0 ms Generation 7 Construction A POI / AL Single enclose 2.0 ms undefined 7 Construction A POI / AL Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 FAIL Division Single enclose 10.0 ms 10.0 ms FAIL Division Division Bit Input 10.0 ms<td>Table (a) (b) (c) Single ended (c) (c)</td><td>Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms</td><td>Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)</td><td>db (a) (b) (c) (c)</td><td>Lab. Linit Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe method 20 m undefined 7 Lab. Dotal methods Strippe method 20 m undefined 7 Lab. Dotal methods Strippe method 100 0 ms 10 s 10 s Lab. Dotal Dotal methods Strippe methods 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab.
 Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s</td><td>Table [s] Point_Fin Support model 2 Dim Modelment 7 Table [s] Point_Fin Support model 2 Dim Modelment 7 Table [s] Point_Finance Support model 2 Dim Modelment 7 Table [s] Point_Finance Support model 2 Dim Modelment 7 Table [s] Point_Finance Support modelment 2 Dim 10 s Table [s] Point_Finance Bit model 100 Dim 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model<td>Lab. 61 1011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 3 3 3 Lab. Double import / outpart Double import / outp</td><td>Image: Part of the strength encode 2 0 mm Strength encode 1 00 0 mm To a strength encode 1 0 mm<!--</td--><td>Image: Product of the structure of</td><td>Table (a) (b) (c) (c)<!--</td--><td>Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms</td><td>Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)</td><td>Table (a) (b) Top Tx Stoppe metal 20 Modeline 7 Ch (a) (b) (c) (c)</td><td>Table (a) (b) Table for the strength ender of the strength</td><td>Table (a) (b) Stripper receipt 2.0 Understand 7 Cable (c) POT_AL Stripper receipt 2.0 understand 7 -FAL Dot DNIN1 Bit input 10.0 10.6 10.0 -FAL Dot DNIN1 Bit input Bit input 10.4 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input 10.0 10.0 -FAL DOL DNIN1 Bit input 10.0</td></td></td></td></td></td></td> |

 | Bit 14 1011/16 310generation 20 min 400000000 7 Bit 16 P017_AL Single-model 20 min 4000000000 7 Bit 16 P017_AL Single-model 20 min 40000000000000 7 Bit 00 min D0 min 10 min 10 min 10 min 10 min Bit 00 min D0 min D0 min 10 min 10 min 10 min Bit 00 min D0 min D0 min 10 min 10 min 10 min Bit 00 min D0 min Bit flops/ 10 min 10 min Bit 00 min D0 min Bit flops/ 10 min 10 min Bit 00 min D0 min Bit flops/ 10 min 10 min Bit 00 min D0 min Bit flops/ 10 min 10 min Bit 00 min D0 min Bit flops/ 10 min 10 min Bit 00 min Bit flops/ Bit flops/

 | Table (ii) (iii) Stright ended 2 Unit Units 7 Construct (iii) (iiii) Stright ended 2 O m undefined 7 Construct (iiii) (iiii) Stright ended 2 O m undefined 7 Construct (iiii) Stright ended 2 O m undefined 7 Construct (iiii) Stright ended 10 is 10 is 10 is FAL Dent Dent Dent Dent Dent

 | -AB. [4] P01_PL Single erobit 2.0 ms Underside 7 -AB. [4] P01_PL Single erobit 2.0 ms Underside 7 -AB. [4] P01_PL Single erobit 2.0 ms Underside 7 -AB. [4] P01_PL Single erobit 2.0 ms Underside 7 -AB. Disk lends/

 | Lab. 14 POT_FAR Strapheneode 2.0 Interviewei 7 Lab. 16 POT_FAR Strapheneode 2.0 Interviewei 7 Lab. 10 POT_FAR Strapheneode 2.0 Interviewei 7 Lab. 2.0 Interviewei Strapheneode 2.0 Interviewei 7 Lab. Detail interviewei Strapheneode 10.0 Interviewei 7 HAB. Detail Detail Detail Detail 10.0 8 HAB. Detail Detail Detail Detail 10.0 8 HAB. Detail Detail Detail Detail 10.0 8 HAB. Detail Detail Detail Detail 10.0 10.0 HAB. Detail Detail Detail 10.0 10.0 10.0 HAB. Detail Detail Detail 10.0 10.0
 10.0 HAB. Detail

 |

 | Lab. (a) (b) (c) (c) <td>Lab. Leit Leit Sangle encode 2.0 ms Galance 7 Lab. Leit PDT_AL Sangle encode 2.0 ms undefined 7 Lab. Leit PDT_AL Sangle encode 2.0 ms undefined 7 Lab. Leit PDT_AL Sangle encode 2.0 ms undefined 7 Lab. Lob Calanced - Lobard Sangle encode 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DOVID Bit Input 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DOVID Bit Input 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DDVID DEVID DEVID DEVID 10.0 ms 10.0 ms HAB. DPLAP DEVID DEVID DEVID 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DEVID DEVID 10.0 ms 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DEVID DEVID</td> <td>Table (a) (a)<!--</td--><td>Table Isia Pol Tr. Straght ended 2.0 mm Understand 7 Cable Isia POl Tr. Straght ended 2.0 mm undefined 7 Cable Isia POl Tr. Straght ended 2.0 mm undefined 7 Cable Isia Pol Tr. Straght ended 2.0 mm undefined 7 Cable Isia Pol Pol Tr. Straght ended 10 mm 10 mm 10 mm FAIL Pol Pol Tr. Straght ended 10 mm 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Pol Pol Pol Pol Pol Pol Pol</td><td>Table Isia Pol / Tk Single mode 2 Um Generation 7 Table Isia POl / Tk Single mode 2 0 m undefined 7 Table Isia POl / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 10 0 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Pol / Tk 10 0 10 0 10 0 10 0 10 0 10 0 <</td><td>Table Isia Pol / PL Single enclose 2.0 ms Generation 7 Construction A POI / AL Single enclose 2.0 ms undefined 7 Construction A POI / AL Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 FAIL Division Single enclose 10.0 ms 10.0 ms FAIL Division Division Bit Input 10.0 ms<td>Table (a) (b) (c) Single ended (c) (c)</td><td>Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms</td><td>Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)</td><td>db (a) (b) (c) (c)</td><td>Lab. Linit Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe method 20 m undefined 7 Lab. Dotal methods Strippe method 20 m undefined 7 Lab. Dotal methods Strippe method 100 0 ms 10 s 10 s Lab. Dotal Dotal methods Strippe methods 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s</td><td>Table [s] Point_Fin Support model 2 Dim Modelment 7 Table [s] Point_Fin Support model 2 Dim Modelment 7 Table [s] Point_Finance Support model 2 Dim Modelment 7 Table [s] Point_Finance Support model 2 Dim Modelment 7 Table [s] Point_Finance Support modelment 2 Dim 10 s Table [s] Point_Finance Bit model 100 Dim 10 s Table [s] Point_Finance Bit model
10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model<td>Lab. 61 1011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 3 3 3 Lab. Double import / outpart Double import / outp</td><td>Image: Part of the strength encode 2 0 mm Strength encode 1 00 0 mm To a strength encode 1 0 mm<!--</td--><td>Image: Product of the structure of</td><td>Table (a) (b) (c) (c)<!--</td--><td>Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms</td><td>Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)</td><td>Table (a) (b) Top Tx Stoppe metal 20 Modeline 7 Ch (a) (b) (c) (c)</td><td>Table (a) (b) Table for the strength ender of the strength</td><td>Table (a) (b) Stripper receipt 2.0 Understand 7 Cable (c) POT_AL Stripper receipt 2.0 understand 7 -FAL Dot DNIN1 Bit input 10.0 10.6 10.0 -FAL Dot DNIN1 Bit input Bit input 10.4 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input 10.0 10.0 -FAL DOL DNIN1 Bit input 10.0</td></td></td></td></td></td> | Lab. Leit Leit Sangle encode 2.0 ms Galance 7 Lab. Leit PDT_AL Sangle encode 2.0 ms undefined 7 Lab. Leit PDT_AL Sangle encode 2.0 ms undefined 7 Lab. Leit PDT_AL Sangle encode 2.0 ms undefined 7 Lab. Lob Calanced - Lobard Sangle encode 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DOVID Bit Input 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DOVID Bit Input 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DDVID DEVID DEVID DEVID 10.0 ms 10.0 ms HAB. DPLAP DEVID DEVID DEVID 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DEVID DEVID 10.0 ms 10.0 ms 10.0 ms 10.0 ms HAB. DPLAP DEVID DEVID

 | Table (a) (a) </td <td>Table Isia Pol Tr. Straght ended 2.0 mm Understand 7 Cable Isia POl Tr. Straght ended 2.0 mm undefined 7 Cable Isia POl Tr. Straght ended 2.0 mm undefined 7 Cable Isia Pol Tr. Straght ended 2.0 mm undefined 7 Cable Isia Pol Pol Tr. Straght ended 10 mm 10 mm 10 mm FAIL Pol Pol Tr. Straght ended 10 mm 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Pol Pol Pol Pol Pol Pol Pol</td> <td>Table Isia Pol / Tk Single mode 2 Um Generation 7 Table Isia POl / Tk Single mode 2 0 m undefined 7 Table Isia POl / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 10 0 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Pol / Tk 10 0 10 0 10 0 10 0 10 0 10 0 <</td> <td>Table Isia Pol / PL Single enclose 2.0 ms Generation 7 Construction A POI / AL Single enclose 2.0 ms undefined
7 Construction A POI / AL Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 FAIL Division Single enclose 10.0 ms 10.0 ms FAIL Division Division Bit Input 10.0 ms<td>Table (a) (b) (c) Single ended (c) (c)</td><td>Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms</td><td>Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)</td><td>db (a) (b) (c) (c)</td><td>Lab. Linit Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe method 20 m undefined 7 Lab. Dotal methods Strippe method 20 m undefined 7 Lab. Dotal methods Strippe method 100 0 ms 10 s 10 s Lab. Dotal Dotal methods Strippe methods 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s</td><td>Table [s] Point_Fin Support model 2 Dim Modelment 7 Table [s] Point_Fin Support model 2 Dim Modelment 7 Table [s] Point_Finance Support model 2 Dim Modelment 7 Table [s] Point_Finance Support model 2 Dim Modelment 7 Table [s] Point_Finance Support modelment 2 Dim 10 s Table [s] Point_Finance Bit model 100 Dim 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model<td>Lab. 61 1011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 3 3 3 Lab. Double import / outpart Double import / outp</td><td>Image: Part of the strength encode 2 0 mm Strength encode 1 00 0 mm To a strength encode 1 0 mm<!--</td--><td>Image: Product of the structure of</td><td>Table (a) (b) (c) (c)<!--</td--><td>Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms</td><td>Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)</td><td>Table (a) (b) Top Tx Stoppe metal 20 Modeline 7 Ch (a) (b) (c) (c)</td><td>Table (a) (b) Table for the strength ender of the strength</td><td>Table (a) (b) Stripper receipt 2.0 Understand 7 Cable (c) POT_AL Stripper receipt 2.0 understand 7 -FAL Dot DNIN1 Bit input 10.0 10.6 10.0 -FAL Dot DNIN1 Bit input Bit input 10.4 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input 10.0 10.0 -FAL DOL DNIN1 Bit input 10.0</td></td></td></td></td> | Table Isia Pol Tr. Straght ended 2.0 mm Understand 7 Cable Isia POl Tr. Straght ended 2.0 mm undefined 7 Cable Isia POl Tr. Straght ended 2.0 mm undefined 7 Cable Isia Pol Tr. Straght ended 2.0 mm undefined 7 Cable Isia Pol Pol Tr. Straght ended 10 mm 10 mm 10 mm FAIL Pol Pol Tr. Straght ended 10 mm 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol
Pol Tr. Straght ended 10 mm 10 mm FAIL Pol Pol Pol Tr. Straght ended 10 mm 10 mm FAIL Pol

 | Table Isia Pol / Tk Single mode 2 Um Generation 7 Table Isia POl / Tk Single mode 2 0 m undefined 7 Table Isia POl / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 2 0 m undefined 7 Table Isia Pol / Tk Single mode 10 0 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Single mode 10 0 10 0 Table Pol / Tk Pol / Tk Pol / Tk 10 0 10 0 10 0 10 0 10 0 10 0 <

 | Table Isia Pol / PL Single enclose 2.0 ms Generation 7 Construction A POI / AL Single enclose 2.0 ms undefined 7 Construction A POI / AL Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 Construction Single enclose 2.0 ms undefined 7 FAIL Division Single enclose 10.0 ms 10.0 ms FAIL Division Division Bit Input 10.0 ms <td>Table (a) (b) (c) Single ended (c) (c)</td> <td>Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms</td> <td>Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)</td> <td>db (a) (b) (c) (c)</td> <td>Lab. Linit Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe method 20 m undefined 7 Lab. Dotal methods Strippe method 20 m undefined 7 Lab. Dotal methods Strippe method 100 0 ms 10 s 10 s Lab. Dotal Dotal methods Strippe methods 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s</td> <td>Table [s] Point_Fin Support model 2 Dim Modelment 7 Table [s] Point_Fin Support model 2 Dim Modelment 7 Table [s] Point_Finance Support model 2 Dim Modelment 7 Table [s] Point_Finance Support model 2 Dim Modelment 7 Table [s] Point_Finance Support modelment 2 Dim 10 s Table [s] Point_Finance Bit model 100 Dim 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model<td>Lab. 61 1011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 3 3 3 Lab. Double import / outpart Double import / outp</td><td>Image: Part of the strength encode 2 0 mm Strength encode 1 00 0 mm To a strength encode 1 0 mm<!--</td--><td>Image: Product of the structure of</td><td>Table (a) (b) (c) (c)<!--</td--><td>Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms</td><td>Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)</td><td>Table (a) (b) Top Tx Stoppe metal 20 Modeline 7 Ch (a) (b) (c) (c)</td><td>Table (a) (b) Table for the strength ender of the strength</td><td>Table (a) (b) Stripper receipt 2.0 Understand 7 Cable (c) POT_AL Stripper receipt 2.0 understand 7 -FAL Dot DNIN1 Bit input 10.0 10.6 10.0 -FAL Dot DNIN1 Bit input Bit input 10.4 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input 10.0 10.0 -FAL DOL DNIN1 Bit input 10.0</td></td></td></td> | Table (a) (b) (c) Single ended (c)

 | Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms

 | Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)

 | db (a) (b) (c)
 | Lab. Linit Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe methods 20 m Model method 7 Lab. Linit FOT_FM Strippe method 20 m undefined 7 Lab. Dotal methods Strippe method 20 m undefined 7 Lab. Dotal methods Strippe method 100 0 ms 10 s 10 s Lab. Dotal Dotal methods Strippe methods 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s Lab. Dotal Dotal Bit next 10 s 10 s

 | Table [s] Point_Fin Support model 2 Dim Modelment 7 Table [s] Point_Fin Support model 2 Dim Modelment 7 Table [s] Point_Finance Support model 2 Dim Modelment 7 Table [s] Point_Finance Support model 2 Dim Modelment 7 Table [s] Point_Finance Support modelment 2 Dim 10 s Table [s] Point_Finance Bit model 100 Dim 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model 10 s 10 s Table [s] Point_Finance Bit model <td>Lab. 61 1011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 3 3 3 Lab. Double import / outpart Double import / outp</td> <td>Image: Part of the strength encode 2 0 mm Strength encode 1 00 0 mm To a strength encode 1 0 mm<!--</td--><td>Image: Product of the structure of</td><td>Table (a) (b) (c) (c)<!--</td--><td>Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms</td><td>Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)</td><td>Table (a) (b) Top Tx Stoppe metal 20 Modeline 7 Ch (a) (b) (c) (c)</td><td>Table (a) (b) Table for the strength ender of the strength</td><td>Table (a) (b) Stripper receipt 2.0 Understand 7 Cable (c) POT_AL Stripper receipt 2.0 understand 7 -FAL Dot DNIN1 Bit input 10.0 10.6 10.0 -FAL Dot DNIN1 Bit input Bit input 10.4 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input 10.0 10.0 -FAL DOL DNIN1 Bit input 10.0</td></td></td> | Lab. 61 1011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th 30100 mmode 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. 10 P011_F/th Single mode/ 2.0 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 Mode/med 2 Lab. Double import / outpart Bit imput Mode/med 2 3 3 3 Lab. Double import / outpart Double import / outp
 | Image: Part of the strength encode 2 0 mm Strength encode 1 00 0 mm To a strength encode 1 0 mm </td <td>Image: Product of the structure of</td> <td>Table (a) (b) (c) (c)<!--</td--><td>Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms</td><td>Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)</td><td>Table (a) (b) Top Tx Stoppe metal 20 Modeline 7 Ch (a) (b) (c) (c)</td><td>Table (a) (b) Table for the strength ender of the strength</td><td>Table (a) (b) Stripper receipt 2.0 Understand 7 Cable (c) POT_AL Stripper receipt 2.0 understand 7 -FAL Dot DNIN1 Bit input 10.0 10.6 10.0 -FAL Dot DNIN1
Bit input Bit input 10.4 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input 10.0 10.0 -FAL DOL DNIN1 Bit input 10.0</td></td> | Image: Product of the structure of | Table (a) (b) (c) (c) </td <td>Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms</td> <td>Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii)</td> <td>Table (a) (b) Top Tx Stoppe metal 20 Modeline 7 Ch (a) (b) (c) (c)</td> <td>Table (a) (b) Table for the strength ender of the strength</td> <td>Table (a) (b) Stripper receipt 2.0 Understand 7 Cable (c) POT_AL Stripper receipt 2.0 understand 7 -FAL Dot DNIN1 Bit input 10.0 10.6 10.0 -FAL Dot DNIN1 Bit input Bit input 10.4 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input 10.0 10.0 -FAL DOL DNIN1 Bit input 10.0</td> | Table (ii) POT Table Single model 2 0 m. Modelment 7 Cable (i) POT Table Single model 2 0 m. Modelment 7 Cable (ii) POT Table Single model 2 0 m. Modelment 7 Cable (iii) POT Table Single model 2 0 m. Modelment 7 Cable (iiii) POT Table Single model 100.0 ms 10 s 10 ms - Table POT Table Bit Input Bit Input 100.0 ms 10 s 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms 10 ms 10 ms 10 ms - Table POT Table Bit Input Bit Input 10 ms
 | Table (ii) (iii) (iii) (iii) (iii) (iii) (iii) (iii) (iiii) (iiiiii) | Table (a) (b) Top Tx Stoppe metal 20 Modeline 7 Ch (a) (b) (c)
 | Table (a) (b) Table for the strength ender of the strength | Table (a) (b) Stripper receipt 2.0 Understand 7 Cable (c) POT_AL Stripper receipt 2.0 understand 7 -FAL Dot DNIN1 Bit input 10.0 10.6 10.0 -FAL Dot DNIN1 Bit input Bit input 10.4 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL Dot DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input Bit input 10.0 -FAL DOL DNIN1 Bit input 10.0 10.0 -FAL DOL DNIN1 Bit input 10.0 |
| Image: Statut Image: Statut

 | Image: Section 2017 S

 | Base (a) POTI_A Single model 2.0 mi Underset 7 PopIal PotI_A Single model 2.0 mi Underset 7 DipIal PotI_A Single model 0.0 mi 10.0 mi 10.0 mi DipIal PotI_A Single model 0.0 mi 10.0 mi 10.0 mi DipIal DipIal <td>Table (a) PUT_Jun Strage encode 20 mu Modernet 7 Adb - Sprage encode 20 mu Modernet 7 Adb - Sprage encode 20 mu Modernet 7 Adb - Sprage encode 100 mu 10 mu 10 mu - ML Doint DN01 Bit Input 10 mu - ML Doint DN01 Bit Input 10 mu - ML Doint DN01 Bit Input 10 mu 10 mu - ML DOINTO Bit Input 10 mu 10 mu<</td> <td>Table Initial Profile Strate mode 2.0 ms Modeline 7 Table </td> <td>Lab. (a) PCLAr Single mode 2.0 Undefined 7 Lab. (a) PCT_Ar Single mode 2.0 undefined 7 LB. (b) (c) Single mode 1000 100 1000 LB. (c) DUTO Bridge Bridge 1000 1000 -BB. (c) DUTO Bridge Bridge 1000 1000 -BB. DUTO DUTO Bridge Bridge 10000 1000 -BB. DUTO DUTO Bridge Bridge 10000 10000 -BB. DUTO DUTO Bridge Bridge 100000 100000000 1000000000000000000000000000000000000</td> <td>Table (a) (b) (b) (c) (c)<!--</td--><td>Table [a] [b] [b]<!--</td--><td>Table Initial Standbe model 20 ms undefined 7 LB </td><td>Table Initial DOIT_AR Scape exclut 20 ms undefined 7 LB Optimization of polytics - Symple exclut 100 ms 10 ms</td><td>Table 101 DTT_AL Signle model 20 ms undefined 7 LD Opdatingst / outputs Signle model 100 ms 10 s 10 s LD Opdatingst / outputs Device B index 100 ms 10 s LD Dividing Dividing Dividing B index 100 ms 10 s LD Dividing Dividing Dividing B index 10 ms 10 ms LD Dividing Dividing B index 10 ms 10 ms 10 ms LD Dividing Dividing B index 10 ms 10 ms 10 ms
 LD Dividing B index B index 10 ms 10 ms 10 ms LD Dividing B index B index 10 ms 10 ms 10 ms 10 ms LD Dividing B index B index 10 ms 10 m</td><td>Table Initial Stappe model 20 ms undefined 7 Cable </td><td>Table Interface Standbarred 20 ms Maddened 7 Charle </td><td>Table (a) POT 7AL Single model 20 ms Undefined 7 10 Pot 7AL Single model 20 ms Undefined 7 11 Digital registry Single model 100 ms 10 s 10 s 11 Digital registry Digital registry Single model 100 ms 10 s -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Bit Injust Bit Injust 10 ms 10 ms</td><td>Table (iii) (iiii) (iii) (iiii) (iii) (iii) <</td><td>Table Init POT_AL Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 10 m 10 m 10 m A Dipal protocol Dival B Protocol 10 m 10 m -ADL Dival Dival B Protocol 10 m -ADL Dival Dival Dival 10 m -ADL Dival Dival Dival Dival -ADL Dival<td>All Def POIL_Ant Stepse medice 2.0 min Defended 7 1. Dipla POIL_Ant Stepse medice 2.0 min Defended 7 1. Dipla POIL_Ant Stepse medice 100.0 min 10 s 1. Dipla POIL_Ant Defended 7 J.D. Defended 7 10 s J.D. DEfEnded 81 hpoul 10 s J.D. DEFENDE 81 hpoul 10 s J.D. DEFENDE 81 hpoul 10 s</td><td>Table [4] Prof_Unit Single mode 2.0 ms Outcome 7 Table [4] POT_UNIt Single mode 2.0 ms outcome 7 Table [4] Pot_UNIt Single mode 2.0 ms outcome 10 s Table [4] Pot_UNIt Single mode 100 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] [4] Bit lock 10 s 10 s Table [4] [4] [4] [4] 10 s 10 s Table [4] [4] [4] [4] 10 s Table [4]</td><td>Table [14] POT_UM Single relate 2.0 ms dividend 7 Dial POT_UM Single relate 2.0 ms undefined 7 Dial Single relate 2.0 ms undefined 7 Dial Single relate 2.0 ms undefined 7 Dial Dial Dial Dial 10 ms 10 ms -BB Dial Dial Dial Dial 20 ms 10 ms -BB Dial Dial Dial Dial Dial 20 ms 10 ms -BB Dial Dial</td><td>Table [4] POT_LIN Single mode 2.0 Biodecid 2 Display Single mode 2.0 ms Modecid 2 Mo</td><td>Table [4] POT_UN Straph mode 2.0 Generation 7 Abs Sprc_Modedo_rol_active_1 Straph mode 2.0 Generation 7 Abs Sprc_Modedo_rol_active_1 Straph mode 100 100 ADs Double Divot Bellowit 100 -ABL Divot Bellowit Bellowit 100 -ABL</td><td>Table 10 POT Two Signal model 2.0 ms Undefined 7 Table Signal model 2.0 ms Undefined 7 1000 ms 10 s Table Signal model 2.0 ms Undefined 7 10 s 10 s Table Divolt Divolt Divolt Divolt 8 10 s Table Divolt Divolt Divolt B kinowi 10 s 10 s Table Divolt Divolt B kinowi 8 kinowi 10 s 10 s Table Divolt Divolt B kinowi 8 kinowi 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s Table <</td><td>Table (is) POT 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 100 ms 10 s 10 s LD Pot 7AL Single model 100 ms 10 s 10 s LD Pot 7AL Single model 100 ms 10 s 10 s -FAL DVA2 DUV02 B4 Ungat </td><td>Table (iii) (iiii) (iii) (iiii) (iii) (iii) <</td><td>Table Init POT_AL Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 10 m 10 m 10 m A Dipal protocol Dival B Protocol 10 m 10 m -ADL Dival Dival B Protocol 10 m -ADL Dival Dival Dival 10 m -ADL Dival Dival Dival Dival -ADL Dival<td>Table (a) P(1)_UF Stright ended 2.0 m. Undered 7 UP (2) P(1)_UF Stright ended 2.0 m. uddred ? UP (2) D(1)_UF Stright ended 10.0 m. 10.0 m. 10.0 m. LP_UP (2) D(1)_UF Bright ended 10.0 m. 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m</td><td>Table [M] POT_UM Single mode 2.0 ms Underade 7 Serult Serult Sarult Sarult 2.0 ms Underade 7 Serult Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 10 ms 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult
 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms</td><td>Table (a) P(1)_s(k) Stright ended 20 ms Modified 7 Abs </td></td></td></td></td> | Table (a) PUT_Jun Strage encode 20 mu Modernet 7 Adb - Sprage encode 20 mu Modernet 7 Adb - Sprage encode 20 mu Modernet 7 Adb - Sprage encode 100 mu 10 mu 10 mu - ML Doint DN01 Bit Input 10 mu - ML Doint DN01 Bit Input 10 mu - ML Doint DN01 Bit Input 10 mu 10 mu - ML DOINTO Bit Input 10 mu 10 mu<

 | Table Initial Profile Strate mode 2.0 ms Modeline 7 Table

 | Lab. (a) PCLAr Single mode 2.0 Undefined 7 Lab. (a) PCT_Ar Single mode 2.0 undefined 7 LB. (b) (c) Single mode 1000 100 1000 LB. (c) DUTO Bridge Bridge 1000 1000 -BB. (c) DUTO Bridge Bridge 1000 1000 -BB. DUTO DUTO Bridge Bridge 10000 1000 -BB. DUTO DUTO Bridge Bridge 10000 10000 -BB. DUTO DUTO Bridge Bridge 100000 100000000 1000000000000000000000000000000000000

 | Table (a) (b) (b) (c) (c) </td <td>Table [a] [b] [b]<!--</td--><td>Table Initial Standbe model 20 ms undefined 7 LB </td><td>Table Initial DOIT_AR Scape exclut 20 ms undefined 7 LB Optimization of polytics - Symple exclut 100 ms 10 ms</td><td>Table 101 DTT_AL Signle model 20 ms undefined 7 LD Opdatingst / outputs Signle model 100 ms 10 s 10 s LD Opdatingst / outputs Device B index 100 ms 10 s LD Dividing Dividing Dividing B index 100 ms 10 s LD Dividing Dividing Dividing B index 10 ms 10 ms LD Dividing Dividing B index 10 ms 10 ms 10 ms LD Dividing Dividing B index 10 ms 10 ms 10 ms LD Dividing B index B index 10 ms 10 ms 10 ms LD Dividing B index B index 10 ms 10 ms 10 ms 10 ms LD Dividing B index B index 10 ms 10 m</td><td>Table Initial Stappe model 20 ms undefined 7 Cable </td><td>Table Interface Standbarred 20 ms Maddened 7 Charle </td><td>Table (a) POT 7AL Single model 20 ms Undefined 7 10 Pot 7AL Single model 20 ms Undefined 7 11 Digital registry Single model 100 ms 10 s 10 s 11 Digital registry Digital registry Single model 100 ms 10 s -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Bit Injust Bit Injust 10 ms 10 ms</td><td>Table (iii) (iiii) (iii) (iiii) (iii) (iii) <</td><td>Table Init POT_AL Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 10 m 10 m 10 m A Dipal protocol Dival B Protocol 10 m 10 m -ADL Dival Dival B Protocol 10 m -ADL Dival Dival Dival 10 m -ADL Dival Dival Dival Dival -ADL Dival<td>All Def POIL_Ant Stepse medice 2.0 min Defended 7 1. Dipla POIL_Ant Stepse medice 2.0 min Defended 7 1. Dipla POIL_Ant Stepse medice 100.0 min 10 s 1. Dipla POIL_Ant Defended 7 J.D. Defended 7 10 s J.D. DEfEnded 81 hpoul 10 s J.D. DEFENDE 81 hpoul 10 s J.D. DEFENDE 81 hpoul 10 s</td><td>Table [4] Prof_Unit Single mode 2.0 ms Outcome 7 Table [4] POT_UNIt Single mode 2.0 ms outcome 7 Table [4] Pot_UNIt Single mode 2.0 ms outcome 10 s Table [4] Pot_UNIt Single mode 100 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] [4] Bit lock 10 s 10 s Table [4] [4] [4] [4] 10 s 10 s Table [4] [4] [4] [4] 10 s Table [4]</td><td>Table [14] POT_UM Single relate 2.0 ms dividend 7 Dial POT_UM Single relate 2.0 ms undefined 7 Dial Single relate 2.0 ms undefined 7 Dial Single relate 2.0 ms undefined 7 Dial Dial Dial Dial 10 ms 10 ms -BB Dial Dial Dial Dial 20 ms 10 ms -BB Dial Dial Dial Dial Dial 20 ms 10 ms -BB Dial Dial</td><td>Table [4] POT_LIN Single mode 2.0 Biodecid 2 Display Single mode 2.0 ms Modecid 2 Mo</td><td>Table [4] POT_UN Straph mode 2.0 Generation 7 Abs Sprc_Modedo_rol_active_1 Straph mode 2.0 Generation 7 Abs Sprc_Modedo_rol_active_1 Straph mode 100 100 ADs Double Divot Bellowit 100 -ABL Divot Bellowit Bellowit 100 -ABL</td><td>Table 10 POT Two Signal model 2.0 ms Undefined 7 Table
 Signal model 2.0 ms Undefined 7 1000 ms 10 s Table Signal model 2.0 ms Undefined 7 10 s 10 s Table Divolt Divolt Divolt Divolt 8 10 s Table Divolt Divolt Divolt B kinowi 10 s 10 s Table Divolt Divolt B kinowi 8 kinowi 10 s 10 s Table Divolt Divolt B kinowi 8 kinowi 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s Table <</td><td>Table (is) POT 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 100 ms 10 s 10 s LD Pot 7AL Single model 100 ms 10 s 10 s LD Pot 7AL Single model 100 ms 10 s 10 s -FAL DVA2 DUV02 B4 Ungat </td><td>Table (iii) (iiii) (iii) (iiii) (iii) (iii) <</td><td>Table Init POT_AL Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 10 m 10 m 10 m A Dipal protocol Dival B Protocol 10 m 10 m -ADL Dival Dival B Protocol 10 m -ADL Dival Dival Dival 10 m -ADL Dival Dival Dival Dival -ADL Dival<td>Table (a) P(1)_UF Stright ended 2.0 m. Undered 7 UP (2) P(1)_UF Stright ended 2.0 m. uddred ? UP (2) D(1)_UF Stright ended 10.0 m. 10.0 m. 10.0 m. LP_UP (2) D(1)_UF Bright ended 10.0 m. 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m</td><td>Table [M] POT_UM Single mode 2.0 ms Underade 7 Serult Serult Sarult Sarult 2.0 ms Underade 7 Serult Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 10 ms 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms</td><td>Table (a) P(1)_s(k) Stright ended 20 ms Modified 7 Abs </td></td></td></td>
 | Table [a] [b] [b] </td <td>Table Initial Standbe model 20 ms undefined 7 LB </td> <td>Table Initial DOIT_AR Scape exclut 20 ms undefined 7 LB Optimization of polytics - Symple exclut 100 ms 10 ms</td> <td>Table 101 DTT_AL Signle model 20 ms undefined 7 LD Opdatingst / outputs Signle model 100 ms 10 s 10 s LD Opdatingst / outputs Device B index 100 ms 10 s LD Dividing Dividing Dividing B index 100 ms 10 s LD Dividing Dividing Dividing B index 10 ms 10 ms LD Dividing Dividing B index 10 ms 10 ms 10 ms LD Dividing Dividing B index 10 ms 10 ms 10 ms LD Dividing B index B index 10 ms 10 ms 10 ms LD Dividing B index B index 10 ms 10 ms 10 ms 10 ms LD Dividing B index B index 10 ms 10 m</td> <td>Table Initial Stappe model 20 ms undefined 7 Cable </td> <td>Table Interface Standbarred 20 ms Maddened 7 Charle </td> <td>Table (a) POT 7AL Single model 20 ms Undefined 7 10 Pot 7AL Single model 20 ms Undefined 7 11 Digital registry Single model 100 ms 10 s 10 s 11 Digital registry Digital registry Single model 100 ms 10 s -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Bit Injust Bit Injust 10 ms 10 ms</td> <td>Table (iii) (iiii) (iii) (iiii) (iii) (iii) <</td> <td>Table Init POT_AL Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 10 m 10 m 10 m A Dipal protocol Dival B Protocol 10 m 10 m -ADL Dival Dival B Protocol 10 m -ADL Dival Dival Dival 10 m -ADL Dival Dival Dival Dival -ADL Dival<td>All Def POIL_Ant Stepse medice 2.0 min Defended 7 1. Dipla POIL_Ant Stepse medice 2.0 min Defended 7 1. Dipla POIL_Ant Stepse medice 100.0 min 10 s 1. Dipla POIL_Ant Defended 7 J.D. Defended 7 10 s J.D. DEfEnded 81 hpoul 10 s J.D. DEFENDE 81 hpoul 10 s J.D. DEFENDE 81 hpoul 10 s</td><td>Table [4] Prof_Unit Single mode 2.0 ms Outcome 7 Table [4] POT_UNIt Single mode 2.0 ms outcome 7 Table [4] Pot_UNIt Single mode 2.0 ms outcome 10 s Table [4] Pot_UNIt Single mode 100 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s
 Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] [4] Bit lock 10 s 10 s Table [4] [4] [4] [4] 10 s 10 s Table [4] [4] [4] [4] 10 s Table [4]</td><td>Table [14] POT_UM Single relate 2.0 ms dividend 7 Dial POT_UM Single relate 2.0 ms undefined 7 Dial Single relate 2.0 ms undefined 7 Dial Single relate 2.0 ms undefined 7 Dial Dial Dial Dial 10 ms 10 ms -BB Dial Dial Dial Dial 20 ms 10 ms -BB Dial Dial Dial Dial Dial 20 ms 10 ms -BB Dial Dial</td><td>Table [4] POT_LIN Single mode 2.0 Biodecid 2 Display Single mode 2.0 ms Modecid 2 Mo</td><td>Table [4] POT_UN Straph mode 2.0 Generation 7 Abs Sprc_Modedo_rol_active_1 Straph mode 2.0 Generation 7 Abs Sprc_Modedo_rol_active_1 Straph mode 100 100 ADs Double Divot Bellowit 100 -ABL Divot Bellowit Bellowit 100 -ABL</td><td>Table 10 POT Two Signal model 2.0 ms Undefined 7 Table Signal model 2.0 ms Undefined 7 1000 ms 10 s Table Signal model 2.0 ms Undefined 7 10 s 10 s Table Divolt Divolt Divolt Divolt 8 10 s Table Divolt Divolt Divolt B kinowi 10 s 10 s Table Divolt Divolt B kinowi 8 kinowi 10 s 10 s Table Divolt Divolt B kinowi 8 kinowi 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s Table <</td><td>Table (is) POT 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 100 ms 10 s 10 s LD Pot 7AL Single model 100 ms 10 s 10 s LD Pot 7AL Single model 100 ms 10 s 10 s -FAL DVA2 DUV02 B4 Ungat </td><td>Table (iii) (iiii) (iii) (iiii) (iii) (iii) <</td><td>Table Init POT_AL Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 10 m 10 m 10 m A Dipal protocol Dival B Protocol 10 m 10 m -ADL Dival Dival B Protocol 10 m -ADL Dival Dival Dival 10 m -ADL Dival Dival Dival Dival -ADL Dival<td>Table (a) P(1)_UF Stright ended 2.0 m. Undered 7 UP (2) P(1)_UF Stright ended 2.0 m. uddred ? UP (2) D(1)_UF Stright ended 10.0 m. 10.0 m. 10.0 m. LP_UP (2) D(1)_UF Bright ended 10.0 m. 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m</td><td>Table [M] POT_UM Single mode 2.0 ms Underade 7 Serult Serult Sarult Sarult 2.0 ms Underade 7 Serult Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 10 ms 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms</td><td>Table (a) P(1)_s(k) Stright ended 20 ms Modified 7 Abs </td></td></td>
 | Table Initial Standbe model 20 ms undefined 7 LB

 | Table Initial DOIT_AR Scape exclut 20 ms undefined 7 LB Optimization of polytics - Symple exclut 100 ms 10 ms

 | Table 101 DTT_AL Signle model 20 ms undefined 7 LD Opdatingst / outputs Signle model 100 ms 10 s 10 s LD Opdatingst / outputs Device B index 100 ms 10 s LD Dividing Dividing Dividing B index 100 ms 10 s LD Dividing Dividing Dividing B index 10 ms 10 ms LD Dividing Dividing B index 10 ms 10 ms 10 ms LD Dividing Dividing B index 10 ms 10 ms 10 ms LD Dividing B index B index 10 ms 10 ms 10 ms LD Dividing B index B index 10 ms 10 ms 10 ms 10 ms LD Dividing B index B index 10 ms 10 m

 | Table Initial Stappe model 20 ms undefined 7 Cable

 | Table Interface Standbarred 20 ms Maddened 7 Charle

 | Table (a) POT 7AL Single model 20 ms Undefined 7 10 Pot 7AL Single model 20 ms Undefined 7 11 Digital registry Single model 100 ms 10 s 10 s 11 Digital registry Digital registry Single model 100 ms 10 s -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Divital registry Bit Injust 10 ms 10 ms -FAL Divital registry Bit Injust Bit Injust 10 ms

 | Table (iii) (iiii) (iii) (iiii) (iii) (iii) <

 | Table Init POT_AL Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 10 m 10 m 10 m A Dipal protocol Dival B Protocol 10 m 10 m -ADL Dival Dival B Protocol 10 m -ADL Dival Dival Dival 10 m -ADL Dival Dival Dival Dival -ADL Dival <td>All Def POIL_Ant Stepse medice 2.0 min Defended 7 1. Dipla POIL_Ant Stepse medice 2.0 min Defended 7 1. Dipla POIL_Ant Stepse medice 100.0 min 10 s 1. Dipla POIL_Ant Defended 7 J.D. Defended 7 10 s J.D. DEfEnded 81 hpoul 10 s J.D. DEFENDE 81 hpoul 10 s J.D. DEFENDE 81 hpoul 10 s</td> <td>Table [4] Prof_Unit Single mode 2.0 ms Outcome 7 Table [4] POT_UNIt Single mode 2.0 ms outcome 7 Table [4] Pot_UNIt Single mode 2.0 ms outcome 10 s Table [4] Pot_UNIt Single mode 100 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] [4] Bit lock 10 s 10 s Table [4] [4] [4] [4] 10 s 10 s Table [4] [4] [4] [4] 10 s Table [4]</td> <td>Table [14] POT_UM Single relate 2.0 ms dividend 7 Dial POT_UM Single relate 2.0 ms undefined 7 Dial Single relate 2.0 ms undefined 7 Dial Single relate 2.0 ms undefined 7 Dial Dial Dial Dial 10 ms 10 ms -BB Dial Dial Dial Dial 20 ms 10 ms -BB Dial Dial Dial Dial Dial 20 ms 10 ms -BB Dial Dial</td> <td>Table [4] POT_LIN Single mode 2.0 Biodecid 2 Display Single mode 2.0 ms Modecid 2 Mo</td> <td>Table [4] POT_UN Straph mode 2.0 Generation 7 Abs Sprc_Modedo_rol_active_1 Straph mode 2.0 Generation 7 Abs Sprc_Modedo_rol_active_1 Straph mode 100 100 ADs Double Divot Bellowit 100 -ABL Divot Bellowit Bellowit 100 -ABL</td> <td>Table 10 POT Two Signal model 2.0 ms Undefined 7 Table Signal model 2.0 ms Undefined 7 1000 ms 10 s Table Signal model 2.0 ms Undefined 7 10 s 10 s Table Divolt Divolt Divolt Divolt 8 10 s Table Divolt Divolt Divolt B kinowi 10 s 10 s Table Divolt Divolt B kinowi 8 kinowi 10 s 10 s Table Divolt Divolt B kinowi 8 kinowi 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s Table <</td> <td>Table (is) POT 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 100 ms 10 s 10 s LD Pot 7AL Single model 100 ms 10 s 10 s LD Pot 7AL Single model 100 ms 10 s 10 s -FAL DVA2 DUV02 B4 Ungat </td> <td>Table (iii) (iiii) (iii) (iiii) (iii) (iii) <</td> <td>Table Init POT_AL Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 10 m 10 m 10 m A Dipal protocol Dival B Protocol 10 m 10 m -ADL Dival Dival B Protocol 10 m -ADL Dival Dival Dival 10 m -ADL Dival Dival Dival Dival -ADL Dival<td>Table (a) P(1)_UF Stright ended 2.0 m. Undered 7 UP (2) P(1)_UF Stright ended 2.0 m. uddred ? UP (2) D(1)_UF Stright ended
10.0 m. 10.0 m. 10.0 m. LP_UP (2) D(1)_UF Bright ended 10.0 m. 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m</td><td>Table [M] POT_UM Single mode 2.0 ms Underade 7 Serult Serult Sarult Sarult 2.0 ms Underade 7 Serult Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 10 ms 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms</td><td>Table (a) P(1)_s(k) Stright ended 20 ms Modified 7 Abs </td></td> | All Def POIL_Ant Stepse medice 2.0 min Defended 7 1. Dipla POIL_Ant Stepse medice 2.0 min Defended 7 1. Dipla POIL_Ant Stepse medice 100.0 min 10 s 1. Dipla POIL_Ant Defended 7 J.D. Defended 7 10 s J.D. DEfEnded 81 hpoul 10 s J.D. DEFENDE 81 hpoul 10 s J.D. DEFENDE 81 hpoul 10 s
 | Table [4] Prof_Unit Single mode 2.0 ms Outcome 7 Table [4] POT_UNIt Single mode 2.0 ms outcome 7 Table [4] Pot_UNIt Single mode 2.0 ms outcome 10 s Table [4] Pot_UNIt Single mode 100 s 10
s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] Pot_UNIt Bit lock 10 s 10 s Table [4] [4] Bit lock 10 s 10 s Table [4] [4] [4] [4] 10 s 10 s Table [4] [4] [4] [4] 10 s Table [4]
 | Table [14] POT_UM Single relate 2.0 ms dividend 7 Dial POT_UM Single relate 2.0 ms undefined 7 Dial Single relate 2.0 ms undefined 7 Dial Single relate 2.0 ms undefined 7 Dial Dial Dial Dial 10 ms 10 ms -BB Dial Dial Dial Dial 20 ms 10 ms -BB Dial Dial Dial Dial Dial 20 ms 10 ms -BB Dial
 | Table [4] POT_LIN Single mode 2.0 Biodecid 2 Display Single mode 2.0 ms Modecid 2 Mo
 | Table [4] POT_UN Straph mode 2.0 Generation 7 Abs Sprc_Modedo_rol_active_1 Straph mode 2.0 Generation 7 Abs Sprc_Modedo_rol_active_1 Straph mode 100 100 ADs Double Divot Bellowit 100 -ABL Divot Bellowit Bellowit 100 -ABL

 | Table 10 POT Two Signal model 2.0 ms Undefined 7 Table Signal model 2.0 ms Undefined 7 1000 ms 10 s Table Signal model 2.0 ms Undefined 7 10 s 10 s Table Divolt Divolt Divolt Divolt 8 10 s Table Divolt Divolt Divolt B kinowi 10 s 10 s Table Divolt Divolt B kinowi 8 kinowi 10 s 10 s Table Divolt Divolt B kinowi 8 kinowi 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s 10 s Table Divolt Divolt B kinowi 10 s 10 s Table < | Table (is) POT 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 20 ms Understand 7 LD Pot 7AL Single model 100 ms 10 s 10 s LD Pot 7AL Single model 100 ms 10 s 10 s LD Pot 7AL Single model 100 ms 10 s 10 s -FAL DVA2 DUV02 B4 Ungat
 | Table (iii) (iiii) (iii) (iiii) (iii) (iii) <
 | Table Init POT_AL Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 2 On Underword 7 A Dipal protocol Signer model 10 m 10 m 10 m A Dipal protocol Dival B Protocol 10 m 10 m -ADL Dival Dival B Protocol 10 m -ADL Dival Dival Dival 10 m -ADL Dival Dival Dival Dival -ADL Dival <td>Table (a) P(1)_UF Stright ended 2.0 m. Undered 7 UP (2) P(1)_UF Stright ended 2.0 m. uddred ? UP (2) D(1)_UF Stright ended 10.0 m. 10.0 m. 10.0 m. LP_UP (2) D(1)_UF Bright ended 10.0 m. 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m</td> <td>Table [M] POT_UM Single mode 2.0 ms Underade 7 Serult Serult Sarult Sarult 2.0 ms Underade 7 Serult Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 10 ms 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms</td> <td>Table (a) P(1)_s(k) Stright ended 20 ms Modified 7 Abs </td> | Table (a) P(1)_UF Stright ended 2.0 m. Undered 7 UP (2) P(1)_UF Stright ended 2.0 m. uddred ? UP (2) D(1)_UF Stright ended 10.0 m. 10.0 m. 10.0 m. LP_UP (2) D(1)_UF Bright ended 10.0 m. 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF Bright ended Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m. 10.0 m. -IRL D(1)_UF D(1)_UF Bright ended 10.0 m | Table [M] POT_UM Single mode 2.0 ms Underade 7 Serult Serult Sarult Sarult 2.0 ms Underade 7 Serult Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 2.0 ms Underade 7 Sarult Sarult Sarult 10 ms 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel
Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms Full Dipular travel Sarult Sarult 10 ms 10 ms | Table (a) P(1)_s(k) Stright ended 20 ms Modified 7 Abs |
| Lab 10 10 10 20

 | india (a) (b) (c) (c) </td <td>Base 10 FUD_M Adva</td> <td>Department Department <thdepartment< th=""> Department Departme</thdepartment<></td> <td>LB bit Dir/L Single mode 20 million Mode 20 million Mode Mode</td> <td>Lb 101 PUT_AL Single mode 2 0 ms underlind LD Dial Single mode 2 0 ms 10 s LD Dial Dial 0 ms 10 s LD Dial Dial Single mode 2 0 ms LD Dial Dial B kinput 10 s LD Dial Dial B kinput 10 s</td> <td>Lab. [ai] [aii] [aiii] [aii] [aii]</td> <td>Image: Part of the second se</td> <td>Lab Lab <thlab< th=""> <thlab< th=""> <thlab< th=""></thlab<></thlab<></thlab<></td> <td>Image: Constraint of the state of</td> <td>Lab Image Product Specific reduct Specific reduct</td> <td>Lag Init Lag Single rector 200 mis 10 s Lag Digital product Single rector 200 mis 10 s Lag Digital product Single rector 200 mis 10 s Lag Digital product Bit Input 10 s Lag Digital product Digital product 10 s Lag Digital product Bit Input 10 s Lag Digital product Bit Input</td> <td>Lag Init Lag Single mode 200 ms 10 s Lag Dipal-focular Single mode 10 s Lag Dipal-focular Single mode 10 s Lag Dipal-focular Dipal-focular Dipal-focular Lag</td> <td>Las (a) PUT_AL Single rector 2.0 ms Constrained 7 Displained for support Single rector 2.0 ms 10 s 10 s Displained for support Note Single rector 10 s 10 s Displained for support Note Single rector 10 s 10 s Displained for support Note Single rector 10 s Displained for support Note Single rector 10 s Displained for support Single rector Single rector 10 s Displained for support Displained for support Single rector Single rector -DIS Displained for support Single rector Single rector Single rector -DIS Displained for support Single rector Single rector Single rector -DIS Displained for support Single rector Single rector Single rector -DIS Displained for support Single rector Single rector Single rector Single rector -DIS Displaine Single support Si</td> <td>Lab (a) (b) (b) (c) (c)<td>Table Interval Topic encode C (III) C (IIII) C (III) C (III)</td><td>data (m) (m)<td>Construct Direct <thdirect< th=""> <thdirect< th=""> <thdirect<< td=""><td>Image: Project System System Project 200 Display for the System System System Project 200 Display for the System System Display for the System Display for the System System System Display for the System System Display for the System System Display for the System System System Display for the System System System System Display for the System System System System System System System Display for the System Syst</td><td>Last Dir During Direct Stragge mode Constraint Topical mode <thtopical mode<="" t<="" td=""><td>Lab. (a) (c) (c)<td>Lab. (a) (b) Jul (c) J</td><td>Lab (a) PUT_AL Set (a) Set (a)</td><td>Lab (a) (b) (b) (c) (c)<td>Table Interval Topic encode C (III) C (IIII) C (III) C (III)</td><td>Image Image <th< td=""><td>Image: Image: Image:</td><td>Lab </td></th<></td></td></td></thtopical></td></thdirect<<></thdirect<></thdirect<></td></td></td>
 | Base 10 FUD_M Adva

 | Department Department <thdepartment< th=""> Department Departme</thdepartment<>

 | LB bit Dir/L Single mode 20 million Mode 20 million Mode

 | Lb 101 PUT_AL Single mode 2 0 ms underlind LD Dial Single mode 2 0 ms 10 s LD Dial Dial 0 ms 10 s LD Dial Dial Single mode 2 0 ms LD Dial Dial B kinput 10 s

 | Lab. [ai] [aii] [aiii] [aii] [aii]

 | Image: Part of the second se

 | Lab Lab <thlab< th=""> <thlab< th=""> <thlab< th=""></thlab<></thlab<></thlab<>

 | Image: Constraint of the state of

 | Lab Image Product Specific reduct

 | Lag Init Lag Single rector 200 mis 10 s Lag Digital product Single rector 200 mis 10 s Lag Digital product Single rector 200 mis 10 s Lag Digital product Bit Input 10 s Lag Digital product Digital product 10 s Lag Digital product Bit Input

 | Lag Init Lag Single mode 200 ms 10 s Lag Dipal-focular Single mode 10 s Lag Dipal-focular Single mode 10 s Lag Dipal-focular Dipal-focular Dipal-focular Lag

 | Las (a) PUT_AL Single rector 2.0 ms Constrained 7 Displained for support Single rector 2.0 ms 10 s 10 s Displained for support Note Single rector 10 s 10 s Displained for support Note Single rector 10 s 10 s Displained for support Note Single rector 10 s Displained for support Note Single rector 10 s Displained for support Single rector Single rector 10 s Displained for support Displained for support Single rector Single rector -DIS Displained for support Single rector Single rector Single rector -DIS Displained for support Single rector Single rector Single rector -DIS Displained for support Single rector Single rector Single rector -DIS Displained for support Single rector Single rector Single rector Single rector -DIS Displaine Single support Si

 | Lab (a) (b) (b) (c) (c) <td>Table Interval Topic encode C (III) C (IIII) C (III) C (III)</td> <td>data (m) (m)<td>Construct Direct <thdirect< th=""> <thdirect< th=""> <thdirect<< td=""><td>Image: Project System System Project 200 Display for the System System System Project 200 Display for the System System Display for the System Display for the System System System Display for the System System Display for the System System Display for the System System System Display for the System System System System Display for the System System System System System System System Display for the System Syst</td><td>Last Dir During Direct Stragge mode Constraint Topical mode <thtopical mode<="" t<="" td=""><td>Lab. (a) (c) (c)<td>Lab. (a) (b) Jul (c) J</td><td>Lab (a) PUT_AL Set (a) Set (a)</td><td>Lab (a) (b) (b) (c) (c)<td>Table Interval Topic encode C (III) C (IIII) C (III) C (III)</td><td>Image Image <th< td=""><td>Image: Image: Image:</td><td>Lab </td></th<></td></td></td></thtopical></td></thdirect<<></thdirect<></thdirect<></td></td>
 | Table Interval Topic encode C (III) C (IIII) C (III) C (III)
 | data (m) (m) <td>Construct Direct <thdirect< th=""> <thdirect< th=""> <thdirect<< td=""><td>Image: Project System System Project 200 Display for the System System System Project 200 Display for the System System Display for the System Display for the System System System Display for the System System Display for the System System Display for the System System System Display for the System System System System Display for the System System System System System System System Display for the System Syst</td><td>Last Dir During Direct Stragge mode Constraint Topical mode <thtopical mode<="" t<="" td=""><td>Lab. (a) (c) (c)<td>Lab. (a) (b) Jul (c) J</td><td>Lab (a) PUT_AL Set (a) Set (a)</td><td>Lab (a) (b) (b) (c) (c)<td>Table Interval Topic encode C (III) C (IIII) C (III) C (III)</td><td>Image Image <th< td=""><td>Image: Image: Image:</td><td>Lab </td></th<></td></td></td></thtopical></td></thdirect<<></thdirect<></thdirect<></td> | Construct Direct Direct <thdirect< th=""> <thdirect< th=""> <thdirect<< td=""><td>Image: Project System System Project 200 Display for the System System System Project 200 Display for the System System Display for the System Display for the System System System Display for the System System Display for the System System Display for the System System System Display for the System System System System Display for the System System System System System System System Display for the System Syst</td><td>Last Dir During Direct Stragge mode Constraint Topical mode <thtopical mode<="" t<="" td=""><td>Lab. (a) (c) (c)<td>Lab. (a) (b) Jul (c) J</td><td>Lab (a) PUT_AL Set (a) Set (a)</td><td>Lab (a) (b) (b) (c) (c)<td>Table Interval Topic encode C (III) C (IIII) C (III) C (III)</td><td>Image Image <th< td=""><td>Image: Image: Image:</td><td>Lab </td></th<></td></td></td></thtopical></td></thdirect<<></thdirect<></thdirect<> | Image: Project System System Project 200 Display for the System System System Project 200 Display for the System System Display for the System Display for the System System System Display for the System System Display for the System System Display for the System System System Display for the System System System System Display for the System System System System System System System Display for the System Syst

 | Last Dir During Direct Stragge mode Constraint Topical mode Topical mode <thtopical mode<="" t<="" td=""><td>Lab. (a) (c) (c)<td>Lab. (a) (b) Jul (c) J</td><td>Lab (a) PUT_AL Set (a) Set (a)</td><td>Lab (a) (b) (b) (c) (c)<td>Table Interval Topic encode C (III) C (IIII) C (III) C (III)</td><td>Image Image <th< td=""><td>Image: Image: Image:</td><td>Lab </td></th<></td></td></td></thtopical> | Lab. (a) (c) (c) <td>Lab. (a) (b) Jul (c) J</td> <td>Lab (a) PUT_AL Set (a) Set (a)</td> <td>Lab (a) (b) (b) (c) (c)<td>Table Interval Topic encode C (III) C (IIII) C (III) C (III)</td><td>Image Image <th< td=""><td>Image: Image: Image:</td><td>Lab </td></th<></td></td> | Lab. (a) (b) Jul (c) J
 | Lab (a) PUT_AL Set (a)
 | Lab (a) (b) (b) (c) (c) <td>Table Interval Topic encode C (III) C (IIII) C (III) C (III)</td> <td>Image Image <th< td=""><td>Image: Image: Image:</td><td>Lab </td></th<></td> | Table Interval Topic encode C (III) C (IIII) C (III) C (III) | Image Image <th< td=""><td>Image: Image: Image:</td><td>Lab </td></th<> | Image: | Lab |
| Table Dela locat / Andows
TABL Dela Dela locat / Andows
TABL Dela Dela Dela Dela Dela Dela Dela Dela

 | B (II) Dipal inclusi / status

 | Biggli Provid / Publicki Diffe_mone

 | All Digital Input / output

 | In Dipid a point / houghts

 | B. Digital input / August

 | AD System Control

 |

 | All Diff Diff Diff Diff Diff -FAL Diff Diff Diff Diff Diff Diff -FAL Diff

 | Construct - Section - Section - Section - Section Fall Doint Diff Bit space - Section - Section Fall Doint Diff Bit space - Section - Section - Section Fall Doint Diff Bit space - Section - Section - Section Fall Doint DOInt Diff - Section - Section - Section Fall Doint DOInt Doint Bit space - Section - Section - Section Fall Doint Doint Doint Doint - Section

 | All Digital input / output

 | All Diplain point / output Diplain point / output Diplain point / output -FAL Divid Divid Divide Divide -FAL Divide Divide Divide

 | II Dipati product / product - Open mode

 | Dipute product / product

 | Dipatiproperty / popular

 | Bigging property / coput;
 | Algo algority / Stypes/ Stype / Note of Stypes/ Stype / Note of Stypes/ Algo algority / Stypes/ Stype / Note of Stypes/ Stype / Note of Stypes/ JBL Drift Drift Stype / Note of Stypes/ JBL Drift Drift Stype / Note of Stypes/ JBL Drift Drift Stype / Stypes/ JBL Drift Drift Stype / Stypes/ JBL Drift Drift Drift JBL Drift Drif

 | -26. Splat lexels / output -Steg. Intol. -Steg. Intol. -Steg. Intol. -Steg. Intol. -26. Displat lexels / output -Displat lexels / output -Displat lexels / output -Steg. -26. Displat lexels / output -Displat lexels / output -Bit lexel - - -26. Displat lexels / output -Bit lexel -
 | Biologia trout / output:

 | Ab point incurst / surget
 | Biggla (notif / toglog):
 | All Dipid (arbit) / fudput;
 | Dipute product / product - product / product - product / product - product / product - DAL Divid Divid Divid Divid - DAL Divid Divid Divid Divid </td <td>Dipatiproperty / popular </td> <td>Bigging property / coput; </td> <td>Dipate inspire / subject - Sync, findow</td> <td></td> <td>All Option Power / Output - Option Power / Output - Option Power / Output </td>
 | Dipatiproperty / popular | Bigging property / coput;
 | Dipate inspire / subject - Sync, findow | | All Option Power / Output - Option Power / Output - Option Power / Output |
| All Carterian Derif Volker Britinger
All Douge Derif Volker Britinger
All Douge Derif Volker Britinger
All Douge Derif Derift Derift Britiger
All Douge Derift Derift Derift Britiger
All Douge Derift Derift Britiger
All Douge Derift Derift Britiger
All Douge Derift Derift Britiger
Statuer Activer V Duation: undefined V Sampler
Statuer Activer V Sampler
Sampler Beller

 | AL DMIT DNIT Bitmout -FAR DOV DOV DOV -FAR DOV DOV DOV

 | Lin Ohn DiNO1 Bk Input DiL Obu2 DOUT02 Bk Ouput DiL DOU2 DOUT02 Bk Input DL DOU2 DOUT02 Bk Input DL DOU2 DOUT02 Bk Input DL DOU2 DOUT02 Bk Input DU DINOT_BRID DNOT_BRID Bk Input DL DOUT02 Bk Input Bk Input <

 | Ind Defail DBM01 DB troot IndL D0401 D041002 D041002 IndL D0402 D041002 D040002 IndL D0402 D041002 D040002 IndL D040102 D040002 D040002 IndL D040102 D040002 D040002 IndL D040101 D04001 D040012 IndL D040101 D04001 D040012 IndL D0401010 D04001 D040010 IndL D04010101 D04001 D0400100

 | Like Description DNM01 DNM04 DNM04 Like Description DNM04 DNM04 DNM04 Like DAL DM04 DNM04 DNM04 Like DNM04 DNM04 DNM04

 | Link Description Dividing -10L Defund Dividing Defund -10L Defund Defund Defund

 | Ind Dent Direct Bit Input -FUL Dirl Dirlot Bit Input -FUL Dirlot Bit Input Bit Input -FUL Dirlot Dirlot Bit Input

 | DLL Divid Divid Bit low/r DRL 0000 D00100 Bit low/r - RL 0010 D001002 Bit low/r - RL 0010 Bit low/r Bit low/r - RL 0010 D00102 Bit low/r - RL 0012 Bit low/r Bit low/r

 | I-RIL Divid Bit lock I-RIL Divid Dividing I-RIL Dividing Dividi

 | ALL Don't Bit Insuk ALL Don't Bit Insuk ALL DOL't DOL'tO Bit Insuk -ALL DOL't DOL'tO Bit Insuk -ALL DOL'tO Bit Insuk Bit Insuk -ALL DOL'TO DBAND_BAD2 Bit Insuk -ALL DOL'DOL'D DNUT_BAD3 Bit Insuk -ALL DOL'S DINIT DNUT_BAD3 Bit Insuk -ALL DOL'S DINIT DNUT_BAD3 Bit Insuk -ALL DOL'S DINIT DNUT_BAD3 Bit Insuk

 | I-BL Divini Bit Insuk I-BL Divini Divini Bit Insuk

 | I-RI Orivin DB1001 Bit locat I-RI Orivin DD10102 Bit Origini I-RI Orivin DD10102 Bit Origini I-RI Orivin DD10102 Bit Origini I-RI Orizin DD101 Bit Orizini I-RI Distribution Bit Orizini Bit Orizini

 | LHL Divid DBH011 Bit locat LHL Divid DDU102 Bit locat LHL Divid DDU102 Bit locat LHL Divid Dipped Bit locat LHL Divid Divid Bit locat

 | Inst. Unit Districut Bit Insut Inst. Districut Districut Bit Insut Inst. Districut Districut Bit Insut Inst. Districut Bit Insut Bit Insut

 | A Duration Concern DNM Bit Input -URL Divit DOUTO2 Bit Output -URL Divit DOUTO2 Bit Output -URL Divit Douto2 Bit Output -URL Divit Douto2 Bit Input

 | A. Out Point Dilloti Billipot JIIL Dilloti Dilloti Billipot JIIL Dilloti Integer Billipot JIIL Dilloti Integer Billipot JIIL Dilloti Dilloti Billipot JIIL Dilloti Dilloti Billipot JIIL Dilloti Dilloti Billipot JILL Dilloti Dilloti Billipot JILL Dilloti Dilloti Billipot
 | 20 Dinf Dinf Dinput 200 Dinput Dinput Dinput 200 Dinput Dinput Dinput 200 Dinput Dinput Dinput 201 Dinput Dinput Dinput 202 Dinput Dinput Dinput 203 Dinput Dinput Dinput 204 Dinput Dinput Dinput 203 Dinput Dinput Dinput 204 Dinput Dinput Dinput

 | All Ohrint DN01 Bk Inpuk -JAL Ohrint DN01 Bk Inpuk -JAL Doltation DN01 Bk Inpuk -JAL Doltation Bk Inpuk Bk Inpuk -JAL Doltation Bk Inpuk Bk Inpuk -JAL Doltation Bk Inpuk Bk Inpuk -JAL Doltation DN01_BR02 Bk Inpuk -JAL Doltation DN01_BR03 Bk Inpuk -JAL Doltation DN01_BR04 Bk Inpuk -JAL Doltation DN01_BR04 Bk Inpuk
 | All Dinti Dinti Bk input FAIL Dinti Dinti Bk input FAIL Douting Bk input Bk input FAIL Douting Bk input Bk input FAIL Dinti Bigger Bk input FAIL Dinti Dintig Bk input FAIL Dintig Bk input Bk input FAIL Did (Dinti) DNUT_BAUS Bk input FAIL Did (Dinti) DNUT_BAUS Bk input FAIL Did (Dinti) DNUT_BAUS Bk input

 | Link Chini DiN01 Bk Input - JAL Din1 DiN01 Bk Input - JAL Din2 DO102 Bk Duput - JAL Din1 Input Bk Input - JAL Din1 Input Bk Input - JAL Din1 Din2 Bk Input - JAL Din1 Din11 Bk Input - JAL Din11 Din111 Bk Input - JAL Din11 Din111 Bk Input
 | Link Dirkit Dirkol -ARL Dirkit Dirkol Bit input -ARL Dolrad Dirkol Bit input -ARL Dolrad Bit input Bit input
 | Lat. Description DN01 Bit Insult FAR. DOL 20 DN0102 Sk Oudput FAR. DOL 20 DN0102 Sk Oudput FAR. DOL 20 DN0102 Sk Oudput FAR. DOL 201011 topper Sk Input FAR. DOL 201011 DN019 SR02 Sk Input FAR. DOL 2010111 DN019 SR03 Sk Input FAR. DOL 201011 DN019 SR05 Sk Input
 | At Sum Device DNM Bit Input At Sum Device DNM Bit Input Attende DNM DNM Attende DNM DNM Attende DNM DNM Attende DNM Bit Input | A Duration Concern DNM Bit Input -URL Divit DOUTO2 Bit Output -URL Divit DOUTO2 Bit Output -URL Divit Douto2 Bit Output -URL Divit Douto2 Bit Input
 | A. Out Point Dilloti Billipot JIIL Dilloti Dilloti Billipot JIIL Dilloti Integer Billipot JIIL Dilloti Integer Billipot JIIL Dilloti Dilloti Billipot JIIL Dilloti Dilloti Billipot JIIL Dilloti Dilloti Billipot JILL Dilloti Dilloti Billipot JILL Dilloti Dilloti Billipot | Infl Divid Dividual Dividual Infl Dividual Dividual Dividual Dividual Didual Dividual <td>LLDL Don't Bit Insuk -DAL DOUTO2 BK Unsuk -DAL DOUTO2 BK Unsuk</td> <td>ALL Divini Divinous Divinous ALL Divinous Divinous Divinous</td>
 | LLDL Don't Bit Insuk -DAL DOUTO2 BK Unsuk | ALL Divini Divinous Divinous ALL Divinous Divinous Divinous |
| DBL DOURCE DOURCE BY Duput ABL D10 (D1102) BY Engut BY Engut ABL D10 (D111) D1001 BM2 BY Engut Annee FBL Hight Value Sampling 20 mm Correction: Values Sampling 20 mm Values *2000 µ app *2000 µ app

 |

 | 00. 00.01/02 00.00x/ 01. 01.01/01/01 Mode 01. 01.01/01/01 0Mode 01. 01.01/01/01 0Mode 01.01/01/01 0MMOde Bit Input 01.01/01 0MMOde Bit Input 01.01/01 0MMOde Bit Input

 | III. DOU/02 DOU/102 BR Duppet III. D10 (bit) Ingpat BR Input III. D10 (bit) D100/2 BR Input III. D10 (bit) D100/2 BR Input III. D10 (bit) D100/2 BR Input III. D10 (bit) BR Input BR Input III. D100/2 BR Input BR Input III. D100/2 BR Input BR Input

 | Juli Obst/2 D01/02 D01/02 <td></td> <td></td> <td>Table Dowl/2 DOW/T02 Re Markat Table Dowl/2 DOW/T02 Re Markat Table DAL DOW/T02 Re Markat Table DAL DAL DAL Table DAL DAL Response Table DAL DAL DAL Table DAL DAL DA</td> <td>THE DOU2 DOU702 BR Output THE DOU702 BR Output BR Output THE DOU702 BR Input THO THE DOU702 BR Input THO</td> <td>Table Douz2 DOUTC2 BR Outpoint Table Douz2 DOUTC2 BR Input Table Douz2 DOUTC2 BR Input Table Douz2 Dout1 DoutC1 BR02 BR Input Table Douz2 Dout1 DNMT BR02 BR Input Table DATE DOUTC2 BR Input Table DATE DATE BR Input</td> <td>TAR FORUZ DOUTO2 BR Depart TAR FORUZ DOUTO2 BR Depart TAR DOUTO2 BR Depart TAR DOUTO2 BR Input TAR DOUTO2 BR Input</td> <td>THE COMUL COUTO2 Depart THE COUTO2 Depart Detarman THE COUTO2 Detarman Detarman THE Detarman Detarman Detarman THE Detarman Detarman Detarman THE Detarman Detarman Detarman THE Detarman Detarman Detarman</td> <td>III DGU42 DGU102 BR Dupput III DTDIn11 bitopat BR thopat III DTDIn11 Bitopat BR thopat III DTDIn11 Bitopat Bit thopat III DTDIn11 DNUT_BAD2 Bit thopat III DTDIN1 Bitopat Bit thopat</td> <td>III. DOUTO2 DF OUGAT III. DF OUTO2 DF OUTO2 IIII. DF OUTO2 DF</td> <td>-FML DOU/D2 DB / Dubut -FML DF0 / Dubut Bit Input -FML DF1 / Dubut Bit Input</td> <td>-10L 00/u2 00/01/02 88:00/wu4
-10L 01/01/01 bioger Bithopat
-10L 01/20/011 00/01_Bit02 Bithopat
-10L 01/20/01 00/01_Bit03 Bithopat
-10L 01/20/01 00/01_Bit03 Bithopat</td> <td>JBL D01/102 D01/004 JBL D01/004 Bit logat JBL D11/011 D1001_01 JBL D01/011 D1001_01 JBL D01/011 D10101_01 JBL D01/011 D10101_01 JBL D01/011 D10101_01 JBL D01/011 D10101_01 JBL D01/011 D10101_0403 JBL D01/011 D10101_0404 JBL D01/011_01044 B1 logat JDL D01/01_0405 B1 logat</td> <td>JBL Doluzi <thdoluzi< th=""> <thdoluzi< th=""></thdoluzi<></thdoluzi<></td> <td></td> <td>JBL D'0ur2 D'0ur2 BP Duput JBL D'0ur101 Hogan Br Hogan JBL D'01D'01 Hogan Br Hogan JBL D'01D'01 Br Hogan Br Hogan JBL D'01D'01 D'010 Br Hogan</td> <td>J.B. DOW/2 DOUTO2 BR Output J.B. D11 (D11) Hopset Bk Input J.B. D12 (D111) Hopset Bk Input J.B. D12 (D111) Hopset Bk Input J.B. D14 (D111) D1401_B402 Bk Input J.B. D14 (D111) D1401_B403 Bk Input J.B. D14 (D111) D1401_B403 Bk Input J.B. D14 (D111) D1401_B403 Bk Input</td> <td>J.BL DOW/2 DOW/102 BF Output J.BL D101/D1 Hogos Bk Input J.BL D12/D101/D1 Hogos Bk Input J.BL D12/D101/D1
 D1001_BR02 Bk Input J.BL D12/D101/D1 D1001_BR02 Bk Input J.BL D12/D101/D1 D1001_BR03 Bk Input J.BL D12/D101_D1 D1001_BR03 Bk Input J.BL D12/D101_D1 D1001_BR03 Bk Input</td> <td>III. D'Oluzi D'Oluzi De Oluzi III. D'Oluzi D'Oluzi De Normalia III. D'Oluzi De Normalia De Normalia</td> <td>-FML DOU/D2 DB / Dubut -FML DF0 / Dubut Bit Input -FML DF1 / Dubut Bit Input</td> <td>-10L 00/u2 00/01/02 88:00/wu4
-10L 01/01/01 bioger Bithopat
-10L 01/20/011 00/01_Bit02 Bithopat
-10L 01/20/01 00/01_Bit03 Bithopat
-10L 01/20/01 00/01_Bit03 Bithopat</td> <td>III DOUA2 DOUTO2 DR Durget III DOUA2 DR Durget DR provide III DR DURGE DR provide DR provide III DR DURGE DR provide DR provide IIII DR DURGE DR DURGE DR Provide</td> <td>Table Douz2 DOUT02 DE Output -Table DOUZ2 DE Output Deve -Table DOUZ2 DE Nort0 DEVE -Table DOUZ2 DE Nort0 DEVE -Table DE Output DEVE DE Nort0 -Table DE Output DEVE DEVE -Table DE Output DEVE DEVE</td> <td>Inst. TOPU/2 DR Output 0.001/02 DR Output DR Output 0.011/02 DR Output DR Output -001. DR 2010/01 DR Output DR Output</td>
 |

 |

 | Table Dowl/2 DOW/T02 Re Markat Table Dowl/2 DOW/T02 Re Markat Table DAL DOW/T02 Re Markat Table DAL DAL DAL
 Table DAL DAL Response Table DAL DAL DAL Table DAL DAL DA

 | THE DOU2 DOU702 BR Output THE DOU702 BR Output BR Output THE DOU702 BR Input THO

 | Table Douz2 DOUTC2 BR Outpoint Table Douz2 DOUTC2 BR Input Table Douz2 DOUTC2 BR Input Table Douz2 Dout1 DoutC1 BR02 BR Input Table Douz2 Dout1 DNMT BR02 BR Input Table DATE DOUTC2 BR Input Table DATE DATE BR Input

 | TAR FORUZ DOUTO2 BR Depart TAR FORUZ DOUTO2 BR Depart TAR DOUTO2 BR Depart TAR DOUTO2 BR Input

 | THE COMUL COUTO2 Depart THE COUTO2 Depart Detarman THE COUTO2 Detarman Detarman THE Detarman Detarman Detarman THE Detarman Detarman Detarman THE Detarman Detarman Detarman THE Detarman Detarman Detarman

 | III DGU42 DGU102 BR Dupput III DTDIn11 bitopat BR thopat III DTDIn11 Bitopat BR thopat III DTDIn11 Bitopat Bit thopat III DTDIn11 DNUT_BAD2 Bit thopat III DTDIN1 Bitopat Bit thopat

 | III. DOUTO2 DF OUGAT III. DF OUTO2 DF OUTO2 IIII. DF OUTO2 DF

 | -FML DOU/D2 DB / Dubut -FML DF0 / Dubut Bit Input -FML DF1 / Dubut Bit Input

 | -10L 00/u2 00/01/02 88:00/wu4
-10L 01/01/01 bioger Bithopat
-10L 01/20/011 00/01_Bit02 Bithopat
-10L 01/20/01 00/01_Bit03 Bithopat
-10L 01/20/01 00/01_Bit03 Bithopat
 | JBL D01/102 D01/004 JBL D01/004 Bit logat JBL D11/011 D1001_01 JBL D01/011 D1001_01 JBL D01/011 D10101_01 JBL D01/011 D10101_01 JBL D01/011 D10101_01 JBL D01/011 D10101_01 JBL D01/011 D10101_0403 JBL D01/011
D10101_0404 JBL D01/011_01044 B1 logat JDL D01/01_0405 B1 logat
 | JBL Doluzi Doluzi <thdoluzi< th=""> <thdoluzi< th=""></thdoluzi<></thdoluzi<>
 |

 | JBL D'0ur2 D'0ur2 BP Duput JBL D'0ur101 Hogan Br Hogan JBL D'01D'01 Hogan Br Hogan JBL D'01D'01 Br Hogan Br Hogan JBL D'01D'01 D'010 Br Hogan
 | J.B. DOW/2 DOUTO2 BR Output J.B. D11 (D11) Hopset Bk Input J.B. D12 (D111) Hopset Bk Input J.B. D12 (D111) Hopset Bk Input J.B. D14 (D111) D1401_B402 Bk Input J.B. D14 (D111) D1401_B403 Bk Input J.B. D14 (D111) D1401_B403 Bk Input J.B. D14 (D111) D1401_B403 Bk Input
 | J.BL DOW/2 DOW/102 BF Output J.BL D101/D1 Hogos Bk Input J.BL D12/D101/D1 Hogos Bk Input J.BL D12/D101/D1 D1001_BR02 Bk Input J.BL D12/D101/D1 D1001_BR02 Bk Input J.BL D12/D101/D1 D1001_BR03 Bk Input J.BL D12/D101_D1 D1001_BR03 Bk Input J.BL D12/D101_D1 D1001_BR03 Bk Input
 | III. D'Oluzi D'Oluzi De Oluzi III. D'Oluzi D'Oluzi De Normalia III. D'Oluzi De Normalia De Normalia
 | -FML DOU/D2 DB / Dubut -FML DF0 / Dubut Bit Input -FML DF1 / Dubut Bit Input | -10L 00/u2 00/01/02 88:00/wu4
-10L 01/01/01 bioger Bithopat
-10L 01/20/011 00/01_Bit02 Bithopat
-10L 01/20/01 00/01_Bit03 Bithopat
-10L 01/20/01 00/01_Bit03 Bithopat
 | III DOUA2 DOUTO2 DR Durget III DOUA2 DR Durget DR provide III DR DURGE DR provide DR provide III DR DURGE DR provide DR provide IIII DR DURGE DR DURGE DR Provide | Table Douz2 DOUT02 DE Output -Table DOUZ2 DE Output Deve -Table DOUZ2 DE Nort0 DEVE -Table DOUZ2 DE Nort0 DEVE -Table DE Output DEVE DE Nort0 -Table DE Output DEVE DEVE -Table DE Output DEVE DEVE | Inst. TOPU/2 DR Output 0.001/02 DR Output DR Output 0.011/02 DR Output DR Output -001. DR 2010/01 DR Output DR Output
 |
| FILE OTI (Din1) trigger Bit regul FILE OTI (Din1) DNOT Bit regul Stature Active V Duation: Stature Active V Duation: Viving Ansteined V Value Active V Value Active V Value Active V Value Active V

 | -IDL D11 (Din1) trigger D2 forget -IDL D12 (Din1) D1400 (B402) B3 forget -IDL D12 (Din1) D1400 (B402) B3 forget -IDL D12 (Din1) D1400 (B402) B4 forget -IDL D12 (Din1) D1400 (B402) B4 forget -IDL D12 (Din1) D1400 (B402) B4 forget -IDL D14 (Din1) D1400 (B402) B4 forget -IDL D14 (Din1) D1400 (B402) B4 forget

 | 0.01 0.01 Moge# 0.01 Appa# Appa# Appa# Appa# Appa# Appa#

 | -R0L 011 Dinut) togger Bit from
R0L 012 Dinut Dinut Bat2 Bit from
-R0L 013 Dinut Dinut Bat2 Bit from
-R0L 013 Dinut Dinut Bat3 Bit from
R0L 014 Dinut Dinut Bat4 Bit from
Bit from
-R0L 014 Dinut Dinut Bat4

 | -10, 01 (Din) toget Balance Balance

 | -/0, Dr1(Din1) tripper BR Input
-/0, Dr1(Din1) DN/G BR02 BR Input
-/0, Dr12Din1] DN/G BR02 BR Input

 | -101, Di11Din11 togger Di21oput
-101, Di21Din11 Di1016 Bit02 Bitroput
-101, Di21Din11 Di1016 Bitr03 Bitroput

 | -100. 011 (Din1) 10994 04
https://discupression.org/discupressio

 | -10. 011 [Din1] bigger Bit now -10. 012 [Din1] DIN0_B402 Bit now -10. 012 [Din1] DIN0_B403 Bit now -10. 012 [Din1] DIN0_B403 Bit now -10. 015 [Din1] DIN0_B404 Bit now -10. 016 [Din1] DIN0_B404 Bit now -10. 016 [Din1] DIN0_B404 Bit now -10. 016 [Din1] DIN0_B405 Bit now

 | -100. 011 [Din1] 105per Reinpart -100. 011 [Din1] DIN0_E402 Reinpart -100. 012 [Din1] DIN0_E402 Reinpart -100. 012 [Din1] DIN0_E403 Reinpart -100. 015 [Din1] DIN0_E404 Reinpart

 | -FAR Oti T[Din1] tisper Bit Insut -FAR Dit2 [Din1] DBMD_B402 Bit Insut -FAR Dit3 [Din1] DBMD_B403 Bit Insut -FAR Dit3 [Din1] DBMD_B403 Bit Insut -FAR Dit3 [Din1] DBMD_B404 Bit Insut

 | -FAR Oti TQDint) tisperi Bit Insut -FAR Oti ZQDint) DNOT_BAD2 Bit Insut -FAR Oti ZQDint) DNOT_BAD3 Bit Insut -FAR Dit ZQDint) DNOT_BAD3 Bit Insut

 | -FIR011 bitgpari Bit Induit -FIR012[bit1] bitM01_B402 Bit Induit -FIR012[bit1] bitM01_B403 Bit Induit -FIR012[bit1] bitM01_B403 Bit Induit -FIR012[bit1] bitM01_B403 Bit Induit -FIR013[bit1] bitM01_B405 Bit Induit -FIR013[bit1] bitM01_B405 Bit Induit -FIR013[bit1] bitM01_B405 Bit Induit

 | -FIDL 011 Display Distribut -FIDL 012 Distribut Distribut Distribut -FIDL 012 Distribut Distribut Distribut -FIDL 012 Distribut Distribut Distribut -FIDL 013 Distribut Distribut Distribut -FIDL 015 Distribut Distribut Distribut

 | -/-0. 0.11 Disput Distribut -/0. 0.12 Distribut Distribut Distribut -/0. 0.13 Distribut Distribut Distribut -/0. 0.15 Distribut Distribut Distribut

 | -/10, 011 (Din1) bigger Bit Input
-/20, 012 (Din1) D1N(0)_B002 Bit Input
-/20, 013 (Din1) D1N(0)_B003 Bit Input
-/20, 014 (Din1) D1N(0)_B004 Bit Input
-/20, 014 (Din1) D1N(0)_B004 Bit Input

 | JBL D110 [bin1] bigger Bit locat JBL D12 [Din1] DN01_BR02 Bit locat JDL D12 [Din1] DN01_BR03 Bit locat JDL D14 [Din1] DN07_BR03 Bit locat JDL D14 [Din1] DN07_BR04 Bit locat JDL D14 [Din1] DN07_BR05 Bit locat
 | - 1.01 (Diri) topper Bainput
- 1.01 (Diri) NUT_BRO2 Bainput
- 1.01 (Diri) DIVI_BRO2 Bainput
- 1.01 (Diri) DIVI_BRO3 Bainput
- 1.01 (Diri) DIVI_BRO4 Bainput
- 1.01 (Diri) DIVI_BRO4 Bainput

 |
 | -0.01 D011 bitgset Bit freed -0.01 D012 Bit freed Bit freed -0.01 D0101 D0101 Bit freed
 | -1.00. 0.01 (Din1) bispar Ball Induct -0.01. 0.01 (Din1) DN07_BR02 Ball Induct -0.01. D10 (Din1) DN07_BR03 Ball Induct -0.01. D10 (Din1) DN07_BR03 Ball Induct -0.01. D1007_BR03 Ball Induct Ball Induct -0.01. D1007_BR03 Ball Induct Ball Induct -0.01. D1007_BR03 Ball Induct Ball Induct -0.01. D1007_BR05 Ball Induct Ball Induct

 | -1.00 0.01 (Din1) bigger Balagar -0.00 0.01 (Din1) DN01 (Bal2) Balagar -0.01 DN01 (Bal2) Balagar Balagar -0.01 DN01 (Bal2) Balagar Balagar | Ind CH1 Dirty Bigger Dirty Ind Dirty Dirty Dirty Dirty
 | -/-0. 0.11 Disput Distribut -/0. 0.12 Distribut Distribut Distribut -/0. 0.13 Distribut Distribut Distribut -/0. 0.15 Distribut Distribut Distribut
 | -/10, 011 (Din1) bigger Bit Input
-/20, 012 (Din1) D1N(0)_B002 Bit Input
-/20, 013 (Din1) D1N(0)_B003 Bit Input
-/20, 014 (Din1) D1N(0)_B004 Bit Input
-/20, 014 (Din1) D1N(0)_B004 Bit Input | -AR, DY1 Din1) tegger Bitmput
-AR, D(2) Din1) DNN(_BN2) Bit Pout
-AR, D(2) Din1) DNN(_BN2) Bit Pout
-AR, D(4) Din1) DNN(_BN3) Bit Pout
-AR, D(4) Din1) DNN(_BN3) Bit Pout
-AR, D(4) Din1) DNN(_BN3) Bit Pout | RC D11 (Din1) topper B2 https://
RC D12 (Din1) DNNT_B202 B2 https://
-RC D12 (Din1) DNNT_B203 B2 https://
-RC D14 (Din1) DNNT_B203 B2 https://
-RC D14 (Din1) DNNT_B204 B4 https://
-RC D14 (Din1) DNNT_B204 B4 https://
 | - FAL 011 Dinit) tigger BR troat
- FAL 012 Dinit Dinit() BR02 BR troat
- FAL 012 Dinit Dinit() BR03 BR troat
- FAL 013 Dinit Dinit() BR03 BR troat
- FAL 014 Dinit Dinit() BR03 BR troat |
| RL Diz (Dini) DiÑO BRO2 BRIrow RL Diz (Dini) DiÑO BRO2 BRIrow

 | - 101, Diz Dioni) Difut Bato Bato Bit Input
- 101, Diz Dioni) Difut Bato Bato Bit Input
- 101, Dist Diriti Difut Bato Bit Input
- 101, Dist Diriti Difut Bato Bit Input

 | DB Diz Dinij Divij Briz Bitov
UR Diz Dinij Divij Briz Bitov
UR Diz Dinij Divij Briz Bitov
UR Diz Dinij Divij Briz Bitov
Bitovi

 | DL 012(Din1) DB(01, Bh02 B4 Hood
DL 013(Din1) DB(01, Bh03 B4 Hood
DL 013(Din1) DB(01, Bh04 B4 Hood

 | -10, D(2)(D(n1) D(N0) 8002 Bit Input
-10, D(2)(D(n1) D(N0) 8003 Bit Input
-0, D(4)(D(n1) D(N0) 8003 Bit Input

 | 0.0, D12(Din1) D1N01_8x02 Bit Input
0.0, D13(Din1) D1N01_8x03 Bit Input

 | -00 DI3(Div1) DINOLBAC2 B& Input
-00 DI3(Div1) DINOLBAC3 B& Input

 | -10. 0.10.2 (Dirit) Differ BR02 Bit Input -10. 0.10.2 (Dirit) Differ BR02 Bit Input -10. 0.10.4 (Dirit) Differ BR03 Bit Input -10. 0.14 (Dirit) Differ BR03 Bit Input -10. 0.14 (Dirit) Differ BR03 Bit Input -10. 0.16 (Dirit) Differ BR05 Bit Input -10. 0.16 (Dirit) Differ BR05 Bit Input
 -10. 0.16 (Dirit) Differ BR05 Bit Input

 | -FIL Dis2(Din1) DiM0 (\$402 Bit Input -FIL Dis2(Din1) DIM0 (\$403 Bit Input -FIL Dis2(Din1) DIM0 (\$403 Bit Input -FIL Dis2(Din1) DIM0 (\$403 Bit Input -FIL Dis2(Din1) DIM0 (\$405 Bit Input -FIL Dis2(Din1) DIM0 (\$405 Bit Input

 | DB D(2) Divit DMM (1902) Bit Input -DB D(3) Divit DMM (1902) Bit Input -DB D(3) Divit
 DMM (1903) Bit Input -DB D(3) Divit DMM (1903) Bit Input -DB D(3) Divit DMM (1903) Bit Input -DB D(3) Divit DMM (1904) Bit Input

 | I.B. D12 (Din1) DNM (B402) B8 Input -D.B. D12 (Din1) DNM (B402) B4 Input -D.B. D14 (Din1) DNM (B403) B4 Input -D.B. D15 (Din1) DNM (B403) B4 Input

 | -FIR D12 (Din1) DNM (B402) B4 Input -FIR D12 (Din1) DNM (B402) B4 Input -FIR D14 (Din1) DNM (B403) B4 Input -FIR D15 (Din1) DNM (B405) B4 Input -FIR D15 (Din1) DNM (B405) B4 Input -FIR D15 (Din1) DNM (B405) B4 Input

 |

 | 10, 012/01n1 016/01_8402 Bitmout
-RU, 012/01n1 01407_8403 Bitmout
-RU, 014/01n1 01407_8403 Bitmout
-RU, 014/01n1 01407_8404 Bitmout
-RU, 016/01n1 01407_8406 Bitmout

 | - PDL 012 (Din1) DBM0_BM02 BB troot
PDL 013 (Din1) DBM0_BM03 BB troot
- PDL 013 (Din1) DBM0_BM03 BB troot
- PDL 014 (Din1) DBM0_BM05 BB troot
- PDL 014 (Din1) DBM0_BM05 BB troot

 | - PLD 012 (2014) 0.95(01) 8002 Bit Hoped
PLD 013 (2014) 0.0407 BA03 Bit Hoped
- PLD 014 (2014) 0.0407 BA04 Bit Hoped
- PLD 015 (2014) 0.0407 BA05 Bit Hoped

 | -IDL D122 D14 D16 D17 -IDL D122 D14 D14 D14 -IDL D122 D14 D14 D14 -IDL D121 D147 D14 D14 -IDL D147 D14 D14 D14 -IDL D147 D14 D14 D14 -IDL D147 D14 D14 D14
 | -102, 012/01/1 D16/01_8022 Bitrow
ADL, 012/01/1 D16/01_8023 Bitrow
-102, 014/01/1 D16/01_8023 Bitrow
-102, 014/01/1 D16/01_8026 Bitrow

 | -102 012/Din11 DiM0_B402 Bitrowi
-102 012/Din11 DiM0_B403 Bitrowi
-102 014/Din1 DiM0_B403 Bitrowi
-102 014/Din1 DiM0_B404 Bitrowi
 | -102, 012(Dini) DVR01_B002 Bitrowi
-102, 012(Dini) DVR01_B003 Bitrowi
-102, 014(Dini) DVR01_B003 Bitrowi
-103, 014(Dini) DVR01_B004 Bitrowi

 | -100, 012/01/11 D1007_08/02 B81/664
-100, 012/01/11 D1007_08/03 B81/664
-100, 014/01/11 D1007_08/04 B81/664
-100, 014/01/11 D1007_08/04 B81/664
 | -100, 012/01/11 D1007_8022 Bitrowi
-100, 012/01/11 D1007_8023 Bitrowi
-100, 014/01/11 D1007_8023 Bitrowi
-100, 014/01/11 D1007_8024 Bitrowi
 | - AR 012 (Din1) D1401_8402 B4 Input
- AR 013 (Din1) D1401_8403 B4 Input
- AR 04 (Din1) D1401_8404 B4 Input
- AR 04 (Dis1) D1401_8404 B4 Input
- AR 045 (Din1) D1401_8405 B4 Input
- D1 015 (Din1) D1401_8405 B4 Input | - PDL 012 (Din1) DBM0_BM02 BB troot
PDL 013 (Din1) DBM0_BM03 BB troot
- PDL 013 (Din1) DBM0_BM03 BB troot
- PDL 014 (Din1) DBM0_BM05 BB troot
- PDL 014 (Din1) DBM0_BM05 BB troot
 | - PLD 012 (2014) 0.95(01) 8002 Bit Hoped
PLD 013 (2014) 0.0407 BA03 Bit Hoped
- PLD 014 (2014) 0.0407 BA04 Bit Hoped
- PLD 015 (2014) 0.0407 BA05 Bit Hoped |
 | -10, 012/0111 D/019/8022 Bitrowi
-10, 012/0111 D/017/8023 Bitrowi
-10, 014/D011 D/017/8023 Bitrowi
-10, 014/D011 D/017/8026 Bitrowi | -0.0. 012 (Din1) D6(01, Bh2 Bb
-0.0. 013 (Din1) D6(01, Bh2 Bb
-0.0. 014 (Din1) D6(01, Bh2 Bb |
| -III. D1010/11 D1N07/2403 Bit Input -III. D1010/11 D1N07/2403 Bit Input -III. D15 (D111) D1N07/2403 Bit Input -III. D15 (D111) D1N07/2403 Bit Input -III. D101/0111 D1N07/2403 Bit Input Stature Aptive Values Duation: undefined Values Stature Aptive Values Duation: undefined Values Values Aptive Values Sampler 20mm

 | -012 013/01611 DNND_8043 84 Input
-012 014/0141 DNND_8044 84 Input
-012 015/01611 DNND_8045 84 Input

 | UL US UT [Unit] DNUT_BAG3 B4 Input UL D4 (Din1) DNUT_BAG4 B4 Input UL D10 (Din1) DNUT_BAG5 B4 Input

 | -,0,0,013(Din1) DIN01_8403 Bit Input
-,0,0,044(Din1) DIN01_8404 Bit Input

 | RI DI3 (Din1) DIN01_8403 B8 Input
RI DI4 (Din1) DIN01_8404 B8 Input

 | -ML DI3(DIn1) DIN01_B#03 Bit Input

 | -00 DI3/Din11 DIN01 8403 B4 Insut

 | Int Dist Divit Divit Bits Bit mode Int Dist Divit Divit Bits Bits Bit mode Int Dist Divit Divit Bits Bits Bits Bits

 | -RIL D102 [D011] D1001 [8403 B4 Insold -RIL D145 [D011] D1001 [8404 B4 Insold -RIL D145 [D011] D1001 [8405 B4 Insold -RIL D145 [D011] D1001 [8405 B4 Insold -RIL D145 [D013] D1001 [8405 B4 Insold

 |

 | - FAIL D132 (Din1) D1010 ⁻

 | - FAIL 0.132 (Din1) DNINT_BR03 B&t input - FAIL 0.163 (Din1) DNINT_BR04 B&t input - FAIL 0.163 (Din1) DNINT_BR05 B&t input - FAIL 0.163 (Din1) DNINT_BR05 B&t input - FAIL 0.163 (Din1) DNINT_BR05 B&t input

 | - FIL 0.13 (Din1) DNMT_BR03 B4 Input - FIL 0.15 (Din1) DNMT_BR04 B4 Input - FIL 0.15 (Din1) DNMT_BR05 B4 Input - FIL 0.15 (Din1) DNMT_BR05 B4 Input - FIL 0.15 (Din1) DNMT_BR05 B4 Input

 | -PIIL 013210hr1 D1M07_B403 B4 Input -PIIL 014510hr1 D1M07_B404 B4 Input -PIIL 015510hr1 D1M07_B405 B4 Input -PIIL 015510hr1 D1M07_B405 B4 Input -PIIL 015510hr1 D1M07_B405 B4 Input

 | -PDL D13 Dhr11 DH017 Br03 B4 Input -PDL D45 Dhr11 DH017 Br04 B4 Input -PDL D45 Dhr11 DH017 Br04 B4 Input -PDL D45 Dhr11 DH017 Br04 B4 Input -PDL D45 Dhr11 DH017 Br05 B4 Input

 | -/IDL 013 (Din1) DNI01 2603 B4 Input
-/IDL 014 (Din1) DNI01_2604 B4 Input
-/IDL 015 (Din1) DNI01_2605 B4 Input

 | -101, D13 (D1vh1) D1N107 (B403 B4 hoput
100, D14 (D1vh1) D1N107, B404 B4 hoput
201, D14 (D1vh1) D1N107, B405 B4 hoput
 | 0.0 D01D/m11 D1N07_B003 B4 Input
0.0 04 (Din11 D1N07_B004 B4 Input
0.0 05 (Din11 D1N07_B004 B4 Input
0.0 05 (Din11) D1N07_B005 B4 Input

 | RE 0/3 (Din1) DIN07_5903 B8 Input
-RE 0/4 (Din1) DIN07_5904 B9 Input
-RE 0/5 (Din1) DIN07_5805 B8 Input
 | -101 DO1Din1 D1NUT_B003 B4 result
-101 D44 Din11 D1NUT_B004 B4 heput
-101 D55 Din11 D1NUT_B005 B4 heput

 | -1.02, 012/10/m1 D1N07_8003 84/new4
-02, 04/10/m1 D1N07_8044 84/new4
-02, 045/00/m1 D1N07_8045 84/new4
 | -1.02, 0.02 (Divit) DIVID_2003 84 (mps)
-02, 0.04 (Divit) DIVID_2004 84 (mps)
-02, 0.05 (Divit) DIVID_2004 84 (mps)
-02, 0.05 (Divit) DIVID_2005 84 (mps)
 | -PIIL 013 (Din1) DNMT_BR03 B4 Input -PIIL 015 (Din1) DNMT_BR04 B4 Input -PIIL 015 (Din1) DNMT_BR05 B4 Input -PIIL 015 (Din1) DNMT_BR05 B4 Input | -PDL D13 Dhr11 DH017 Br03 B4 Input -PDL D45 Dhr11 DH017 Br04 B4 Input -PDL D45 Dhr11 DH018 Br04 B4 Input -PDL D45 Dhr11 DH018 Br05 B4 Input -PDL D45 Dhr11 DH018 Br05 B4 Input
 | -/IDL 013 (Din1) DNI01 2603 B4 Input
-/IDL 014 (Din1) DNI01_2604 B4 Input
-/IDL 015 (Din1) DNI01_2605 B4 Input | -RR, D(3)(Din1) D1N07_BR03 B4 Input
-RR, D(4)(Din1) D1N07_BR04 B4 Input
-RR, D(4)(Din1) D1N07_BR04 B4 Input
 | 00, D13 (Dim1) D1N0T_8403 B4 Input
00, D4 (Dim1) D1N0T_8404 B4 Input
00, D5 (Dim1) D1N0T_8404 B4 Input | D_13(D/m1) DINOT_5403 Bit Input
D_1_04(D/m1) DINOT_5404 Bit Input
D_104(D/m1) DINOT_5404 Bit Input |
| 1.00. 0.014 (Din1) DINUT_BRIA Bit Input 1.00. 0.016 (Din1) DINUT_BRIA Bit Input 1.00. DIR DIN1 DIN01_BRIA Bit Input 1.00. Diration undefined V Viring Anstein V Duration 1.00. Diration undefined V Viring Sampling 2.0 ms V Varing Tormer* Diration*

 | - JUL D14 (D1n1) D1N01_B4004 B4 Input
- JUL D14 (D1n1) D1N01_B4004 B4 Input
- JUL D15 (D1n1) D1N01_B4005 B4 Input

 | D14 (Din1) D1N01_B404 Bit Input QL D15 (Din1) D1N01_B405 Bit Input

 | - III D14 (Di-1) DN0T_B004 B4 Input

 | -BR DI4[Din1] DIN07 B404 B4 Inst

 |

 | AND

 | -100 014 (Din1) DIN07_BR04 B4 tripud -000 016 (Din1) DIN07_BR05 B4 tripud

 | - 10, 04 (Div1) DNUT_8404 B4 (read
- 10, 05 (Div1) DNUT_8405 B4 (read
- 10, 05 (Div1) DNUT_8405 B4 (read

 | -102, D14 [Din1] D1007_20104 B4 (regul
-102, D15 [Din1] D1007_20105 B4 (regul
-102, D1007_20105 B4 (regul)
-102, D

 | - DIL OH (Din1) DNM_R044 B4 Input - DIL 045 (Din1) DNM_R045 B4 Input

 | - 101, 014 (Din1) DNNT_B004 B4 (nput
- 101, 015 (Din1) DNNT_B056 B4 (nput
- 101, 015 (Din1) DNNT_B056 B4 (nput

 | - 101, 014 (Din1) DNNT_2004 B4 (nput
- 101, 015 (Din1) DNNT_2005 B4 (nput
- 101, 015 (Din1) DNNT_2005 B4 (nput

 | - 10, 0 k4 (Din1) D (NUT, BAN4 B4 keput
- 10, 0 K5 (Din1) D (NUT, BAN5 B4 keput
- 10, D (B (Din1) D (NUT, BAN5 B4 keput

 | - JUL D14 (Din1) D1N0T_B404 B4 Input
- JUL 015 (Din1) D1N0T_B405 B4 Input
- JUL 015 (Din1) D1N0T_B405 B4 Input

 | -JJL D14 [Din1] D1N07_B804 B& Input
-JJL D15 [Din1] D1N07_B805 B& Input

 | -JUL D/4 (Din1) DIN01_B804 Bit Input
-JUL D/5 (Din1) DIN01_B805 Bit Input
 | -I/L 0/4 (Din1) DIN01_8804 Bk Input
-I/L 0/5 (Din1) DIN01_8805 Bk Input

 | D14 (Din1) DIN01_B404 B4 Input
D15 (Din1) DIN01_B405 B4 Input
 | -JUL DI4 (Din1) DIN01_B404 B& Input
-JUL 0/5 (Din1) DIN01_B405 B& Input
 | -10. D14 [Din1] D1N01_B804 Bå teput
-0.0. D15 [Din1] D1N01_B805 Bå teput

 | -10, 014 (01n1) D1N01_8404 Bit Input
-101_015 (015 01n1) D1N01_8405 Bit Input
-010_015 (015 01n1) D1N10_8405 Bit Input | - JU, D14 [D1n1] D1N07_B404 B4 heput
- JU, D15 [D1n1] D1N07_B405 B8 heput
- D0, D15 [D1n1] D1N07_B405 B8 heput
 | - JUL D14 (Din1) D1N0T_B404 B4 Input
- JUL 015 (Din1) D1N0T_B405 B4 Input
- JUL 015 (Din1) D1N0T_B405 B4 Input
 | -JJL D14 [Din1] D1N07_B804 B& Input
-JJL D15 [Din1] D1N07_B805 B& Input | -AD DI4 (DIn1) DIN0T_8404 Bit Input
-AD DI5 (DIn1) DIN0T_8405 Bit Input
 | -101, D14 (D1n1) D1N07_B404 B4.input
-010 D16 (D1n1) D1N07_865 B4.input | - 101 014 (DIn1) DIN07_8404 B4 Input
- 101 015 (DIn1) DIN07_8404 B4 Input |
| -III. 016 (Din1) DIN0_BR05 Bit legue ALL 016 (Din1) DIN0_BR05 Bit legue Status Aptive V Dutation: Status: Aptive V Dutation: Status: Aptive V Status: Aptive V Status: Aptive V Vining: Aptive V

 | -00 0(5(0)n1) 0(00 8065 8tingut

 | III 0/5(DIn1) DIN01_8405 Bit Input

 | 88 000 (00-1) 0.000 0.000 0.000 0.000

 |

 | -RL DI4[DIn1] DIN01_B804 Bt input

 | -RE_DI4(Din1) DIN01_BA04 B& Input

 | -101, 015(Din1) DNND_8605 Bit leput
-101, 015(Din1) DNND_8065 Bit leput
-101, 015(Din1) DNND_8065 Bit leput

 | -00. 0/5(Din1) DIN07_6805 Bit Input
-00. D/6 Din1 DIN07_6805 Bit Input

 | 02, 0/6 (Din1) DN01_8605 Bit leput
-02, 0/6 (Din1) DN01_8605 Bit leput
-02, 0/2 (Din1) DN01_8405 Bit leput

 | -JUL 0/5 (Din1) DIN01_6405 Bit Input
-JUL 0/6 (Din1) DIN01_6406 Bit Input
-JUL 0/6 (Din1) DIN01_6406 Bit Input

 | -JUL DIS (DIn1) DIN01_6805 Bit Input
-JUL DIS (Din1) DIN01_6805 Bit Input
DIS (Din1) DIN01_6805 Bit Input

 | -101 DIS(DIn1) DIN01_8405 Bit Input
-101 DIS(DIn1) DIN01_8405 Bit Input

 | -JQL DIS(DIn1) DIN07_8805 Bit Input
-JQL DIS(DIn1) DIN07_8805 Bit Input

 | - 10, 0% (Din1) DIN01 8405 B4 Input
- 00 0% (Din1) DIN01 8405 B4 Input

 | - 015 (Din1) DIN01_8405 Bit Input

 | JUL 015(Din1) DIN01_8805 Bt input
 | -JIL DIS [Din1] DIN01_BH05 Bit Input

 | -JUL DIS(DIn1) DIN01_8805 Bit Input
 | -JJL 0/5(0In1) 0IN01_8405 Bit Input

 | -RL D(S(D(n1) D)N01_BR05 Bit input
 |
 | -101 D(5(D(n1) D(N0)_8805 Bt Input
-101 D(5(D(n1) D(N1)_8805 Bt Input | - 10, 0% (Din1) DIN01 8405 B4 Input
- 00 0% (Din1) DIN01 8405 B4 Input
 | - 015 (Din1) DIN01_8405 Bit Input | -JUL DIS(DIn1) DIN01_8805 Bit Input
 | -00 DIS(D(n1) DIN01 8405 Bit Insut | -BE DIS (Dis1) DINDE BROS BE leave |
| Int Dist Dinit Dinit Brid Brid All Dir/T Dinit Dinit Brid Brid strati Diration undefrind Wing Contection Linear Ving Status Active: V Duration: undefrind V Noner Fill IF Sampling 2.0 ms V Linear V

 | BB DIF DUTY DIALO DIAL DIAL

 |

 | PUL VIS (VINT) VINVI_SKO SKINKK

 | -JUL 0/5 (Din1) DIN01_8405 Bit Input

 | -00 DIS DIe11 DIN01 8405 Bt Innet

 |

 | IIL DIS Din11 DIN01.8405 Bit Input
-IIL DI7 Din11 DIN01.8405 Bit Input

 | - III DIS (DIn1) DIN0 BIOS Bit Input

 | III. D16 (Din11 DiN01, B406 Bk Input
III. D12 (Din11 DiN01, B407 Bk Input

 | - III. 016 (Din11 DIN01_B806 Bit Input
DID 012 (Din11 DIN01_B807 Bit Input

 | - RL DIS (DIn1) DIN0 BADS Bit Input

 | -III DIN01_B406 Bit Input

 | -RL DIS(Din1) DIN01_Bit06 Bit Input

 | DI DIS (Dis1) DINOT RADE BAland

 | The second

 |
 |

 |
 | The second s
 | BR BULGELEN BULGE BURGE BURGE

 | DI DIE DIA11 DINOT RADE Rèlensé | -10 DIS (Din1) DIN01 BitOS Bit Input
 | DI DIS (Dis1) DINOT RADE BAland
 | The second | | AMP AND
 | The statement for the statement of the s |
| LFLIX DV/ID/ID/II DN/ID BK/7 Bit recut armel Statut Active W Dutation: undefined W Statut Active W Dutation: undefined W Contextion: Lineor W Name FBL IEFT Sampling 20 ms W Page 10000 µ sps #

 | Fill orginality organization or repart

 | AL DIS (DIn1) DIN01_BR05 B# Input

 | -I/L DI6 (Din1) DIN01_Bk06 Bit Input

 | 68 DIE (Die1) DIMOS BACK BALend

 | - Mr. enderning ender

 | -JUL DIS[DIn1] DIN01_8805 Bit Input

 | HAL DI7 (Din1) DIN01 Bk07 Bk Input

 | 0.0 (0.1.1.) (0.1.0.0 (0.0.0.) (0.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1.1

 | LER DIZ (DIo1) DIN01 Bi07 Bit loss

 | -BE D/Z (Die1) DIN01 B207 B2 loss1

 | 88 847 (B) 51 (B) 101 (B) 877 (B) 100

 |

 |

 | Lar molecular evides

 | -JUL DIS(DIn1) DIN01_BHDS Bit Input

 | -AL DIS (Din1) DIN01_Bit06 Bit Input
 | -JIL DI6(Din1) DIN01_Bi06 Bit Input

 | -AL DIS (Din1) DIN01_BR06 Bit Input
 | - ML DI6(Din1) DIN01_Bi06 Bit Input
 | -JUL DIS [DIN1] DIN01_BR06 BR Input

 | The eventual events events |
 | Lar molecular evides
 | -JUL DIS(DIn1) DIN01_BHDS Bit Input | -AL DIS(DIn1) DIN01_Bi06 Bit Input
 | -JJL DIS(DIn1) DIN01_Bk06 Bit Input | -ML DI6(Din1) DIN01_Bi06 Bit Input |
| annel
Statut Active V Duration undefined V Conscion Linear V
Wring Angline Sampling 20ms V Lood range 2000 µ ps
Sampling Linear V Sampling 20ms V Lood range 2000 µ ps

 | -AL DI7 [Din1] DIN01 Bk07 Bk Input

 | AL DI7 (Din1) DIN01 8k07 Bit Input

 | The state of the s

 | The molecular and bridge

 | -III Dis(Din1) DIN01_BR06 Bit Input

 | - AL 0/5 (Din1) DIN01_8805 Bit Input
- AL DI6 (Din1) DIN01_8806 Bit Input

 |

 | HILE DE DINE DINE DE DE DE

 | THE WITHIN PRIVE PRIVE PRIVE

 | Park on round one of the

 | Han outonal outor Big upor

 | HJUL DI7 [DIN1] DIN01 BK07 Bit Input

 | H/JL DI7[Din1] DIN01 BK07 Bk Input

 | HIL DI7 (Din1) DIN01 Bit07 Bit Input

 | -AL DI7 (DIn1) DIN01 Bit07 Bit Input

 | -BE DIZ (Dio1) DIN01 BH0Z BELION E
 | - 00 DV2 (D(s1) DIN01 B207 B2 loss t

 | LBB DIZ (Dio1) DINO1 B207 B2 loss t
 | -AR D(7 (D(n)) D(N) B#07 Bt Inext
 | HIL DI7 (Din1) DIN01 Bk07 Bk Input

 | H/III DI/Oln11 DIN01 8407 Bit Input | H/JL DI7[Din1] DIN01 BK07 Bk Input
 | HIL DI7 (Din1) DIN01 Bit07 Bit Input
 | -//L DI7 (DIn1) DIN01 Bk07 Bit Input | - RE D/Z (D(s1) DN01 DN02 DE loss t
 | 0.0 (5) (5) (5) (5) (5) (5) (5) (5) (5) (5) | |
| Statu: Active: V Dutation: undefined V Connection: Linear V Wring: Anspire: Sampling: 20 ms Topic range: #2000 µ spir #2000 µ spir None: FBL [FF] Sampling: Undefined Valence: "Duran" Duran" Duran"

 |

 |

 | HUL DIVIDINI DIVIDI BKOV BK INDUK

 | -AL DI7 DIn11 DIN01 BK07 Bt Insut

 | -ID DI6 Din1 DIN01 BAG Bit Input
-ID DI7 Din1 DIN01 BAG Bit Input

 | -101, 015 (Din1) DIN10, 8665 88 Higur
-101, DIS (Din1) DIN10, 8665 88 Higur
-101, 015 (Din1) DIN10, 8665 88 Higur

 |

 |

 |

 |

 |

 |

 |

 |

 |

 | and an initial and an and
 | Care An India Anna Anna Anna Anna Anna Anna Anna An

 | LAR ALIMIT AND AND DURA
 |

 |
 |
 | |
 | | Park on long over DUBDA
 | HALL DEVIDENT DRVD BR INDU | -JUL DI7 [Din1] DIN01 Bk07 Bit Input |
| Wing Amplifier V Sampling 2.0 ms rpot range:
(amplifier) 2.000 µ eps Name PR LFT Sampling Lindefined Visions' Zin ans''

 | Channel

 |

 | vey - TIT Os loui l Oural Bras. Bruch

 | LALL VOR JUNIT DINOT BKO7 BK Input

 | 101. D015 (Din1) D01401 Strole Bit Input 101. D017 (Din1) D01401 Brido? Bit Input

 | -10L 005 (Din1) D01013-8805 Bit Input
-20L 005 (Din1) D01013-8805 Bit Input
-20L 007 (Din1) D01013-8805 Bit Input
-20L 007 (Din1) D01013-8807 Bit Input

 |

 | www

 | annel and a second s

 | WINS .

 | nnel

 | non

 | mel

 | wei -

 |

 | 2005 VITISTIET VITET VITET VITET VITET VITET
 | and

 | PAUL VELICIENT VIEW DELECT
 |

 |
 | annel
 | rand | wei -
 | | raus viri lomiti virivi vivi Otilisti
mol
 | Hall on roman one Brinder | -DL D/7/D/n11 D/N01 B/07 B// npu/ |
| Name PB LIFT Sampler Undefined v Yactor 1"a end"

 | Status Active V Duration undefined V Costection Linear V

 | el
tatur Active v Duration undefined v Corection Linear v

 | -IIIL DIFUTIONITI DIFUTI BADY DE FINOU
Vinel
Statut Active V Duration: undefined V Correction: Linear V

 | Land our particul before befor

 | DIS [Din1] DIN01 Brads Balleput DIN01 Brads Balleput DIN01 Brads Balleput Sonau Active V Duration undefined V Contection Linear V

 | -DL DIS Diral DISIDI BROS Bit Input -DL DIS Diral DISIDI BROS Bit Input -DL DIS Diral DISIDI BROS Bit Input -DL DIS Diral DINIGT BROS Bit Input -Statut Active V Dustion Statut Active V Correction:

 | Status Active V Duration undefined V Conection Linear V

 | strati
Statu: Active V Duration undefined V Conection Linear V

 | Statu: Active V Duration undefined V Conection Linear V

 | Statu: Active V Dutation undefined V Correction Linear V

 | nnel
Status Active V Duration undefined V Conection Linear V

 | Status Active v Duration undefined v Contection Linear v

 | nnel
Status Active V Duration undefined V Costection Linear V

 | nnel
Statux Active V Duration undefined V Correction Linear V

 | Statux Active V Duration undefined V Correction Linear V

 | nel active v Duation undefined v Contraction Linear v
 | annel Statut Active V Duasion undefined V Contection Linear V

 | Production of the second of th
 | annel Stauti Active V Durston undefined V Contection Linear V
 | arrel
Statur: Active V Duaston undefined V Correction Linear V
 | annel
Statur: Active V Duaston undefined V Correction Linear V
 | nnel
Status Active V Duration undefined V Correction Linear V
 | nnel
Statux Active V Duration undefined V Correction Linear V
 | val
Status Active V Duration undefined V Correction Linear V | nnel Status Active V Dutation undefined V Correction Linear V | immel oninoi entor entor entor entor entor
status Active V Duasion undefined V Contection Linear V
 | -JIL DINOT BAD? BERROW
wel
Status Active V Duration undefined V Correction Linear V |
| and a support of the second se

 | Dromol Status Active ♥ Duration undefined ♥ Conection Linear ♥ 2000 µ eps

 | el
Jahur Active V Duration undefined V Correction Linear V
Incol range incol v Sumpling 20me V

 | -IIIL DIFUTION1 DIFUT BADY DIFUTO BADY DIFUTO

 | Link Ord Burniti Diversion Diversion <thdiversion< th=""> <thdiversion< th=""> <thdive< td=""><td>BDL Diskit Ends Bit Incut BL Diskit Ends Bit Incut Statut Active Duardon undefined Wing Statut Control on Supplier Wing Supplier Sampling 2.0 m</td><td>-DL DIS Diral DISIDI BROS Bit Input -DL DIS Diral DINITI BROS Bit Input Sampling variant variant Vering Active variant Vering Sampling 2.0 ms</td><td>Status: Active Voing Langtlier Sampling 20 mm Connection Linear Connection Linear Connection Conne</td><td>Vieng Lengther V Dutation undefined V Correction Linear V
Vieng Lengther V Sampling 20 ma V Lengther 1=2000 µ app.</td><td>Viving Langtine V Duration: undefined V Conection: Linear V
Viving Langtine V Sampling 2.0 mm V Import Import Langton = 2000 µ apps</td><td>Veries Conscion Linear V
Wring Constitute Sampling 20 mm V Conscion Linear V
Sampling 20 mm V Sampling 20 mm V</td><td>vanid
Status Active V Dusation undefined V Correction Linear V
Wring Angeliev Sampling 20 mm V Sampling 120 mm V</td><td>ander
Status Active v Dusation undefined v Conection Unear
Wang Cangetine v Sampling 20 mm v Sampling in 2000 µ eps</td><td>And Active Conscience Conscience Conscience Conscience Conscience Conscience Conscience Conscience Conscience</td><td>Vinel
Status Active V Durations undefined V Consistion Linear
Winey Cognitive Samplerg 20 ms V International June V</td><td>Statu: Active Ving Amplifie Sampling 20 ms Ving Amplifie Ving Amplifie</td><td>And a feature of the second of</td><td>amel
Statu: Active V Duration undefined V Conscion Linear V
Wing Angelier V Sampling 20 mm V Sampling 20 mm V</td><td>Vining Langtler Vining Langtler</td><td>Vernel Status Active V Duration undefined V Correction Linear V
Wring Active Sampling 20 mm V Indefined Linear V</td><td>Status: Active V Dutation undefined V Contection Linear V
Wring Lengther V Sampling 20 ms V Sampling 20 ms V</td><td>annal
Slaturi Active V Duation undefined V Conscision Linear V
Wang Angeler V Sampling 20 ms V Ignetifier) #2000 a sps</td><td>nnel
Status Active V Duration undefined V Conection Linear V
Wring Carpitro V Sampling 20 nn V Internet Participation (not single) +2000 (state)</td><td>Vinel
Status Active V Durations undefined V Consistion Linear
Winey Cognitive Samplerg 20 ms V International June V</td><td>Vel Vel Oursion: Correction: Linear V Statu: Active Velocity Sampling 20 ms Correction: Linear V Wring: Amplifier Velocity Sampling Velocity Incertifier Velocity Incertifier Velocity Velocit</td><td>Innel Innel Status Active Viting Angelies Sampling Dutation undefined Correction Linear Propr range Pr</td><td>Paula or romani onino encor encor encor encor encore enco</td><td>-IIIL DIYTDIn11 DIN01 BAO7 BEFroux venil v</td></thdive<></thdiversion<></thdiversion<>
 | BDL Diskit Ends Bit Incut BL Diskit Ends Bit Incut Statut Active Duardon undefined Wing Statut Control on Supplier Wing Supplier Sampling 2.0 m

 | -DL DIS Diral DISIDI BROS Bit Input -DL DIS Diral DINITI BROS Bit Input Sampling variant variant Vering Active variant Vering Sampling 2.0 ms

 | Status: Active Voing Langtlier Sampling 20 mm Connection Linear Connection Linear Connection Conne

 | Vieng Lengther V Dutation undefined V Correction Linear V
Vieng Lengther V Sampling 20 ma V Lengther 1=2000 µ app.

 | Viving Langtine V Duration: undefined V Conection: Linear V
Viving Langtine V Sampling 2.0 mm V Import Import Langton = 2000 µ apps

 | Veries Conscion Linear V
Wring Constitute Sampling 20 mm V Conscion Linear V
Sampling 20 mm V Sampling 20 mm V

 | vanid
Status Active V Dusation undefined V Correction Linear V
Wring Angeliev Sampling 20 mm V Sampling 120 mm V

 | ander
Status Active v Dusation undefined v Conection Unear
Wang Cangetine v Sampling 20 mm v Sampling in 2000 µ eps

 | And Active Conscience Conscience Conscience Conscience Conscience Conscience Conscience
Conscience Conscience

 | Vinel
Status Active V Durations undefined V Consistion Linear
Winey Cognitive Samplerg 20 ms V International June V
 | Statu: Active Ving Amplifie Sampling 20 ms Ving Amplifie

 | And a feature of the second of
 | amel
Statu: Active V Duration undefined V Conscion Linear V
Wing Angelier V Sampling 20 mm V Sampling 20 mm V

 | Vining Langtler
 | Vernel Status Active V Duration undefined V Correction Linear V
Wring Active Sampling 20 mm V Indefined Linear V
 | Status: Active V Dutation undefined V Contection Linear V
Wring Lengther V Sampling 20 ms V Sampling 20 ms V
 | annal
Slaturi Active V Duation undefined V Conscision Linear V
Wang Angeler V Sampling 20 ms V Ignetifier) #2000 a sps
 | nnel
Status Active V Duration undefined V Conection Linear V
Wring Carpitro V Sampling 20 nn V Internet Participation (not single) +2000 (state)
 | Vinel
Status Active V Durations undefined V Consistion Linear
Winey Cognitive Samplerg 20 ms V International June V
 | Vel Vel Oursion: Correction: Linear V Statu: Active Velocity Sampling 20 ms Correction: Linear V Wring: Amplifier Velocity Sampling Velocity Incertifier Velocity Incertifier Velocity Velocit | Innel Innel Status Active Viting Angelies Sampling Dutation undefined Correction Linear Propr range Pr | Paula or romani onino encor encor encor encor encore enco | -IIIL DIYTDIn11 DIN01 BAO7 BEFroux venil v |
| Analysis of Televisian International Internationae Interna

 | Churnel v Duration undefined v Connection: Linear v Vering Lington v Sampling 2.0 ms Incutation: 1/2 medition:

 | el
tatuz Active v Duration undefined v Correction Linear
vieng Impeties v Sampling 20 ms v Indefined v V Actor 1 'la eps'

 | HIL OFFOTIONT OFFOTIONT OFFOTIONT OFFOTIONT Status active Duations indefined Sampling active Sampling 20 mm PRULET Sampling 20 mm PRULET Sampling 20 mm PRULET Sampling 10 active (Indefined ♥)

 | Date Original Diversion Diversion <thdiversion< th=""> <thdivers< td=""><td>DDL DDE SDIMI1 DINOT Stride Bit Input JUL D07 Din11 DIN01 Stride Bit Input Status Active V Duration: undefined V Vining deptitier V Status undefined V Input langue V Name: PR_UFFT Sampling 20 me V Vactor: 1'm en/" /'m en/"</td><td>-UL DOS Din1) DONUT_BROS Bit Input -UL DOV Din11 DIN01_BROS Bit Input -UL DOV Din11 DIN01_BROS Bit Input Shane Active v Duration Viring Active v Sampling Shane PE_LIFT Sampling Correction:
Sampling</td><td>Status: Active W Duration: undefined Constition Linear W Wring: Amplies Sampling: 2.0 ms Vincy fampling: 2.000.0 µ sps 2.000.0 µ sps 2.000.0 µ sps 2.000.0 µ sps 1 "µ sps" 7 µ sps"</td><td>Name PR_LUFT Sampler Undefined V Conection Linear V
Name PR_LUFT Sampler V Sampler V Sampler V Sampler V Sampler V Sampler V Vactor 1" preu" /" preu"</td><td>Normer PR_LUFT Sampler Undefined V Conection Undefined V Conection Contract V Sampler 20 mm V Conection Contract V Sampler 20 mm V Conection Contract V Sampler V Samp</td><td>Name PR_LIFT Sampler Undefined V Correction Linear V
Name PR_LIFT Sampler Undefined V Valor</td><td>And Active V Duration undefined V Conection Linear V
Wring Langtine V Sampling 2.0 m V (section 1°) source (section 1°) source</td><td>And Active Vering Langeline Vering Lang</td><td>nnel
Status Active v Duration undefined v Contection Linear
Wring Sungtitur v Scorpting 2.0 mm v Scorpting 2</td><td>Innel Status Active Vinng Competition Sampling Connection Vinng Competition Sampling Connection Vinng PR_UFT Sampling Conditioned V Vinng Vinng</td><td>Statu Active V Duration andefreed V Conection Linear V Ingentified Sampling Competition Sampling Commercial Undefined V Sampling Linear V Ingentified Ingentified (Sampling Commercial Ingentified Ing</td><td>And Company Duration Output Correction Linear Status Active w Durations undefined Corrections Incut ranges Mining Active v Sampline V Sampline V Sampline V Sampline V Sampline V Variance FEDULIFIT Sampler: Undefined v Y Active: T'm eas'' /'y eps''</td><td>Table Oriential Oriential Operation Op</td><td>Contraction Division Division Division Status Contection Linear Model Vring Sampling 2.0 min Model Incoming and paper Name PR_LUFT Sampling Values 1 model Yalkotor 1 model</td><td>Wand Active W Duration undefined V Conscion Linear V Wang Active V Sampling 20 me V Incur langet x Sampling x Sampling x Sampling x X <td< td=""><td>Virtual V Duration undefined V Conscion Linear V Viring Suppling V Sampling 20 ms V Incut stoppt
Sampling x0000 µ sps x0000 µ sps Mamer (PR_LIFT Sampling Undefined V Viscos "/ Ju sps" "/ Ju sps"</td><td>samel
Status Active V Durations undefined V Connection Linear V
Wing Surgities V Sampling 20 ms V Inort sample
Sampler Undefined V Viscos (1 m cm²) / 20 ms V</td><td>Innel Statuz Active Ving Gargetine Sampling 20 mm Sampling 20 mm Ving Gargetine Sampling 20 mm Ving Gargetine Ving Constant Ving</td><td>Innel Status Active Vinng Competition Sampling Connection Vinng Competition Sampling Connection Vinng PR_UFT Sampling Conditioned V Vinng Vinng</td><td>Net Statu Active V Duration andefreed V Conection Linear V Ingenties Sampling 2.0 ms V Sampling 2.0 ms V Ingenties (source Ingenties) (source Inge</td><td>Table of the first of the first</td><td>Fault Over Dimit Device Dimit Dimit Statut Active V Duration undefined V Viring Sampling 20 min V Inconstance V Name: PR_UIFT Sample: Undefined V Yachoo</td><td>IIIL DINOT BAO? BEFORE Sampling Connection Sampling Connection</td></td<></td></thdivers<></thdiversion<> | DDL DDE SDIMI1 DINOT Stride Bit Input JUL D07 Din11 DIN01 Stride Bit Input Status Active V Duration: undefined V Vining deptitier V Status undefined V Input langue V Name: PR_UFFT Sampling 20 me V Vactor: 1'm en/" /'m en/"

 | -UL DOS Din1) DONUT_BROS Bit Input -UL DOV Din11 DIN01_BROS Bit Input -UL DOV Din11 DIN01_BROS Bit Input Shane Active v Duration Viring Active v Sampling Shane PE_LIFT Sampling Correction:
Sampling

 | Status: Active W Duration: undefined Constition Linear W Wring: Amplies Sampling: 2.0 ms Vincy fampling: 2.000.0 µ sps 2.000.0 µ sps 2.000.0 µ sps 2.000.0 µ sps 1 "µ sps" 7 µ sps"

 | Name PR_LUFT Sampler Undefined V Conection Linear V
Name PR_LUFT Sampler V Sampler V Sampler V Sampler V Sampler V Sampler V Vactor 1" preu" /" preu"

 | Normer PR_LUFT Sampler Undefined V Conection Undefined V Conection Contract V Sampler 20 mm V Conection Contract V Sampler 20 mm V Conection Contract V Sampler V Samp

 | Name PR_LIFT Sampler Undefined V Correction Linear V
Name PR_LIFT Sampler Undefined V Valor

 | And Active V Duration undefined V Conection Linear V
Wring Langtine V Sampling 2.0 m V (section 1°) source

 | And Active Vering Langeline Vering Lang

 | nnel
Status Active v Duration undefined v Contection Linear
Wring Sungtitur v Scorpting 2.0 mm v Scorpting 2

 | Innel Status Active Vinng Competition Sampling Connection Vinng Competition Sampling Connection Vinng PR_UFT Sampling Conditioned V Vinng

 | Statu Active V Duration andefreed V Conection Linear V Ingentified Sampling Competition Sampling Commercial Undefined V Sampling Linear V Ingentified Ingentified (Sampling Commercial Ingentified Ing
 | And Company Duration Output Correction Linear Status Active w Durations undefined Corrections Incut ranges Mining Active v Sampline V Sampline V Sampline V Sampline V Sampline V Variance FEDULIFIT Sampler: Undefined v Y Active: T'm eas'' /'y eps''

 | Table Oriential Oriential Operation Op
 | Contraction Division Division Division Status Contection Linear Model Vring Sampling 2.0 min Model Incoming and paper Name PR_LUFT Sampling Values 1 model Yalkotor 1 model
 | Wand Active W Duration undefined V Conscion Linear V Wang Active V Sampling 20 me V Incur langet x Sampling x Sampling x Sampling x X
 X X X X <td< td=""><td>Virtual V Duration undefined V Conscion Linear V Viring Suppling V Sampling 20 ms V Incut stoppt
Sampling x0000 µ sps x0000 µ sps Mamer (PR_LIFT Sampling Undefined V Viscos "/ Ju sps" "/ Ju sps"</td><td>samel
Status Active V Durations undefined V Connection Linear V
Wing Surgities V Sampling 20 ms V Inort sample
Sampler Undefined V Viscos (1 m cm²) / 20 ms V</td><td>Innel Statuz Active Ving Gargetine Sampling 20 mm Sampling 20 mm Ving Gargetine Sampling 20 mm Ving Gargetine Ving Constant Ving</td><td>Innel Status Active Vinng Competition Sampling Connection Vinng Competition Sampling Connection Vinng PR_UFT Sampling Conditioned V Vinng Vinng</td><td>Net Statu Active V Duration andefreed V Conection Linear V Ingenties Sampling 2.0 ms V Sampling 2.0 ms V Ingenties (source Ingenties) (source Inge</td><td>Table of the first of the first</td><td>Fault Over Dimit Device Dimit Dimit Statut Active V Duration undefined V Viring Sampling 20 min V Inconstance V Name: PR_UIFT Sample: Undefined V Yachoo</td><td>IIIL DINOT BAO? BEFORE Sampling Connection Sampling Connection</td></td<> | Virtual V Duration undefined V Conscion Linear V Viring Suppling V Sampling 20 ms V Incut stoppt
Sampling x0000 µ sps x0000 µ sps Mamer (PR_LIFT Sampling Undefined V Viscos "/ Ju sps" "/ Ju sps"
 | samel
Status Active V Durations undefined V Connection Linear V
Wing Surgities V Sampling 20 ms V Inort sample
Sampler Undefined V Viscos (1 m cm²) / 20 ms V
 | Innel Statuz Active Ving Gargetine Sampling 20 mm Sampling 20 mm Ving Gargetine Sampling 20 mm Ving Gargetine Ving Constant Ving | Innel Status Active Vinng Competition Sampling Connection Vinng Competition Sampling Connection Vinng PR_UFT Sampling Conditioned V Vinng
 | Net Statu Active V Duration andefreed V Conection Linear V Ingenties Sampling 2.0 ms V Sampling 2.0 ms V Ingenties (source Ingenties) (source Inge | Table of the first | Fault Over Dimit Device Dimit Dimit Statut Active V Duration undefined V Viring Sampling 20 min V Inconstance V Name: PR_UIFT Sample: Undefined V Yachoo | IIIL DINOT BAO? BEFORE Sampling Connection |
| Water School (1990)

 | Channel Active V Dutation anderred Correction Lnear V Wring Sampling 2.0 ms V Icourt logur logue Icourt

 | el
Idaus Active: v
vinng Angelier v
same (PR_LUFT Sampler: Undefined v
same (PR_LUFT Sampler: Undefined v
Sampler: Sampler: Undefined v
Sampler: Sampler: Sample

 |

 | Link OP (Binit) Diversion Diversion Diversion samed Samplerg 20 mm Connection: Linear V Wring Samplerg 20 mm Samplerg Samplerg 1'm rear" ^n/m rear" Name: FPLUFT Samplerg 20 mm V Insertion v Name: Drawing Drawing Drawing Drawing non-rear" None: 1'm rear" None: 1'm rear" None: 1'm rear" None: 1'm rear" None:

 | 1.00. Dis [Din1] Dist01 Sinis Bit Input 1.01. Dist01 Bit Input Dist01 Bit Input Bit Input Status Active M Duation undefined Connection: Linear Status Active M Duation undefined Connection: Linear Name PR_LIFT Sample: Undefined Yactor 1"see" Vising Sampling Sample: Undefined Distort angle "angle"

 | -DL DD (Dirth)_BRDS Bit Input Status Active V Verget Amptifie Sampling Name PR_LIFT Samples: Undefined V Value of "// p ept" Values of Trigget 1 Data acquiring (Dired)

 | Statur Active W Duartors undefined Correction Linear W Wring Angelier Sampling 2.0 ms Image/set ima

 | Writing Contraction Linear W Writing Sampling 2.0 mm Exampling

 | Writing
Marker Contraction
Sampling Contention
Sampling Linear W Name PR_LIFT Sampler Linear V Isophiling Isophiling V Name PR_LIFT Sampler Undefined V Isophiling 1* rcs**

 | Wring
and function Work Duration
Sampling Correction
Sampling Linear W Name PR_ULFT Sampler Undefined V Isophiler Value Name PR_ULFT Sampler Undefined V I'm entity Page

 | Annel Active V Durations undefined V Corrections Linear V
Viring Academic Sampling 2.0 mm V Sampling 2.0 mm V Sampling 1.0 mm V Valoritor 11" end" / Valoritor 11" end

 | wind Correction Linear Wing Sampling Dutation: undefined Correction Encoder stage Name PR_LIPT Sampling Description Vision: "reparting" #2000 # pps Name PR_LIPT Sampling Undefined Vision: "in pen" "yes"

 | And Cative

 | nnel
Stotus Active ♥ Durations (undefined ♥ Corrections Linear ♥
Name PR_LUP Samples Undefined ♥ Vactor 1 % rest / ½ reps*
Samples Undefined ♥ Vactor 1 % rest / ½ reps*

 | Nonel Connection Linear Statue vining despities 2.0 ms v Name PR_LIT Sampler Undefined v Value Connections Linear v Value PR_LIT Sampler Value v
 | And Contraction Contraction Status: Active Image: Contraction Manae FR_UPT Sampler: Undefined V Sampler: Vial Contraction Image: Contraction Image: Contraction Image: Contraction Value Image: Contraction Value Image: Contraction Value Value Value Value Value Value Value Value

 | Vitreg Vitre Vitre Vitre <td>Table Over the formation Description Statual Active V Statual Active V Wring Sampling 20 mm Sampling Sampling 20 mm Vining FRLUFT Sampling Vining Sampling 11/1 rept." Vining Duation: Undefined Vining Duation: Undefined Vining Duation: Target: Vining Duation: Duation:</td> <td>Statur Active v Duration undefined v Consection Linear v
Wreng Anstituin v Sampling 20 ms v Sampling 20 ms v Yactor 1" regr / /v eps"
Name (PR_LIFT Sampler Undefined v Jaca Accasitor Undefined v Active 1" on a Accasitor Undefined v Active 1" on a Accasitor Undefined v Viactor 1" regr / /v eps"</td> <td>wavel wavel wavel wavel wavel Duration undefined w Conscion Linear w Viving Sampling 2.0 ms w Incode stoppe #2000.9 apps #2000.9 apps</td> <td>annel Statu: Active Vering Control Sampler Duration Indefined V Control Contro Control Control Control Control Control Control Control</td> <td>nnel Status Active</td> <td>nnel
Stotus Active ♥ Durations (undefined ♥ Corrections Linear ♥
Name PR_LUP Samples Undefined ♥ Vactor 1 % rest / ½ reps*
Samples Undefined ♥ Vactor 1 % rest / ½ reps*</td> <td>Inel Statu Active V Duation andefined Coencilion Linear V Sampling 20ms V Sampling PRULEY Vision 1's each '/p eps'</td> <td>Construction On the form Decision Decision Status Active Image: Construction Connection Status Active Image: Connection Lineor Name FPL_LIFT Samplers Undefined Vision Vision Samplers Undefined Vision Image: Connection Vision FPL_LIFT Samplers Undefined Vision Vision FPL_LIFT Samplers Undefined Vision</td> <td>Paul Original Bit Pool Destroy Different Staturi Active V Duastors undefined V Wring Annie (PR_LUFT Samplere) 2.0 ms v Food in angle e-2000 µ eps Name (PR_LUFT Samplere) Undefined V The cost 1"h eps" /"h eps" Associated (Trigger 1 - - Data ascalation (trigger 4) - Data ascalation (trigger 4)</td> <td>-IIL DINOT BAD? BHO? BHO? BHOR -IIL DINOT BHO? BHOR -IIL DINOT BHO? -IIL DINOT BHO? -IIL DINOT -IIL -IIL</td>
 | Table Over the formation Description Statual Active V Statual Active V Wring Sampling 20 mm Sampling Sampling 20 mm Vining FRLUFT Sampling Vining Sampling 11/1 rept." Vining Duation: Undefined Vining Duation: Undefined Vining Duation: Target: Vining Duation: Duation:
 | Statur Active v Duration undefined v Consection Linear v
Wreng Anstituin v Sampling 20 ms v Sampling 20 ms v Yactor 1" regr / /v eps"
Name (PR_LIFT Sampler Undefined v Jaca Accasitor Undefined v Active 1" on a Accasitor Undefined v Active 1" on a Accasitor Undefined v Viactor 1" regr / /v eps"
 | wavel wavel wavel wavel wavel Duration undefined w Conscion Linear w Viving Sampling 2.0 ms w Incode stoppe #2000.9 apps
 | annel Statu: Active Vering Control Sampler Duration Indefined V Control Contro Control Control Control Control Control Control Control
 | nnel Status Active
 | nnel
Stotus Active ♥ Durations (undefined ♥ Corrections Linear ♥
Name PR_LUP Samples Undefined ♥ Vactor 1 % rest / ½ reps*
Samples Undefined ♥ Vactor 1 % rest / ½ reps* | Inel Statu Active V Duation andefined Coencilion Linear V Sampling 20ms V Sampling PRULEY Vision 1's each '/p eps'
 | Construction On the form Decision Decision Status Active Image: Construction Connection Status Active Image: Connection Lineor Name FPL_LIFT Samplers Undefined Vision Vision Samplers Undefined Vision Image: Connection Vision FPL_LIFT Samplers Undefined Vision Vision FPL_LIFT Samplers Undefined Vision | Paul Original Bit Pool Destroy Different Staturi Active V Duastors undefined V Wring Annie (PR_LUFT Samplere) 2.0 ms v Food in angle e-2000 µ eps Name (PR_LUFT Samplere) Undefined V The cost 1"h eps" /"h eps" Associated (Trigger 1 - - Data ascalation (trigger 4) - Data ascalation (trigger 4) | -IIL DINOT BAD? BHO? BHO? BHOR -IIL DINOT BHO? BHOR -IIL DINOT BHO? -IIL DINOT BHO? -IIL DINOT -IIL |
| Yes: Thendadd 0 'p.e. Poulive daper (w @Yes: Preloger 0 s

 | Description Outsition Indefined V Status Active No Sampling 2.0 ms Indefined Indefined <td>el Tatus Active Duration undefined Conscion Linear Sample 20 ms Sample: Undefined Sample: Undefined Viscotar 1"s equi" /'s ept" Data status Instruction 10 active Viscotar Viscotar</td> <td></td> <td>Link Ord Burniti Diversion Diversion Diversion Stratut Active M Duration: undefined Wring Sampling: 2.0 ms image/image Name: PL_UFT Sampling: undefined Name: PL_UFT Sampling: Undefined Virgit Sampling: Undefined Undefined Virgit Sampling: Undefined Undefined Virgit: Therein's fill (Undefined Undefined Undefined Virgit: Therein's fill (Undefined Undefined Undefined Virgit: Therein's fill (Undefined Undefined Undefined Undefined Undefined Undefined Undefined Virgit: Therein's fill (Undefined Undefined Undefined Virgit: Therein's fill (Undefined Undefined Undefined</td> <td>BDL Did (Dinit) District Strate Bit Neuril Statut District Strate Bit Neuril Statut District Strate Orientifier Strate Wring Angeline District and firmed Name PR_LLFT Sampler: Undefined V Vere: Transition Precision Type Province stope Data scondisch Typered Vere:</td> <td>-DD, DDS (Din1) DS (Din1)<td>Statur Active W Duastors undefined Correction Linear W Viving Supplier 20 ms Correction incorrection incorrection</td><td>Verief Constants: Carbon V Constants: undefined V Constants: Const</td><td>Veret Ving Angelse Value Sampling 20 ms Ving Angelse Value Contention Contentio Contention Contention Contenti</td><td>Writing Applied W Dutations undefined W Corrections Linear W Writing despitien Sampling 2.0 ms V foorting of the part incomplete incompl</td><td>Vining Angelities V Dutations undefined V Corrections Linear V
Vining Angelities V Sampling 2.0 mm V Sampling 1=2000 µ eps
Name (FPL_UPT Sampling Undefined V Visions (11 m ees" // 2/ 2 mp // 2 mp</td><td>Veries Active</td><td>Annue Active</td><td>International Constraints of the second seco</td><td>Instrume Active Image: Contraction Contraction Linear Name FPLUFT Samplere 2.0 ms Image: Contraction Image: Contraction Name FPLUFT Samplere Undefined V Particle 1's reat" /'p' rept" ase of Fright 1 Ty rept" Data acculation intiggered Data acculation intiggered s et Translation Ty rept" Vestor Ty rept"</td><td>Active Outston Undefined Correction Status Active Duration undefined Incorrection Wring Active Sempleric Undefined Incorrection Name PLUFT Samplers Undefined Value or of Trigger 1 put acquiring to perform Put acquiring to perform c Treatment Treatment Treatment</td><td>Correction Correction C</td><td>Table Control of the owner Description Statust Active Image: Control of the owner Description Statust Active Image: Control of the owner Image: Control of the owner Viving: Annee (PL_LPT) Samplere Image: Control of the owner Image: Control of the owner Name: PL_LPT Samplere Image: Control of the owner Image: Control of the owner Image: Control of the owner Name: PL_LPT Samplere Image: Control of the owner Image: Control of the owner Image: Control of the owner Name: Of the owner Image: Control of the owner Image: Control of the owner Image: Control of the owner etc: The owner Image: Control of the owner Image: Control of the owner Image: Control of the owner etc: Image: Control of the owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner</td><td>Status: Active V Dutation: undefined V Wing: Antipitieri V Sampling: 20 ms V Name: PLUFT Sampler: V Sampler: *2000 µ eps Value of Togger 1 V Undefined V Value of "A togger 1 Vec: The individ 0 "µ = Purdver stops Wing 0 #</td><td>samel
Statut Active Durations undefined V
Name PR_LIFT Sampling 20 ms V
Sampling 20 ms V
Sampling</td><td>annel
Statur Active Duators undefined v
None (PR_LUFT Samples Undefined v
has a Croget 1 v
ver. The statut 0 v v Planter days v to the status of the sta</td><td>need Status Active Durations undefined Samples Connections Linear Connections Lin</td><td>International Constraints of the second seco</td><td>Incl Active V Duration: Undefined V Correction: Linear V
Name FPE_LIFT Sample: Undefined V Victor 1's cent[®] /'s cent[®] /'s</td><td>Constant Control of the owner Destinant Wring Langeliner Duration: undefined Contection: Lineor Wring Langeliner Samplerg 2.0 m Isophiler Isophiler Name PLLIEPT Samplerg Undefined Vision Isophiler Vision Isophiler Vision Isophiler Data acquisition toggeed est Terrinant Isophiler Data acquisition toggeed Isophiler</td><td>Paul Oring format Oring interver Distance strated Active Very format Dutations undefined Very format Wang Angeling 20 ms Very format sampling 20 ms very format Name PFLLET Samplers Very format very format very format very format Name PFLLET Samplers Undefined Very format very format very format etc Transition y = Pustment dage Ellipset Data doctation triggeted e</td><td>-IIL DINOT BAD? DINIT BAD? DINIT BAD? -IIL DINOT BAD? -II</td></td>
 | el Tatus Active Duration undefined Conscion Linear Sample 20 ms Sample: Undefined Sample: Undefined Viscotar 1"s equi" /'s ept" Data status Instruction 10 active Viscotar

 |

 | Link Ord Burniti Diversion Diversion Diversion Stratut Active M Duration: undefined Wring Sampling: 2.0 ms image/image Name: PL_UFT Sampling: undefined Name: PL_UFT Sampling: Undefined Virgit Sampling: Undefined Undefined Virgit Sampling: Undefined Undefined Virgit: Therein's fill (Undefined Undefined Undefined Virgit: Therein's fill (Undefined Undefined Undefined Virgit: Therein's fill (Undefined Undefined Undefined Undefined Undefined Undefined Undefined Virgit: Therein's fill (Undefined Undefined Undefined Virgit: Therein's fill (Undefined Undefined Undefined

 | BDL Did (Dinit) District Strate Bit Neuril Statut District Strate Bit Neuril Statut District Strate Orientifier Strate Wring Angeline District and firmed Name PR_LLFT Sampler: Undefined V Vere: Transition Precision Type Province stope Data scondisch Typered Vere:

 | -DD, DDS (Din1) DS (Din1) <td>Statur Active W Duastors undefined Correction Linear W Viving Supplier 20 ms Correction incorrection incorrection</td> <td>Verief Constants: Carbon V Constants: undefined V Constants: Const</td> <td>Veret Ving Angelse Value Sampling 20 ms Ving Angelse Value Contention Contentio Contention Contention Contenti</td> <td>Writing Applied W Dutations undefined W Corrections Linear W Writing despitien Sampling 2.0 ms V foorting of the part incomplete incompl</td> <td>Vining Angelities V Dutations undefined V Corrections Linear V
Vining Angelities V Sampling 2.0 mm V Sampling 1=2000 µ eps
Name (FPL_UPT Sampling Undefined V Visions (11 m ees" // 2/ 2 mp // 2 mp</td> <td>Veries Active</td> <td>Annue Active</td> <td>International Constraints of the second seco</td> <td>Instrume Active Image: Contraction Contraction Linear Name FPLUFT Samplere 2.0 ms Image: Contraction Image: Contraction Name FPLUFT Samplere Undefined V Particle 1's reat" /'p' rept" ase of Fright 1 Ty rept" Data acculation intiggered Data acculation intiggered s et Translation Ty rept" Vestor Ty rept"</td> <td>Active Outston Undefined Correction Status Active Duration undefined Incorrection Wring Active Sempleric Undefined Incorrection Name PLUFT Samplers Undefined Value or of Trigger 1 put acquiring to perform Put acquiring to perform c Treatment Treatment Treatment</td> <td>Correction Correction C</td> <td>Table Control of the owner Description Statust Active Image: Control of the owner Description Statust Active Image: Control of the owner Image: Control of the owner Viving: Annee (PL_LPT) Samplere Image: Control of the owner Image: Control of the owner Name: PL_LPT Samplere Image: Control of the owner Image: Control of the owner Image: Control of the owner Name: PL_LPT Samplere Image: Control of the owner Image: Control of the owner Image: Control of the owner Name: Of the owner Image: Control of the owner Image: Control of the owner Image: Control of the owner etc: The owner Image: Control of the owner Image: Control of the owner Image: Control of the owner etc: Image: Control of the owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner</td> <td>Status: Active V Dutation: undefined V Wing: Antipitieri V Sampling: 20 ms V Name: PLUFT Sampler: V Sampler: *2000 µ eps Value of Togger 1 V Undefined V Value of "A togger 1 Vec: The individ 0 "µ = Purdver stops Wing 0 #</td> <td>samel
Statut Active Durations undefined V
Name PR_LIFT Sampling 20 ms V
Sampling 20 ms V
Sampling</td> <td>annel
Statur Active Duators undefined v
None (PR_LUFT Samples Undefined v
has a Croget 1 v
ver. The statut 0 v v Planter days v to the status of the sta</td> <td>need Status Active Durations undefined Samples Connections Linear Connections Lin</td> <td>International Constraints of the
second seco</td> <td>Incl Active V Duration: Undefined V Correction: Linear V
Name FPE_LIFT Sample: Undefined V Victor 1's cent[®] /'s cent[®] /'s</td> <td>Constant Control of the owner Destinant Wring Langeliner Duration: undefined Contection: Lineor Wring Langeliner Samplerg 2.0 m Isophiler Isophiler Name PLLIEPT Samplerg Undefined Vision Isophiler Vision Isophiler Vision Isophiler Data acquisition toggeed est Terrinant Isophiler Data acquisition toggeed Isophiler</td> <td>Paul Oring format Oring interver Distance strated Active Very format Dutations undefined Very format Wang Angeling 20 ms Very format sampling 20 ms very format Name PFLLET Samplers Very format very format very format very format Name PFLLET Samplers Undefined Very format very format very format etc Transition y = Pustment dage Ellipset Data doctation triggeted e</td> <td>-IIL DINOT BAD? DINIT BAD? DINIT BAD? -IIL DINOT BAD? -II</td> | Statur Active W Duastors undefined Correction Linear W Viving Supplier 20 ms Correction incorrection

 | Verief Constants: Carbon V Constants: undefined V Constants: Const

 | Veret Ving Angelse Value Sampling 20 ms Ving Angelse Value Contention Contentio Contention Contention Contenti

 | Writing Applied W Dutations undefined W Corrections Linear W Writing despitien Sampling 2.0 ms V foorting of the part incomplete incompl

 | Vining Angelities V Dutations undefined V Corrections Linear V
Vining Angelities V Sampling 2.0 mm V Sampling 1=2000 µ eps
Name (FPL_UPT Sampling Undefined V Visions (11 m ees" // 2/ 2 mp // 2 mp

 | Veries Active

 | Annue Active

 | International Constraints of the second seco

 | Instrume Active Image: Contraction Contraction Linear Name FPLUFT Samplere 2.0 ms Image: Contraction Image: Contraction Name FPLUFT Samplere Undefined V Particle 1's reat" /'p' rept" ase of Fright 1 Ty rept" Data acculation intiggered Data acculation intiggered s et Translation Ty rept" Vestor Ty rept"
 | Active Outston Undefined Correction Status Active Duration undefined Incorrection Wring Active Sempleric Undefined Incorrection Name PLUFT Samplers Undefined Value or of Trigger 1 put acquiring to perform Put acquiring to perform c Treatment Treatment Treatment

 | Correction C
 | Table Control of the owner Description Statust Active Image: Control of the owner Description Statust Active Image: Control of the owner Image: Control of the owner Viving: Annee (PL_LPT) Samplere Image: Control of the owner Image: Control of the owner Name: PL_LPT Samplere Image: Control of the owner Image: Control of the owner Image: Control of the owner Name: PL_LPT Samplere Image: Control of the owner Image: Control of the owner Image: Control of the owner Name: Of the owner Image: Control of the owner Image: Control of the owner Image: Control of the owner etc: The owner Image: Control of the owner Image: Control of the owner Image: Control of the owner etc: Image: Control of the owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner Image: Control owner
 | Status: Active V Dutation: undefined V Wing: Antipitieri V Sampling: 20 ms V Name: PLUFT Sampler: V Sampler: *2000 µ eps Value of Togger 1 V Undefined V Value of "A togger 1 Vec: The individ 0 "µ = Purdver stops Wing 0 #

 | samel
Statut Active Durations undefined V
Name PR_LIFT Sampling 20 ms V
Sampling
 | annel
Statur Active Duators undefined v
None (PR_LUFT Samples Undefined v
has a Croget 1 v
ver. The statut 0 v v Planter days v to the status of the sta | need Status Active Durations undefined Samples Connections Linear Connections Lin
 | International Constraints of the second seco | Incl Active V Duration: Undefined V Correction: Linear V
Name FPE_LIFT Sample: Undefined V Victor 1's cent [®] /'s | Constant Control of the owner Destinant Wring Langeliner Duration: undefined Contection: Lineor Wring Langeliner Samplerg 2.0 m Isophiler Isophiler Name PLLIEPT Samplerg Undefined Vision Isophiler Vision Isophiler Vision Isophiler Data acquisition toggeed est Terrinant Isophiler Data acquisition toggeed Isophiler
 | Paul Oring format Oring interver Distance strated Active Very format Dutations undefined Very format Wang Angeling 20 ms Very format sampling 20 ms very format Name PFLLET Samplers Very format very format very format very format Name PFLLET Samplers Undefined Very format very format very format etc Transition y = Pustment dage Ellipset Data doctation triggeted e | -IIL DINOT BAD? DINIT BAD? DINIT BAD? -IIL DINOT BAD? -II |
| Transmit Freeze - Original processory of Transmit Processory

 |

 |

 | HIL DI7 (DIn1) DIN01 BK07 BK Insut

 | Tall the toring of toring of the toring of toring of the toring of the toring of torin

 | -RE DI6 (Din1) DIN01_Bi06 Bit Input
-RE DI7 (Din1) DIN01_Bi07 Bit Input

 | - 00, 005(0in1) DIN0_8055 88 teput
- 00, 005(0in1) DIN0_8066 88 teput
- 00, 007(0in1) DIN0_8077 88 teput

 |

 |

 |

 |

 |

 |

 |

 |

 |

 |
 | THE MULTINE MULTINE MULTINE

 | DE NEL
 |
 |

 | |
 |
 | | THE ATTRACT DEPENDENCE DEPENDENCE DE PERE
 | HIL DIVIDINI DINU BRU | -III DI7 (Din1) DIN01 B#07 B# Inout |
| transfer and the second states and the second states

 | Churnel
Statuzi Active v Duration undefined v Contection Linear v
Wring Langtim v Sampling 2.0 mm v toput raga:

 | el
sabus Active V Duration undefined V Conection Linear V
frog Angelon V Sampling 2.0 mm V Frog Angelon 2000 pps

 | -dut printing inter exercises interview interv

 | Julk OP/ID/INIT Owners/Jonase Distance Statut Zactive V Distation undefined V Statut Antibility Sampling 20 me V Control/in Linear V

 | BDL Dis Dishi1 Dishi2 Sinds Bit how Statut Dishi2 Sinds Bit how Bit how Statut Dishi2 Sinds ordefined Correction: Linear Wring Antime Disation: ordefined Correction: Linear

 | -10L D(5) (Din1) D(1501)_8005 Bit hear -10L D(6) (Din1) D(1501)_8005 Bit hear -10L D(7) (Din1) D(1501)_8005 Bit hear Married w Correction Linear Wring Anglinin Sampling 2.0 ms Correction

 | Status Active W Duration undefined V Contection Linear W Wing Ampling 2 0 ms W Incurrently #20000 µ eps #20000 µ eps

 | Statul Active Duration undefined Contection Linear www.g. Active Sampling 20 ms

 | Statu Active v Duration undefined v Conection Linear v
Wring Antime Sampling 2.0 ms v Conection Linear v

 | Statut Active Duration undefined Conscion Linear withing Active Sampling 20 ms

 | Statut Active V Duration undefined V Connection Linear V
Vieng Langtine V Sampling 20ms V Index angle #2000 µ eps

 | Statut Active V Duration undefined V Consolin Linear V
Vieng Langtine V Sampling 20 ms V Index 1000 apps

 | neel
Status Active V Duration undefined V Contection Linear V
Vieting Langeline V Sampling 2.0 ms V Pour angle ±2000 μ sps

 | ned
Statu: Active V Dutation undefined V Contection Linear V
Wing Langtim V Sampling 2.0 ms V Incut range, s-2000 µ eps

 | Statu Active V Dutation (undefined V Connection Linear V
Vering Langtin V Screeking (2.0 mn V Inc. Linear V

 | nel ordenite view of the second ordenite of the second of
 | annel Active v Duration undefined v Conection Linear v Wing Antitie V Sampling 2.0 ms v Indefined scalar scalar v Conection Linear v

 | stradi
Stotal Active v Duration undefined v Conection Linear v
Wring Annumer v Sampling 20 ms v Conection Linear v
 | Verg Anthe Sampling 2.0 ms v Conection Linear v
 | arrel
Statu: Active V Duration: undefined V Correction: Linear V
Wing Angelier V Sampling 20 ms V Ingut range: ±2000 e pos
 | annel
Statui Active V Duration andefined V Conscion Linear V
Wing Angelier V Sampling 20 ms V Inou Iange, ±2000 e exe
 | nnel
Status Active V Duration undefined V Contection Linear V
Vieng Langten V Sampling 20 ms V hour range, sc000 area
 | ned
Statu: Active V Dutation undefined V Contection Linear V
Wing Langtim V Sampling 2.0 ms V Incut range, s-2000 µ eps
 | Statu Active V Dutation (undefined V Connection Linear V
Vering Langter V Sampling (2.0 mm V Inc. Linear V) | Innel Status Active Virg Langeline V | Preuz vri nomit brivot br | -BIL DIYT DINITI BINT BINT BINT BINT BINT BINT BINT |
|

 | Durnel
Statut Antve v Duration undefined v Conection Linear v
Vrime Landidri v Sander 2.0 ms v toputaget _2000 a pos

 | el
tatuat Active V Duration undefined V Connection Linear V
frequitanges Active V Sweder 2.0 mm V frequitanges 2000 a pos

 | -stub principini pinos p

 | LAL 077 (Don't) Universe Bit types Stratu Active V Stratu Active V Sumain Active Activ

 | BDL Description1 Diston1 priori Bit Input BLL Dir/Tolm1 Diston1 priori Bit Input Status Active V Duration: undefined Correction: Linear Status Active V Duration: undefined Correction: Linear Writering Summer Summer Correction: Linear V

 | -BL Dis Dirititi Distant_Bens Bit Input -BL Dis Dirititi Distant_Bens Bit Input -BL Distant_Bens Bit Input Bit Input -BL Distant_Bens Bit Input Bit Input -BL Distant_Bens Bit Input Bit Input -BL Distantification under Infect Under Infect Statut Active V Duration under Infect Vietna Distantification V Comediant Linear V

 | Statu: Active v Duration undefined v Conection Linear v

 | Statut Active V Duration undefined V Conscion Linear V
Viena Antive Sweder 20 ms V Input Isaget ±0000 a sea

 | Statu Active V Duration undefined V Conscion Linear V
View Antime Sandary 2 and V Induiting 2 and V Induiting

 | Statu Active V Duration undefined V Conscion Linear V Vene Antilia V Sweeting 20 ms V Input larget +2000 a gas

 | Status Active V Duration undefined V Conscion Linear V Vener Jantim V Sweeting (20mm V International Janta)

 | Statut Active V Duration undefined V Consistion Linear V
Viene Janeiter V Sweeters (20 ms V Fred Hage) +2000 a sea

 | nnel
Statu Active V Duration undefined V Contection Linear V
Viener Landeller V Scenative 2.0 mm V Input Inguit _s2000 a ges

 | nel
Statu Active V Duration (undefined V Conscion Linear V
Weiner Landers V Sweeter 2.0 mm V Input traget (±2000 a sec

 | Statu Active V Duration undefined V Connection Unear V
Sweet and the Sweeter 2.0 mm V Incol range 1,2000 a set
 | Statut Active W Duration: underland V Controls:
Water Active V Duration: underland V Controls:
Water Active V Duration: underland V Duration: underland V Duration: underland V Duration:

 | Status Antwo Duration underline v Connection Linear v
Viewer Antwo Status Sanceber 20 ms v Incy Lange p2000 sea

 | stratu Active V Duration undefined V Connection Linear V
Wine Antime V Swedung 20 ms V Indefined Linear V
 | Statut Active V Duration undefined V Contection Linear V
When Active V Duration and Part Part Active V Contection Linear V
 | samel
Statut Active V Duration undefined V Connection Linear V
When Antiper V Samether 20 ms V Incur angle _p2000 a sea
 | annel
Statu Active V Durator undefined V Conscion Linear V
Winn Antibia Sampler 20 ma V Incur angle _p2000 a sta
 | nnel
Statu Active V Duration undefined V Conscion Linear V
Venen Landers V Sceleter 2.0 mm V Input ranget _s2000 a sea
 | nel
Statu Active V Duration (undefined V Conscion Linear V
Weiner Landers V Sweeter 2.0 mm V Input traget (±2000 a sec
 | Statu Active V Duration (undefined V Connection Linear V
Vener Landitor V Sweeting 2.0 mm V Incol range (±2000 a sec | Innel Status Active Vinner Lander Vinner Lan | vrzel vrzeni printi princi pre | -BL DIYD DINDI BNO7 BK Incut
well
Status Active v Duration undefined v Correction Linear v
Vener Landin v Sweder 2.0 mm v hop/ inget is 2000 a set |
|

 | Channel
Status Active v Duration undefined v Costection Linear v
Viteor Status Costection Linear v

 | el
tataliz Active V Duration undefined V Conection Linear V
fropul range

 | -title birribinit birkit ekon
Statu Active v Duration undefined v Correction Linear v
Statu Active Statu Correction Correction Linear v

 | LILL VOR IDNITI DIVIDI BIOT BOT

 | DDL DDE IDINITI DINITI Sandi Bit Input DL DUP IDINITI DINITI Sandi Bit Input DL DUP IDINITI DINITI Sandi Bit Input Status Active V Duration: undefined Correction: Linear Status Active V Duration: undefined Correction: Linear

 | -BL DIS Din1] DIN01_BR05 BE tingut Bit Ingut Bit Ingut Dis Din1 DIN01_BR05 BE tingut Bit Ingut Din01_BR05 BE tingut Bit Ingut Din01_BR05 BE tingut Din01_BR05 D

 | Status Active v Duration undefined v Conscion Linear v

 | Statut Active V Duration undefined V Consolin Linear V Video Consolin 2 0 may 1 front ranget

 | Statu: Active v Duration undefined v Conection Linear v

 | Statut Active v Duration undefined v Contection Linear v

 | Statut Active V Duration undefined V Consolin Linear V Video Section 2 0 ms V Interfined V Consolin and Interfined Interfined

 | Statut Active V Duration undefined V Contection Linear V Votion Section 2.0 ms V Indefined V Contection Linear

 | nnel
Status Active V Duration undefined V Contection Linear V
Vietna Status 2 20 pp. 1 20 pp. 4 pp. 1 20

 | nel Statu Active V Duration undefined V Contection Linear V International Contection Contection V Internation International Contection Contection V Internation International Contection International Contection V Internation V Internatio V Internatio V Internation V Internation V Internatio V Interna

 | Statu Active V Dutation undefined V Conection Unear V Statu Active V Dutation undefined V Conection Unear
 | Statut Active V Duration underland V Conection Linear V
Statut Active V Duration underland V Conection Linear V

 | Status Antive V Dualon under V Consection Linear V
 | Status over service over billion billion over billion billion over billion bil
 | Statut Active V Duration undefined Connection Linear V

 | Statute Active Duration undefined V Connection Linear V
 | Statut Active V Duration undefined V Connection Linear V
 | nnel
Statu Active V Duation undefined V Connection Linear V
Viene Statu 2 20 ans V Incut Langet (2000) and | nel Statu Active V Duration undefined V Contection Linear V International Contection Contection V Internation International Contection Contection V Internation International Contection International Contection V Internation V Internatio V Internatio V Internation V Internation V Internatio V Interna
 | Statu Active V Dutation (undefined V Conestion: Unear V
Statu Active 20 and 20 and 10 | Status Carbon V Conscion Linear V Status Carbon V Conscion Linear V | Vision V
 | - JIL DI7 DINITI DINITI BIO7 BILINOV
vel
Statu Active v Duration undefined v Correction Linear v
Exection 20 ms v Input langt |
| NAME PRICE IN A LINE AND A LINE A

 | Channel Statu: Active V Duration undefined V Contection Linear V

 | el
Italia: Active V Dutation underlined V Contection Linear V
Input House Linear V

 | JUL OFFICIENT DIRATION
Status Active Duration undefined V Controllon Unew M

 | Tall vor summi umv stree
Tall vor summi Divor Bion
Status Active V Dualson undefined V Cotection Linear V

 | 101. Dis Dinini Diskit Sandi Bit Input 101. Dis Dinini Diskit Sandi Bit Input 202. Dis Dinini Diskit Sandi Bit Input Status Active V Duadrice Connection Status Active V Duadrice Input range

 | -IDL DIS Din1] DIN01_BR05 BE tingut -IDL DIS DIN1_BR05 BE tingut -IDL DIS DIN1_BR05 BE tingut -IDL DIV DIV1 BN05 BE tingut -IDL DIV DIV1 BN05 Bit ingut -IDL DIV DIV1 BN05 Bit ingut

 | Statu: Active v Duration undefined v Costection Linear v

 | Statu: Active v Dualion undefined v Cotection Linear v

 | Statu: Active v Duration undefined v Corection Linear v

 | Statut Active v Duration undefined v Cottection Linear v

 | Statu Active v Duration undefined v Costection Linear v

 | stratur Active V Duration undefined V Contection Linear V

 | Innel Status Active V Duration undefined V Connection Linear V

 | Innel
Status Active V Dutation undefined V Conection Linear V

 | Statu Active V Dutation undefined V Conection Unear
 | Statut Active V Duration underfreed V Collection Linear V

 | Statu Active V Dualion undefined V Conection Linear V

 | Statu Active V Duration underned V Consection Linear V
 | Statut Active V Dualion undefined V Conection Linear V
 | Strail Active V Duration undefined V Contection Linear V
 | annel
Statui Active V Duration undefined V Connection Linear V
 | Statu Antive V Duation undefined V Consistion Unear V
 | Innel
Status Active V Dutation undefined V Conection Linear V
 | Statu Active V Dutation undefined V Conection Unear | Status Active V Duators Underfined V Conscion. Linear V | Fault even former energy Bitmede
Statue Active W Duration undefined W Consection Linear W
hood range w
 | International active Duality lands active Duality lands active Duality lands active Duality lands active Tool Input Tool Input Tool Inp |
|

 | Channel
Statuz Active V Duration undefined V Contection Linear V

 | el tatul Active V Dualsion underlined V Conection Linear V

 | -IIIL DIFUTIONI DIFUTIONO
Status Actue Duation undefined Conection Linear M

 | All of Manual Division and Antice Division Bit mode
Status Active V Duration undefined V Contection Linear V

 | 101. Dis Disiti Distant Bandi Bit Input 101. Dist Distant Distant Bit Input 101. Distant Distant Bit Input Status Active V Dustion Status Active V Dustion

 | -IQL DIS Din1] DIN01_BR05 BE tingut -IQL DIS Din1] DIN01_BR05 BE tingut -IQL DIS Din1] DIN01_BR05 BE tingut -IQL DIS DIN1 BR07 Bit Ingut Statu Active Public Diversion Dustion undefined undefined undefined undefined undefined

 | Statu: Active v Duration undefined v Costection Linear v

 | Statu: Active v Duation undefined v Cotection Linear v

 | Statu: Active v Dusation undefined v Costection Linear v

 | Status Active V Duration undefined V Correction Linear V

 | Statu: Active v Duation undefined v Contection Linear v

 | Intel Active V Dualion undefined V Contection Linear V

 | Nel Statu Active V Duastion undefined V Constation Linear V

 | Innel Status Active V Dutation undefined V Conection: Linear V

 | Status Active V Duation undefined V Conection Incert mod

 | Status Active V Duration Indefined Collection Interview V
 | Statu: Active: V Duration undefined V Connection Linear V

 | Statu Antwe
 | Statu: Active v Duation undefined v Connection Linear v

 | strail
Statu: Active v Duration undefined v Connection Linear v
 | annel
Statu: Active: V Duration undefined V Contection Linear V
 | Status Active V Dutation undefined V Conection Linear V | Innel Status Active V Dutation undefined V Conection: Linear V
 | Status Active Duation undefined Connection Unear Incol Incol | Status Active V Duation Underfined V Conscion Unew V
 | Full our format on our prover performed or Connections Linear or formation and the formation of the formatio | -IIIL DIRVIT BIO7 BEFORM VI Status Actue Duation Duation undefined V Contection Linear V V V V V V V V V V V V V |
| Name PR LIFT Sampler Undefined or Vischer 1 "Lene" / Lene"

 | Channel
Status Active V Duration undefined V Conrection Linear V

 | el
tatut Active V Dutation undefined V Conection Linear V

 | -IIIL DIRVIT BION BRON BRON BRON BRON BRON BRONZ
Veel
Statut Active V Duration undefined V Collection Linear V

 | Latter vor put mini universite and a second

 | DB (Dini 1 DIN01 Bade Be Input DIN01 Bade DIN01 Bade Correction Dino1 Bade Correction

 | -BDL DOS D(m1) DOSIND_REMOS Bit Input -BDL DOS D(m1) DOSIND_REMOS Bit Input -BDL DOP TOIL DONOT REMOS Bit Input -HALL DOP TOIL DONOT REMOS Bit Input Statut Active V Duadion undefined Correction: Linear

 | Status Active V Duration undefined V Contection Linear V

 | annel
Status Active v Durations undefined v Cosections Linear v

 | Statu: Active v Duration undefined v Cosection. Linear v

 | Statu: Active V Duration undefined V Contection Linear

 | stratu: Active v Duration: undefined v Correction: Linear v

 | statul Active V Duration undefined V Correction Linear

 | nnel
Status Active V Duration undefined V Correction Linear V

 | nnel
Statux Active V Duration undefined V Costection Linear V

 | Status Active V Duration undefined V Correction Linear V

 | Status Active V Duration undefined V Correction Linear
 | annel Statut Active V Duration undefined V Conection Linear V

 | Prince Pr
 | annel Statu: Active V Duration undefined V Conection Linear V
 | Statur: Active V Duration undefined V Correction Linear V
 | annel
Status: Active V Dutation undefined V Contection Linear V
 | nnel
Status Active V Disation undefined V Cosection Linear V
 | nnel
Statux Active V Duration undefined V Costection Linear V
 | vel
Statux Active V Duration undefined V Correction Linear V | nnel control over periode
nnel solate Active v Dutation (undefined v Correction Linear v | mal control brive brive Bitmod
prod
Status Active V Duasion undefined V Contection Linear V
 | -JIL DIFUT DIFUT BIO7 BEFrout
vnel
Statut Active V Dutation undefred V Collection Linear V |
| Name PR LIFT Sameler Undefined Visioning 1"a end" //a end"

 | Dramel
Status Active V Duration undefined V Contection Linear V

 | el
tabus Active V Dutation undefined V Contection Linear

 | -IIIL DIFUTI DIFUTI BIOF BIOF BERNON
Well
Statut Active V Dutation (undefined V Corection Linear V

 | All OFFICIENT UNIT DINOT DADO
UNITED Status Active V Duation undefined V Conection Linear V

 | DIS (Din1) DIN01_Bi06 Bit Input DIN01_Bi06 Bit Input DIN01_Bi07 Bit Input Straw Active Duation undefined Conection Linear

 | -IDL DIS DIGN1 DISING BRUSS Bit Input -IDL DISING BRUSS Bit Input -IDL DISING BRUSS Bit Input -IDL DISING BRUSS Bit Input -IDL DISING BRUSS Bit Input Subur Active v Duration undefined vv Conection Linear vv

 | Status Active V Duration undefined V Contection Linear V

 | snel
Statut Active v Duration undefined v Cotection Linear v

 | Status Active V Duration undefined V Conection Linear V

 | Status Active V Duration undefined V Contection Linear V

 | statu: Active V Duration undefined V Contection Linear V

 | annel
Status: Active v Dustation undefined v Contection Linear v

 | nnel
Status Active V Duration undefined V Costection Linear V

 | nel
Statur Active V Duation undefined V Correction Linear V

 | vel
Statu Active V Dutation undefined V Conection Linear V

 | nel Statu Active V Duration Undefined V Correction Linear
 | annel straut Active V Duration undefined V Contection Linear V

 | stradi
Statu: Zative V Duration undefined V Controlion Linear V
 | stratel
Statut Active V Duration undefined V Contection Linear

 | annel
Statui Active V Duration undefined V Correction Linear V
 | annel
Status Active V Duration undefined V Controlion Linear V
 | nnel
Status Active V Dutation undefined V Correction Linear V | nel
Statur Active V Duation undefined V Correction Linear V
 | vel
Statu: Active V Dutation undefined V Conection Linear | nnel
Status Active V Dutation undefined V Conection Linear
 | Infault control brow Bitmode
wred
Statul Active V Duration undefined V Contection Linear V | -JIL DI7/Dim1 DIN01 BN07 BK Input
well
Statut Active V Dutation (undefined V Corection Linear V |
| ware Part International Commercial Commercial Company Commercial Comme
Commercial Commercial Comme

 |

 |

 | -III. Dirrolmi Dirot Bior Bior Birnov
Wel

 | TAL VOR DANNI UNIV. DIVOT BALT
DIVOT BALTAN OF DIVOT BALTAN
MARKAN DANA DANA DANA DANA DANA DANA DANA D

 | BIL Distribution Distribution Distribution ALL Distribution Distribution Distribution

 | -100 D(5) (bin1) D(5) (bin3) Bit Input -100 D(5) (bin1) D(14) (Bin5) Bit Input

 | State Anton State Design and Anton State States

 | And Annual Provide Landson and Annual Provide Landson and

 | State And Destate Indefend and Constate Lines and

 | The Area II Destroyed II Constant I are

 | And Annual State Institute and Constant Institute and

 | week

 | mel

 | ned

 | Million and Million Andre Sector Million Andre Million

 | All and a second a
 | And an internet with the second
 | Table Virtismits Virtus With Optimize Optimize Constant France

 | wind
 | and
Description and Analysis and Analysis (Second Second Seco
 | arrol
 | mel
 | ned | New Autor and Autor Instant and Autor Income and
 | rana en | Haus our journit on our low of the low of th | -BL DI7Din11 DIN018607 Bit Incut
Wel |
| Wing Amplier V Sampling 2.0 ms V (amplier) 2.2000 µ dps
Name PR LIFT Sampler Undefined V Varier 1"n em" / n em"

 | hand

 |

 | vel Divoring Biov Bickook

 | LAL VOR JUNIT UNIVER DOOD BARRAN
ALL VOR JUNIT DINOT BARR

 | BDL Distributini Distribution Bit Input ADL Distributini Distributini Distributini Named Distributini Distributini Distributini

 | -10L D(5) (Din1) D(1801) BK805 Bit Input -10L D(5) (Din1) D(1801) BK05 Bit Input -10L D(5) (Din1) D(1801) BK05 Bit Input -10L D(5) (Din1) D(1801) Bit Input

 |

 |

 |

 |

 |

 |

 |

 |

 |

 |
 |

 |
 |

 |
 | and
 | |
 | | CRADE VET VETVEL VETV
 | Hall or joint one box Brinds | -BIL DI/T DI/NII DI/NII BI/07 Bit Incut
vel |
| Wing Amplies V Sampling 2.0 ms V (amplifier) ±2000 μ dps
Name PR LIFT Sampler Undefined V Varier 1 "n em" / "n em"

 | hannel

 |

 | -auc on point on on extraor

 | LAL VOR JUNIT UNIVERSION BAREAN
ALL VOR JUNIT DINOT BARE BAREAN
Annel

 | 100, Dols (Dini 1) DINOT Brands Bit Ingui
101, DINOT DINI 1 DINOT Brands Bit Ingui
Narmet

 | -10L D01011 D01013 BR05 Bit Input -10L D051011 D01013 BR05 Bit Input -10L D01011 D01013 BR05 Bit Input -10L D01011 D01013 BR05 Bit Input -10L D01011 D01013 BR05 Bit Input

 |

 |

 | anne de la companya d

 |

 |

 |

 | nel

 |

 |

 |
 |

 |
 |

 |
 |
 | |
 | | radia verinomiti verevi verevi DEEDOA
 | ANNY ANTININ'I ANNA BENAV | -BIL DI/TDI/N11 DIN/01 BK07 BK Incut
vel |
| Wing Amplier V Sampling 2.0 ms V (amplier) ±2000 µ eps
Name PR UFT Sampler Undefined V (Ambin 1"m em") /"u em"

 | Darnel

 |

 | awy
-TITE privipinut purch private way bernador

 | LALL VOR JUNIT DINOT BIOT BIOT BILTON

 | 100. Des Dinini DiN01 Senis Bit Input 101. DIN01 Dinion DiN01 Senis Bit Input 101. DIN01 Dinion Dinion Dit Input

 | -10L D(5) (Din1) D(1810) BK1 Bk1 D(1810) D(1810) Bk1 D(1810) D(1810) Bk1 D(1810) D(1810) D(1810) Bk1 D(1810)

 |

 | week and a second se

 | annet

 |

 | ind and a second s

 | read and a second se

 | not a second sec

 | web

 |
 | Alle viringente viring viringen veringen.

 | Page of infinite of the owner
 | ANNY ALL MALE ALLAR ANNA DE REAL

 | and
 |
 | and the second
 |
 | web |
 | rana veri jonni vere prince | navel international internationa | -201 DAY DINOT BHOY BY INC. |
| Wing Amplifier V Sampling 2.0 ms rpot range:
(amplifier) 2.000 µ eps Name PR LFT Sampling Lindefined Visions' Zin ans''

 | band

 |

 | -201 ost louvi ovot exot. Bit loon

 | LALL VOR JUNIT CHARLENCE CARTERN
VIII DINIT BUDY BRUT BRUT BRUT BRUT BRUT

 | 1.00. Disk Dini1 Disk Dini1 Disk Dini1 Bit Input 1.00. Disk Dini1 Disk Dini1 Disk Dini1 Disk Dini1 Disk Dini1

 | -100, 005 (Din1) 00101-8005 88 tread
-200, 006 (Din1) 00101-8005 88 8 tread
-200, 007 (Din1) 00101-8007 88 tread

 |

 |

 | WWG

 |

 |

 | row in the second se

 | real .

 |

 |

 | Aus erriterinte erret erret erret erret
 |

 | PAUL OFFICIENTE OFFICE ONLY OFFICE
 |

 |
 | and
 | read . |
 | | TAUL VIETRITE VIEW WWW DEFEN
 | Hall on roman oner Bringer | - III. DY7 (Din1) DIN01 BK07 BK Indvk |
| Wing Amplies v Sampling 2.0 m v Input single
Sampling 2.00m a ps
Value 1 To and"

 |

 |

 | -DUL DI//IDIn11 DINUT BAU/ Brindur

 | TABL DIP IDINTS DINUT_SKIP DINUT_SKIP DINUT_SKIP DINUT_SKIP

 | -00. DIS (Die11 DINOT_BROS BR Input
-00. DV/ TOIn11 DINOT BROS BR Input
-00. DV/ TOIn11 DINOT BROS BR Input

 | 1-00, 005 (Din1) 001807, 8805 881 tegui
1-00, 005 (Din1) 001807, 8805 881 tegui
1-00, 007 (Din1) 001807, 8805 881 tegui

 |

 |

 |

 |

 |

 |

 |

 |

 |

 | Alle en levrin entre ent
 | Calle Articlatic Dirict Day Distance

 | FAIL OF DUTIES OF ONE OF OF
 |
 |

 | |
 |
 | | Table Virtishine Virtuel DEITSU
 | HILL DIFIDINIE DIRUF BEINDUR | -DL DI7 [Din1] DIN01 B#07 Bit Input |
| Solar (and and and and and and and and and and

 |

 |

 | HILL DIVIDINT DINOT BROY BRINDUR

 | L-III DI7 (Din1) DIN01 BR07 BR Input

 | -100 DISIDINTI DINIOTBIOS BRIteau
-200 DIVIDINTI DINIOTBIOS BRIteau

 | 1-00, 005 (Din1) 00807,8405 88 Input
-00, 005 (Din1) 00807,8405 88 Input
-00, 07 (Din1) 00807,8405 88 Input
-00, 07 (Din1) 01807 98 Input

 | and a second secon

 |

 |

 |

 |

 |

 |

 |

 |

 | ARE WEIGHTITE PETER AND PETER
 | Falls of former of the own

 | TABLE STEELINTEE STREET STREET
 |
 |

 | |
 |
 | | THE ANTIANTE ANALY DETERM
 | HAR OFFICIAL DIRAL DIRAL BETADA | -JUL DI7 IDIn11 DIN01 BK07 BK Input |
| Vining Amplier V Someting 20mm V Long Amplier V Someting 20mm V Long Amplier V Someting 20mm V Long Amplier V Long

 |

 |

 | -30L DR/DINT DR07 BKINDU

 | Land Dir Dirini Divot BAD? Bit Input

 | 100 DIS IDIn11 DIN01_BIOS Bit Input
-100 DIV17_DIn11 DIN01_BIOS Bit Input

 | 1-00, 005 (Din1) D01801,8805 Bit Input
-00, 005 (Din1) D01801,8805 Bit Input
-00, 005 (Din1) D01801,8805 Bit Input
-00, 007 (Din1) D01801 9807 Bit Input

 | and a local second s

 |

 |

 |

 |

 |

 |

 |

 |

 | All with sector with the sector secto
 | TABLE OFFICIENTS DIRECTIONS DATION

 | FAILS ATTRACTOR AND DETERM
 |
 |
 |
 |
 |
 | | TALL OF DEVICE OF THE OF THE OF | Hau Diritolinit Dirivi Dirivi Dirivi Dirivi Diritor
 | -JUL DI7[Din1] DIN01 BK07 Bkinout |
| Wing Amplies V Sampling 2.0 ms V Input langer (and the sense of the se

 | hand

 |

 | -101 OLUDINI DINOI BADA. BILINDA

 | TALL OF DINITI DINOT BK07 BR Incut

 | - ADL DIS (Dini) DINO) Selas Belreur
- ADL DIS (Dini) DINO) Belo Belreur
- ADL DIS (Dini) DINO) Belo Belreur

 | 1-00, 005 (Din1) D01801_8805 Bit Input
-00, D05 (Din1) D01801_8805 Bit Input
-00, D07 (Din1) D01801_8805 Bit Input
-00, D07 (Din1) D01801 9807 Bit Input

 |

 |

 |

 |

 |

 |

 | mad and a second s

 |

 |

 | Alle with periods and an an and an
 | Fall of former over over

 | PAUL OFFICIENT OVER DEFICIENT
 |
 |
 |
 |
 |
 | | rang Antifoldut Anton Anton Dringer | HTT OLUDIUI OLUOI DUOL DILIUU
 | -III DI7 [Din1] DIN01 BK07 Bk Input |
| Wing Ampline Sampling 2.0 ms mput range: ± 2000 µ eps Name PR LIFT Sampling Undefined v Values 1"a end"

 | Darnel

 |

 | awy
-TITE privipinut privot privot

 | LALL VOR JUNIT DINOT BIOT BIOT BIOT BITCH

 | 101. D01011 D11011 D01011 B01001 101. D0171 D0101 B0107 B11 Input

 | -10L D(5) (Din1) D(1810) BK1 Bk1 D(1810) D(1810) Bk1 D(1810) D(1810) Bk1 D(1810) D(1810) D(1810) Bk1 D(1810)

 |

 | week and the second

 | annet

 |

 | ind and a second s

 | read and a second se

 | nnel

 | web

 |

 | Alle eriterite eriter eriter
 | Page of infinite of the owner

 | ANNY ALL MALE ALLAR ANNA DI URW
 | and
 |
 | and
 |
 | web
 | | rale on long | navel international internationa | -201 DAY DINOT BHOY BY INC.
 |
| Name PR LIFT Sampler Undefined V Victor 1" env"

 | Namel

 | d
Tana Antina and Devoter Includent and Connector Linear and

 | -IIIE Diricolani Diricol Bator Billion
Well State Caston and Directory Instantiana II Competition (Laware III)

 | LAL VOR BURNI UNIV. DINO BANGA
ALL VOR DINO DINO BANGA
wreat

 | 190 Distributi Distri Bradis Bellevat Distri Bradis Distri Distri Distri Bradis Distri Distri Bradis Distri Bradis

 | -100_ D(5) (bin1) D(1010)_8605 Bit Input -100_ D(1010)_8605 Bit Input -100_ D(1010)_8607 Bit Input Status Antini Dividies undefined Antini

 | States and Distance workford and Constitute Lines

 | State Aster at Dester weblined as Constitute Laws

 | States Astim at Decision undefined at Connection Linear at

 | States Assiss as Decision prototical as Constrained as

 | Status Anton a Devide undefined and Constraint Linear

 | State Antine and Destate Includent and Constraint Lange and

 | nnel
Strau Anton III Durator Indefant II Danator Itaan II

 | nnel
Strang Tanina and Ducation Including and Constraint Finance and

 | nel
Setter Annual and Decetor Includered and Conceptor These and

 | and an annual annua
 | Table viri (virini) virivi vivi viri virinov
viriel
Dave Anton vi

 | Trade or normal protocol protocol protocol and the Constraint Linear and
 | Strate Antine and Provide Indefined and Consisting Taxon
 | strond
Strate Antine at Distanting undefined at Constrainty Linear at
 | and
Status factor and Director indefined of Contention Lange of
 | The Anton of Duration Indefined of Contention Laws
 | nnel
Strang Tanina and Ducation Including and Constraint Finance and
 | nel
Status Anima and Departure Industrial Al Constraint Linear and | rana errorman
1990
Tahan Annas de Danator Indefend de Companio Faces de
 | r-sus or joint one of order where the first of the first | -JIL DI/T/DI/11 DIN01 BK07 BK Incut
Well
Status Leasure well Develop undefined and Develop Lineary and |
| Name PR LIFT Sampler Undefined V Victor 1" eee" // eee"

 | Annual States and Destate and Destates (Sec. 19)

 |

 | -III, Dirrolani Dilan Baor Bernou
Wel -

 | LAL VOR BURNI UNIV. SNOW BANKA
LAL VOR 10 MIN BURY
BANKA STATES

 | BIL Distribution Distribution Bit least ADL Distribution Distribution Distribution

 | -100_ D(5) (bin1) D(1601)_6805 Bit Input

 | State Anton and Design Instituted and Design I have been

 | State Loss and Destate Losses Losses Losses

 | State a sure of Provide Institute and Provide Information (1997)

 | The Area III Destroyed III Country I and

 | And Annual Control and Annual Control of Con

 | week

 | mel

 |

 | with an and the second se
 | and an annual

 | Production (prime) Control of Con
 | Table Virtismits Virtus Virtus Official Official Constants France

 | Want
 | arrel
Data Internet and Description and Description (Second
 | arnel
 | med
 | | Nel Anno 12 Anno 12 Anno 12
 | rana verinomiti vitevi ettevi DEREAN
Real
Data and Derecki ettevite internet | Haus our journit on our long of the Brings | - JIL DI7 DINI DINI BIO7 BE Incut
Wel
 |
| Wing Amplier V Sampling 2.0 ms V (amplifier) ±2000 µ eps
Name PR UFT Sampler Undefined V (Variant 1"h em/")

 | hand

 |

 | vel Divoring Biov Bickook

 | LAL VOR JUNIT UNIVER DOOD BARRAN
ALL VOR JUNIT DINOT BARR

 | BDL Distributini Distribution Bit Input ADL Distributini Distributini Distributini Named Distributini Distributini Distributini

 | -10L D(5) (Din1) D(1801) BK805 Bit Input -10L D(5) (Din1) D(1801) BK05 Bit Input -10L D(5) (Din1) D(1801) BK05 Bit Input -10L D(5) (Din1) D(1801) Bit Input

 |

 |

 |

 |

 |

 |

 |

 |

 |

 |
 |

 |
 |

 |
 | and
 | |
 | | CRADE VET KONNEL VETVER KONNEL DE ENDER
 | Hall or joint one box Brinds | -BIL DI/T DI/NII DI/NII BI/07 Bit Incut
vel |
| Wing Amplier V Sampling 2.0 ms V (amplifier) ±2000 µ eps
Name PR UFT Sampler Undefined V (Variant 1"h em/")

 | hannel

 |

 | -auc on point on on extraor

 | LAL VOR JUNIT DINOT BIOT BRITEAL
servel

 | 100, Dols (Dini 1) DINOT Brands Bit Input
100, DINOT DINI 1 DINOT Brands Bit Input
served

 | -10L D01011 D01013 BR05 Bit Input -10L D051011 D01013 BR05 Bit Input -10L D01011 D01013 BR05 Bit Input -10L D01011 D01013 BR05 Bit Input -10L D01011 D01013 BR05 Bit Input

 |

 |

 | anne de la companya d

 |

 |

 |

 | nel

 |

 |

 |
 |

 |
 |

 |
 |
 | |
 | | radia verinomiti verevi verevi DEEDOA
 | ANNY ANTININ'I ANNA BENAV | -BIL DI/TDI/N11 DIN/01 BK07 BK Incut
vel |
| Name PR LIFT Sampler Undefined V Victor 1" eee" // eee"

 | hand

 |

 | -III. Dirrolini Dilugi Bilor Bilor Bilor
Wel

 | TAL VOR BURNI UNIV. DIVO BURNI

 | BIL Distribution Distribution Bit Input ADL Distribution Distribution Distribution Distribution ADL Distribution Distribution Distribution Distribution Distribution

 | -100 D(5) (bin1) D(1801) B405 Bit Input -100 D(5) (bin1) D(1801) B405 Bit Input -100 D(5) (bin1) D(1801) B405 Bit Input -100 D(5) (bin1) D(1801) B407 Bit Input

 | State Anton and Destate Instituted and Destate From and

 | And Annual and A

 | State a sure of Provide Institute and Provide Internet

 | The Area II Destroyed II Country I are

 | And Annual Control and Constant Control of

 | week

 | mel

 | ned

 | Million and a second seco
 | and an annual

 | Table Vir (Minite United With United Without United
 | Trade verification of the state
 | wind

 | and
Description and Analysis and Analysis (Second Second Second Second Second Second Second Second Second Second Se
 | arnel
 | med | ned
 | Nel Anno 19 Annota Indexed 19 Annotae Inc. 19 | rana virinimit virini virini Distanti
Intel
Dana and Distanti
Virini Virini Virini Virini | Haus our journit on our long of the Brings | - JIL DI7 DINI DINI BIO7 BE Incut
Wel |
| ware Part International Commercial Commercial Company Commercial Comme
Commercial Commercial Comme

 | Namel

 | d
Tana Antina and Devoter Includent and Connector Linear and

 | -IIIE Diricolani Diricol Bator Billion
Well State Caston and Directory Instantiana II Competition (Laware III)

 | LAL VOR BURNI UNIV. DINO BANGA
ALL VOR DINO DINO BANGA
wreat

 | 190 Distributi Distri Bradis Bellevat Distri Bradis Distri Distri Distri Bradis Distri Distri Bradis Distri Bradis

 | -100_ D(5) (bin1) D(1010)_6805 Bit Input -100_ D(1010)_6805 Bit Input -100_ D(1010)_6807 Bit Input Status Loss Bit Input

 | States and Distance workford and Constitute Lines

 | State Aster at Dester weblined as Constitute Laws

 | States Astim at Decision undefined at Connection Linear at

 | States Assiss as Decision prototical as Constrainty Linear as

 | Status Anton a Device preferred at Constitut Linear

 | State Antine and Destate Includent and Constraint Lange and

 | nnel
Strau Anton III Durator Indefant II Danator Itaan II

 | nnel
Strang Tanina and Ducation Including and Constraint Figure and

 | nel
Setter Annual and Decetor Installand and Conserver These and
 | and an annual annua

 | Table viri (virini) virivi vivi viri virinov
viriel
Dave Anton vi
 | Trade or normal
protocol protocol protocol and the Constraint Linear and
 | Strate Antine and Provide Indefined and Consisting Taxon
 | strict
Data Antin at Diractor undefined at Constring Linear at
 | and
Status factor and Director indefined of Contention Lange of
 | The Anton of Duration Indefined and Duration Linear and
 | nnel
Strang Tanina and Ducation Including and Constraint Figure and | nel
Status Anima and Departure Industrial Al Constraint Linear and
 | rana errorenti onter over DETEM
rent
Tata Anton D Duston politikant de Polisika Finan | r-sus or joint one of order where the final set for the first set
 | -JIL DI/T/DI/11 DIN01 BK07 BK Incut
Well
Status Leasure well Develop undefined and Develop Lawrence we |
| Wing Amplier V Sampling 2.0 ms V (amplifier) ±2000 µ eps
Name PR UFT Sampler Undefined V (Variant 1"h em/")

 | hannel

 |

 | ver on control on one and the second of the

 | LALL VOR JUNNI LINNO BIOT BATEAU
Sand

 | 100, Des (Dinini) DINIOT Series Bellegue
100, DINIOT DINIOT DINIOT BRIOT BEllegue
series

 | -10L DOI 1011 DOIX01_8605 Bit Input -10L DD 5016 DDIX01_8065 Bit Input -10L DDIX01_8065 Bit Input -10L DDIX01_8065 Bit Input

 |

 | www.

 | AVVN

 |

 | week and the second sec

 | road

 | not and a second s

 |

 |
 | All Control Co

 |
 | ANNY ALL DALLY ANAL DELEVA

 |
 |
 |
 | med.
 | |
 | rade veri lorret vere prince | vend unit unit universitenv BEInour | -AUL DAY (DIN1) DIN01 BHOY BK Ingul
Vel
 |
| Wring Amplies V Sampling 20 ms Induit sample 2000 µ sps Monor (PR LIFT) Sampler (Lindefront) Values 1/2 mont // mont // mont

 |

 |

 | HILL DIVIDINI DIVOT BKOY BKINGK

 | -III DI71Din11 DIN01 BK07 BR1nput

 | -100 DISIDINTI DINOTIBIOS BRIngut
-100 DISIDINTI DINOTIBIOS BRIngut
-100 DINOTIBIOTI BIOTI BRIngut

 | 1-00, 005 (Din1) D01807, 8805 Bit Input
-00, D016 (Din1) D01807, 8806 Bit Input
-00, D017 (Din1) D01807 Bit Input

 | and the second se

 |

 |

 |

 |

 |

 |

 |

 |

 | ARE ATTRACTOR POTENTIAL AND ADDRESS
 | Fall of Identify over Determined

 | TABLE STEEDSTITE STEED OVER DETERMINE
 | Come and the second sec
 |
 |
 |
 |
 | | TABLE ANT DATES ANTAL ANTAL ANTAL DESTRUCTION
 | HILL UNITOINI UNIVI BRING BRING | -ALL DI7/Din11 DIN01 8407 B4 Input |
| JBL Disk Diniti DiNUT_Brois Bet Input JBL DIS Diniti DINUT_Brois Bet Input JBL DIS Diniti DINUT_Brois Bet Input JBL DIS Diniti DINUT_Brois Bet Input Januari Dinition Duration: undefined ✓ Statut Active ✓ Duration: undefined ✓ Viving: Anstein ✓ Sampling: 20 ms ✓ Viving: Anstein ✓ Sampling: 20 ms ✓ Value: Fill Life ✓ Sampling: 20 ms ✓

 | -AL DIS[Din1] DIN0_BA05 B4 Input

 | UL OS(DINI) DINOT_BROS BRInger

 | The president president president

 | FILL METHOD MENT

 | DIA IDIA 11 DINOT DADA DA DATA

 | TR DMIDI-11 DINOT BOOM Bellow d

 |

 | -10, 016 (0111) 0140, 8405 84 (1qui
-10, 016 (0111) 01401, 8405 84 (1qui
-10, 016 (0111) 01401, 8405 84 (1qui

 | 10. 05 (011) 0110 0100 0405 04194 10. 05 (011) 01101 0405 04194 10. 05 (011) 01101 0405 04194 10. 05 (011) 01101 0405 0419

 | - 10, 05(0)n1 0N02_68/5 Bitnput
- 10, 05(0)n1 0N03_68/5 Bitnput
- 10, 05(0)n1 0N03_68/6 Bitnput

 | - 10, 016 (0111) 01101 8005 81 (1904
- 10, 016 (0111) 01101 8005 81 (1904
- 10, 016 (0111) 01101 8005 81 (1904

 | -10, 015 (Din1) DIN0_8005 Bit Input

 | -010 0/610/min DNND_8405 Bit Input
-010 0/610/min DNND_8405 Bit Input

 | - 10 0(0(1) 0(1) 0(0 805 82 logut

 | ALL DIS[DIN] DIN07_BR05 Bit Input

 | Jul Dis [Din1] DiN0_BN05 Bit Input
 | JUL 0/5 (Din1) DIN01_8405 Bit input

 | -JLL Dis[Din1] DIN0_8805 Bit Input
 | Jul 05(0in1) DN01_805 S Bitnet

 | IL DIS DINIT DINO_BRUS Bringer
 | -JUL 0/5(0/m1) 0/101_8405 Bit input
-JUL 0/5(0/m1) 0/101_8405 Bit input
 | -JUL 015 [01n1] DIN01_8405 B4 Input
-JUL 015 [01n1] DIN01_8405 B4 Input
-JUL 015 [01n1] DIN01_8405 B4 Input | - 10 0(0(1) 0(1) 0(0 805 82 logut
 | ALL DIS[DIN] DIN07_BR05 Bit Input | 0L 05[Din1] DIN0_8805 Bit input
 | - DE DES Desi Divisi Batos Batread | DR DR DIA1 DIND 84% Stored |
| LBL DO HQ Din11 DNNT, BAD4 Bit Input LDL DO BG Din1 DNNT, BAD4 Bit Input LDL DO BG Din1 DNNT, BAD4 Bit Input Status LAL POT Din12 BAD7 Status Antime Variations undefined Viving Antime Sampling 20 ms Viving Antime Sampling 20 ms Viving Line or Viving Sampling Variations

 |

 | UL D14 (Din1) D1N01_BA04 Bit input UL D15 (Din1) D1N01_BA05 Bit input

 | -III DI4 [Din1] DIN01_B404 B4 Input

 | - RR DI4 IDIn11 DIN01 BX04 BX Instal

 |

 | and an and a second a

 | -1.01. 0.14.[Di-11] D1007_0104 0.81mpat -0.01. 0.01.01_0107_0104 0.81mpat -0.01. 0.01.01_0107_0106 0.81mpat -0.01. 0.01.01_0107_0106 0.81mpat -0.01. 0.01.01_0106 0.81mpat

 | Ing. Other District DNNT_BAD4 Belneyset Ing. DVS (Distri) DNNT_BAD5 Belneyset Ing. DVS (Distri) DNNT_BAD5 Belneyset

 | - I_III, 014 [Dist] DINT_2014 Bitsput - I_III, 015 [Dist], 8014 Bitsput - I_III, 015 [Dist], 8015 Bitsput - I_III, 015 [Dist], B016 Bitsput - I_III, 015 [Dist], B016 Bitsput - I_III, 015 [Dist], B017 Bitsput

 | Image Optimization Distribution Distribution Image Amount Distribution Distribution Distribution Image Distribution Distribution Distribution Distribution Distribution Image Distribution Distribution Distribution Distribution Distribution

 | - IR_ 014 (Din1) DNNT_8404 Bitnput
- IR_ 015 (Din1) DNNT_8405 Bitnput
- IR_ 015 (Din1) DNNT_8405 Bit Input

 | - III, 014 (Din1) DNIN_8004 Bilingui
- III, 015 (Din1) DNIN_8056 Bilingui
- III, 016 (Din1) DNIN_8056 Bilingui

 | - DL 04 (Divit) DIVIT_BAN4 B4 heput
- JL 045 (Divit) DIVIT_BAN5 B4 heput
- JL 016 (Divit) DIVIT_BAN5 B4 heput

 | -RL 044(Dim1) DN07_8404 B4/sput
-RL 055(Dim1) DN07_8405 B4/sput
-RL 055(Dim1) DN07_8405 B4/sput

 | - RL 014 (Din1) DIN01_B804 B4 Input
- RL 015 (Din1) DIN01_B805 B4 Input

 | -JUL D14 [Din1] D1N01_B404 B4 Input
-JUL D15 [Din1] D1N01_B405 B4 Input
 | /0, D14 (DIn1) DIN01_8404 8k1 nput
/0, D15 (DIn1) DIN01_8405 8k1 nput

 | 00_ 014 (Din1) DIN01_B804 Bit Input
00_ 015 (Din1) DIN01_B805 Bit Input
 | RL DH4 (DIn1) DH401_B404 Bit Input
RL DH5 (DIn1) DH401_B405 Bit Input

 | 50, DI4(Din1) DIN01_B404 Bitings4
50, DI5(Din1) DIN01_B405 Bitings4
 | 50_ 0/4 (Din1) DINUT_BH04 B4 Input
50_ 0/5 (Din1) DINUT_BH05 B4 Input
50_ 0/5 (Din1) DINUT_BH05 B4 Input
 | - DL 04 (Din1) DIN01_B004 Bit Input
- JU 015 (Din1) DIN01_B005 Bit Input
- DD D15 (Din1) DIN01_B005 Bit Input | -RL 044(Dim1) DN07_8404 B4/sput
-RL 055(Dim1) DN07_8405 B4/sput
-RL 055(Dim1) DN07_8405 B4/sput
 | - RL 014 (Din1) DIN01_B804 B4 Input
- RL 015 (Din1) DIN01_B805 B4 Input | - 10, D4 (Din1) DN01_8404 B4 trpu4
- 10, D45 (Din1) DN01_8405 B4 trpu4
 | - F/L 014 (Din1) DIN01_8004 B4 Input
- 00. DIS (Din1) DIN01_8005 B4 Input | - IQL 044 [Din1] D1N01_8004 B& Input
- IQL 044 [Din1] D1N01_8005 B& Input |
| JIL Did [Din1] DNNT_SHAR Bit Input Status Active V Duration: undefined Vitring Anner (Pin LifeT) Sampling 20 ms V Value Fill Directification V

 | IQLDH4 [Din1]DH01BH04 B# Input
IQLDH5 [Din1]DH01BH05 B# Input
DLDH1DH01_BH05 B# Input

 | 0[[D14[D1n1] D1N07_B4D4 B4 Input
0[[D15[D1n1] D1N07_B4D5 B4 Input

 | - DI DIA [Dia1] DINIT_BADA BA Input

 | ER DI4 IDIn11 DIN07 B404 B4 Input

 |

 | AN DECEMPTOR CONTRACTOR CONT

 | - 100, 014 (Dm) DNU_2404 Beinput
- 100, 016 (Dm) DNU_2405 Beinput
- 100, 016 (Dm) DNU_2405 Beinput
- 100, 016 (Dm) DNU_2406 Beinput

 | -AU, Di Al Dini) DINU, BADA Balingut
AU, DIS (Dini) DINU, BADA BABA
-AU, DIS (Dini) DINU, BADA BABA

 | -100, 014 (Din1) DIN0,2404 Bitnpar
-100, 015 (Din1) DIN0,2405 Bitnpar
-100, 015 (Din1) DIN0,2405 Bitnpar
-100, 015 (Din1) DIN0,2405 Bitnpar

 | - JDL 014 (Din1) DNNL_B004 B4 Input
JDL 015 (Din1) DNNL_B005 B4 Input
- JDL 015 (Din1) DNNL_B005 B4 Input
- JDL 015 (Din1) DNNL_B005 B4 Input
- JDL 015 (Din1) DNNL_B005 B4 Input

 | - JUL 014 (Din1) DNIU_8004 B4 Input
- JUL 015 (Din1) DNIU_8056 B4 Input
- JUL 015 (Din1) DNIU_8066 B4 Input

 | -101, 014 (Din1) D1N07_B404 B4 hout
-101, 015 (Din1) D1N07_B405 B4 hout
-101, 015 (Din1) D1N07_B405 B4 hout

 | - JQL 014 [Din1] D1N07_8004 B4 Input
- JQL 015 [Din1] D1N07_8005 B4 Input
- JQL 015 [Din1] D1N07_8005 B4 Input

 | - JD, Old [Din1] DINUT_BRIA Bit Input
- JD, Old [Din1] DINUT_BRIAS Bit Input
- JD, DIR [Din1] DINUT_BRIAS Bit Input

 | - JUL 014 (Dim1) DINUT_BAUA B4 Input
- JUL 015 (Dim1) DINUT_BAUB B4 Input

 | -JUL 016 [01-1] DINUT_BAD4 84 Input
JUL 016 [01-1] DINUT_BAD5 8805 88 Input
 | JU_014[Din1] DIN01_8404 B4 Input
-JU_015[Din1] DIN01_8405 B4 Input

 | -III, D14 [D1n1] D1N01_8404 Bit Input
-III, D16 [D101] D1N01_8405 Bit Input
 | JUL 014 (Din1) DIN01_8404 84 Input
JUL 015 (Din1) DIN01_8405 84 Input
 | - JUL D14 (Din1) D14(0; 8404 B4 (eput
- JUL 015 (Din1) D14(0; 8405 B4 (eput

 | DL D14[Din1] DNND_B404 B4.tepu4
DL D15[Din1] DNND_B405 B4.tepu4
DL D15[Din1] DNND_B405 B4.tepu4 | -]0, 04.[0in1] DINUT_BAD4 B4.fmput
-]0, 015.[0in1] DINUT_BAD5 B4.fmput
0, 015.[0in1] DINUT_BAD5 B4.fmput
 | - JD, Old [Din1] DINUT_BRIA Bit Input
- JD, Old [Din1] DINUT_BRIAS Bit Input
- JD, DIR [Din1] DINUT_BRIAS Bit Input
 | - JUL 014 (Dim1) DINUT_BAUA B4 Input
- JUL 015 (Dim1) DINUT_BAUB B4 Input | -10, 04 (Din1) DINU-8404 B4 Input
-10, 05 (Din1) DINU-8405 B4 Input
 | - JU D4 (Din1) D1N07_8404 84 input
- 00 D5 (Din1) D1N07_8405 8405 84 input | - 10, D14 (Din1) DIN07_8404 B4 input
- 00_0145 (Din1) DIN07_8405 B4 input |
| JBL Disk Diniti Diskul Sexis Bet legue JBL Disk Diniti Diskul Sexis Bet legue JBL Disk Diniti Diskul Sexis Bet legue JBL Disk Disk Diskul Sexis Bet legue Statut Active Dustion: undefined Vibring Statut Sampling 20 ms Conscion: Lineor 20 ms 20 ms Vibring Statut Sampling 20 ms Valorg

 | -UL Of Oral Band Band Band Band Band Band Band Band

 | UL DIS(DINT) DINO_BNDS BRITS

 | The one (only one) and and other other

 | DI MAN DIN DIN DI

 | DR DM DL-11 DM/R DADA DA Los A

 | TO DISTRICT DISTRICT DISTRICT DISTRICT

 | - 100, 0 % (0 m) 0 NNT_86/5 82 (nput
- 100, 0 % (0 m) 0 NNT_86/5 82 (nput
- 100, 0 % (0 m) 0 NNT_86/6 82 (nput
- 100, 0 % (0 m) 0 NNT_86/6 82 (nput

 | -10, 01 (011) DNO 805 BH Input
-10, 015 (011) DNO 805 BH Input
-10, 015 (011) DNO 805 BH Input

 | -100, 00 (00 m) 0 m07_80/6 8 m mpu
-100, 00 (00 m) 0 m07_80/6 8 m mpu
-100, 00 (00 m) 0 m07_80/6 8 m mpu
-100, 00 (00 m) 0 m07_80/6 8 m mpu

 | - 100, 0 % (0 m1) 0 m 0 m 0 m 0 m 0 m m 0 m m 0 m m 0 m m 0 m m 0 m m 0 m m 0 m m 0 m m 0 m m 0 m m 0 m m 0 m

 | 100, 0 % (0 m1) 0 m 0 8 % 0 m 0 m 0 m 0 m 0 m 0 m 0 m 0 m 0 m 0

 | -10L 0/6 (Diri) Diriot_cond Diriot_cond
-10L 0/6 (Diri) Diriot_60/5 Bit hput
-10L 0/6 (Diri) Diriot_60/6 Bit hput

 | - 10, 0% (0/m) DNN 8405 Bingut
- 10, 0% (0/m) DNN 8405 Bingut

 | - 20_001011 0000_0004 001002
- 20_001011 0000_8005 8t input
- 20_001011 00001 8405 8t input

 | JLL DiS[Din1] Din0_Bolds Bitiput

 | Jul 05 (0h1) 0h01_8005 Bt Input
 | -JUL 016 [01n1] 01N01_8805 Bit input

 | -JL 05(0h1) 0h0-5605 8t liqut
 | JUL Dis [Din1] Direct_solution of input
JUL Dis [Din1] Direct_solution of the solution of the
 | -ILL Officient] Dinol_Bobb Skinput

 | -JUL 0/5 (Din1) DiN01_8405 Bit input | -700_006(0m1) 01001_0005 08thpt
-700_005(0m1) 01001_8055 08thpt
-700_005(0m1) 01001_8055 08thpt
 | - 20_001011 0000_0004 001002
- 20_001011 0000_8005 8t input
- 20_001011 00001 8405 8t input
 | JLL DiS[Din1] Din0_Bolds Bitiput | -JL 05(0h1) DH0_5805 BH hput
 | | - DC DE DIATI DINUESSON DE EST |
| LDL Dots (Din1) DIN07, Brins Bit Reput LDL DIS (Din1) DIN07, Brins Bit Reput LDL DIS (Din1) DIN07, Brins Bit Reput Statut Active Warg (ansitive Duarison undefined Warg (ansitive Sampling (20 ms V Values Justion undefined V Values "undefined Values

 | - US (Diri) DNU1-8065 Bt Ingut

 | UL 0/5 (Din1) DIN01_8#05 Bit Input

 | RE DE DU TU DADE DADE DADE DALLAS

 |

 | - RR DI4 (Din1) DIN01 B404 B4 Insut

 | -R0 DI4 (Din1) DIN01 B404 B4 (maxt

 | -20, D(S)D(n1) D(N0)_8805 B& Input
-20, D(S)D(n1) D(N0)_8805 B& Input
-20, D(S)D(n1) D(N0)_8805 B& Input
-20, D(S)D(n1) D(N0)_8807 B& Input

 | -101 DIS (Din1) DIN01_8405 Bit input
-101 DIN01_8405 Bit input

 | -20, 015 (01n1) 01N01_8405 Bit Input
-20, 015 (01n1) 01N01_8405 Bit Input
-00, 012 (011) 01N01_8405 Bit Input

 | -102, 0/6 (Din1) DIN07_8605 Bit Input
-02, 0/6 (Din1) DIN07_8605 Bit Input
-02, 0/6 (Din1) DIN07_8605 Bit Input
-02, 0/2 (Din2) DIN07_8605 Bit Input

 | -101, 015 (Din1) DIN01_8405 Bit Input
-101, 045 (Din1) DIN01_8406 Bit Input

 | -10, 0/6 (Din1) DIN01_6805 Bit Input
-10, D/6 (Din1) DIN01_6806 Bit Input

 | -30, 0/5(Din1) DIN0_8405 Bit Input
-30, Di6(Din1) DIN0_8406 Bit Input

 | -10, DIS(Din1) DIND_BR05 B4 Input

 | -JUL DIS[Din1] DIN01_BH05 Bit Input

 | JUL D/S (D/m1) D/N01_BK05 Bit Input
 | -JUL DIS(DIn1) DIN01_8805 Bit Input

 | -JUL 0/5(Din1) 0/N01_8405 Bit input
 | -JUL DIS(Din1) DIN01_BH05 Bit Input

 | -RL DIS (Din1) DIN01_BR05 Bit Input
 | - ()[[0/5 (0/n1] 0/N01_8405 Bit Input
- ()[[0/5 (0/n1] 0/N01_8405 Bit Input
- ()[[0/5 (0/n1] 0/N01_8405 Bit Input
 | -7(1, DIS (DIn1) DINIT_8405 Bit Input
-7(1, DIS (DIn1) DINIT_8405 Bit Input | -10, DIS(Din1) DIND_BR05 B4 Input
 | -JUL DIS[Din1] DIN01_BH05 Bit Input | -JL DIS [DIn1] DIN01_8405 Bit Input
 | 00 0/5 (Din1) DIN0 8405 Bt Inst | -BB DISIDIAL DINO 8405 BEIneve |
| LBL DO IS (Din1) DIN10) = RMS Bit Input LBL DO IS (Din1) DIN10) = RMS Bit Input Status Active V Verse Sample Dutation: undefined V Valence

 | - DL D(S (Din1) DND_BR05 BE Input

 | III DIS(DIn1) DIN01_BADS Bit Input

 | 88 00 00-00 0000 0000 00 local

 |

 | -RL DI4 [Din1] DIN01_Bd04 Bit input

 | -III D14 (DIn1) D1N01_Bit04 Bit Input

 | -101, 016 (Din1) DN01,8405 Bit hput
-01, D16 (Din1) DN01,8405 Bit hput
-02, D17 (Din1) DN01,8405 Bit hput

 | -10, 06(0in1) 01N01_8405 Bit Input
-10, 06(0in1) 01N01_8405 Bit Input
-10, 06(0in1) 01N01_8405 Bit Input

 | -101, 0/5 (0/n1) 0/101 8/6/5 8k (nput
-101, 0/6 (0/n1) 0/101 8/6/6 8k (nput
-01, 0/6 (0/n1) 0/101 8/6/7 8k (nput

 |

 | -/IIL 0/5(Din1) DIN01_8405 Bit Input
-/IIL 0/5(Din1) DIN01_8405 Bit Input

 | -JUL 0/5(Din1) DIN01_8405 Bit Input
-JUL 0/6(Din1) DIN01_8406 Bit Input

 | -JUL 0/5 (Din1) DIN01_8405 Bit Input
-JUL DIS (Din1) DIN01_8406 Bit Input

 | -DL DIS (DIAT) DINOT 8805 Bit Input

 | -JJL 0/5(Din1) DIN01_8#05 Bit Input

 | -DL 0/5 (0/n1) 0/N01_8#05 Bit Input
 | -JUL DIS(DIn1) DIN01_8405 Bit Input

 | -JJL DIS (DIn1) DIN01_8805 Bit Input
 | -JJL 0/5(0/m1) 0/N01_8#05 Bit Input

 | -JIL DIS DIn1 DIN0_8405 Bit Input
 | RL DIS (Dirit) DINUT_8405 BE input
RL DIS (Dirit) DINUT_8405 BE input
 | -10, 015 (01n1) 01N01,8405 Bit Input
-10, 015 (01n1) 01N01,8405 Bit Input | -DL DIS (DIAT) DINOT 8805 Bit Input
 | -JJL 0/5(Din1) DIN01_8#05 Bit Input | -RL DIS(DIn1) DIN01_8805 Bit Input
 | -00 D(5(D(n1) D(N0) 8605 Bt (next | -BE DIS(DIs1) DIND 8405 BE level |
| JUL Uo B (Din1) DINU_servo Bit report JUL UO B (Din1) DINU_servo Bit report JUL UO J (Din1) DINU_servo Bit report Status: Active: V Dutation: Status: Active: V Status: Active: V Status: Active: V Verge: Sampling: 20 ms Varies: 10 mm* 10 mm*

 | -JU 05 (Din1) DIN01_6805 Stingut

 | III DIS (DIn1) DIN01_BR05 Bit Input

 |

 |

 | -JJL DI4 (Din1) DIN01_B804 Bit Input

 | -JUL D14 (Din1) D1N01_B404 B4 Input

 | -101, Dis Dini1, DiN01_BH/B, BH/B
-101, Dis Dini1, DiN01_BH/B
-101, Dis Dini1, DiN01_BH/B, BH/B
-101, Div10_BH/D, BH/D, BH/B

 | - (J], DIS [DIN1] DIN01_BR/D BE Input
- (J], DIS [DIN1] DIN01_BR/D BE Input

 | QL DIS (Din1) DIN01_8405 Bit Input
QL DIS (Din1) DIN01_8406 Bit Input
QL DIZ (Din1) DIN01_8407 Bit Input

 |

 | -DU DIS DINI DING BHD BE Input
-DU DIS DINI DING BHD BE Input

 | -10L DI6(Din1) DIN01_8805 Bit input
-10L DI6(Din1) DIN01_8806 Bit input

 | -10L 016 (01n1) 01N01_8805 Bit input
-10L 016 (01n1) 01N01_8805 Bit input

 | -UL DIS [Din1] DIN0_BRDS Bit input
-DD DIS [Din1] DIN0_BRDS Bit input

 | -JUL DIS [Din1] DIN0_BRDS Bit input

 | -JUL DIS(DIn1) DIN01_B805 Bit Input
 | -RL DIS(DIn1) DIN01_8805 B# Input

 | -JUL 0/5 (Din1) DIN01_8#05 B# Input
 | -JUL DIS(DIn1) DIN01_BR05 Bit Input

 | -JUL DIS [DIN1] DIN0_BR05 Bit Input
 | -JUL DIS [DIn1] DIMD_BRID BRIDE BRIDE
 | -JUL DIS (Din1) DIN01 8406 Bit Input
- DI DIS (Din1) DIN01 8406 Bit Input | -UL DIS [Din1] DIN0_BRDS Bit input
-DD DIS [Din1] DIN0_BRDS Bit input
 | -JUL DIS [Din1] DIN0_BRDS Bit input | - JUL DIS(DIn1) DIN01_8605 Bit Input
 | I-DE DIS (DIS1) DINO 8625 Et Intert | - DE DINDE SKE SELENE |
| 100. D04.00.n1 D1400.130.06 B2 Hoput 007.01 D1401.130.06 B2 Hoput B2 Hoput Statu: Active: v Duration: undefined v Voing: Active: v Duration: undefined v box Noner P81.187 Sampling: 2.0 ms v box box v Valing: Active: v Sampling: 2.0 ms v box

 | BR DIS DIS 11 DISOS DI LINE

 |

 | HIL USIDINI DINU BUD BUDU

 | -III DISIDINI DINOI 8405 Bit Input

 | DI DISIDIOTI DINO 805 BEInnet

 |

 | -0.0 0/6 (Din1) DIN01_8406 Bit Input
-0.0 0/7 (Din1) DIN01_8407 Bit Input

 | - III DIS DIA11 DIN01 BR06 Bilinput

 | -101. DIS DIN1 DIN01 B406 BR/most

 | -00. DIS DIN1 DIN01806 Bit Input

 | -III Dis Din11 DiN01_Babs Bit Input

 | -III Dis (Din1) DIN01_Bk06 Bit Input

 | -JJL DIS (DIn1) DIN01_8806 Bit Input

 | DE DIS DISTE DINOT BADE BALand

 |

 |
 |

 |
 |

 | 55 515 51 51 5105 5105 511 51
 | DIS DIS DIST DINUT BROS BEInget
 | - TE DIS (Din1) DING BROS BEInout | DE DIS DISTE DINOT BADE BALand
 | |
 | | CARL MARKET MARKET DE MARK |
| ULL Unit Joint Divid Joints Bit report annel Statut Divid Strip Bit report Statut Annel Duration undefined V Vring Jointim Divid Intro Duration undefined V Vring Jointim Sampling 20 ms Innov v Voring Jointim Sampling 20 ms V 2000 µ eps Sampling Life Indefined V Valorg Duration

 |

 |

 | -UL USUNI UNU SUD SEIDU

 | -JIL UIS [DIn1] DIN01_BR05 Bit Input

 | L-00 DIS(D)(61) DIS(0, 8405). Stimut

 | The definition of the second sec

 | -3LL DIS (Din1) DIN01_SNDS BX INDUX

 | -JUL UIS [DIN1] DINUT_BRUE BEITRE

 | - JUL UIS (UINT) UNUT_SINDS BRITING
- DR DIZ DIATI DINOT_SINDS BRITING

 | -JUL DISIDIATI DINU_SBDS Skingut

 | -JUL UIS (DIN1) DINUI SAIDS BEI INGLE

 | JUL DIS (DINI) DINU BRUS BR Input

 | -JUL DIS(DIN1) DINU1_SR05 B# Input

 | VIII DINID KAN KAINA

 | BULL NO.

 | BR BUILDE BUILDE BUILDE BUILDE BUILDE
 | THE BUSINESS BUSINESS BUSINESS BUSINESS

 | AN DIALAST DIALAST DIALAST DIALAST
 | THE DISCOULD
 |
 | L-DD DIR DDR DD DDR DDR DDR DDR DDR DDR DD
 | HII DISIDISTI DINUI SEDS SEINCH
 | VIII DINID KAN KAINA
 | BULL NO. | THE DISCOULT DISCOULI | | an ever the second seco |
| Statu: Active: Voing Janpine Samping 20ms Samping 20ms Voing Janpine Vo

 | -AL DI7 (Din1) DIN01 BR07 BE Input

 | AL DI7 [Din1] DIN01 BK07 Bk Inskt

 | The second of the second

 | THE MEDIANEL MENT DEED DEFENSION

 | nn Dis Diati DiNU Bids Bit Irout

 | -00, 0/5 (0/m1) DN00_8805 Bitingut
-00 D/5 (0/m1) DN00_8805 Bitingut

 |

 | Hat bir bind bior bior

 | CARL NOT INTELL MORE NOTION

 | DUBLICATION DUBLICATIO

 | Hall Diribini Diribi Brun Brinda

 | HIL DI7 (DIn1) DIN01 B#07 Bit Input

 | -AL DI7 (Din1) DIN01 BK07 Bk Inout

 | HIL DI7 (Din1) DIN01 B407 B4 Input

 | -III DIV01 BK07 Bt Input
 | 00 DIZ Dio11 DIN01 B40Z Bit lovet

 | DR DR DR DBD DBD7 DBD7 DB1met
 | DR D/2 (Dist) DN01 DA02 DE Inst

 | -RR DIZ (Din1) DIN01 8407 Bit Input
 | -AL DI7 [Din1] DIN01 8407 Bit Inst
 | -AL DI7 [Din1] DIN01 8407 Bit Inst
 | -RL D/7 (Din1) DIN01 8407 Bit Input
 | HIL DI7 (Din1) DIN01 B407 B4 Input | -III DIV01 BK07 Bt Input
 | BR DIZ DIA1 DINOT BADZ BATWARD | 00 0/00/11 01/00 0000 01/00 |
 |
| Statut Active W Duration undefined Connection Lineor W Viving Anattitier Sampling 20 ms Viving Anattitier Sampling 20 ms Viving Anattitier Viving Anattitier <td< td=""><td></td><td></td><td>HUL DIVIDINI DINOT BROZ BRIDDR</td><td>Juli Di Dini Dini Bilo Bilo Bilo Bilo Bilo Bilo Bilo Bil</td><td>-III DI6 (DIn1) DIN0T_BR06 Bit Input
-IIII DI7 (DIn1) DIN0T_BR07 Bit Input</td><td>-101, 015 (Din1) DN01,805 Belreyut
-101, D16 (Din1) DN01,806 Bèlreyut
-101, D17 (Din1) DN01,806 Bèlreyut</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>AND ALL ALL ALL ALL ALL ALL ALL ALL ALL AL</td><td>FARE ALL AND AND AND AND AND AND AND AND AND AND</td><td>Part of the owner owner</td><td>The second second</td><td></td><td></td><td></td><td></td><td></td><td>Part of the owner owner</td><td>HILL DIFIDINIT DIRVE BRIDDE</td><td>HIL DI7[Din1] DIN01 B#07 B# Input</td></td<>

 |

 |

 | HUL DIVIDINI DINOT BROZ BRIDDR

 | Juli Di Dini Dini Bilo Bilo Bilo Bilo Bilo Bilo Bilo Bil

 | -III DI6 (DIn1) DIN0T_BR06 Bit Input
-IIII DI7 (DIn1) DIN0T_BR07 Bit Input

 | -101, 015 (Din1) DN01,805 Belreyut
-101, D16 (Din1) DN01,806 Bèlreyut
-101, D17 (Din1) DN01,806 Bèlreyut

 |

 |

 |

 |

 |

 |

 |

 |

 |
 | AND ALL ALL ALL ALL ALL ALL ALL ALL ALL AL

 | FARE ALL AND
 | Part of the owner

 | The second
 |
 |
 |
 | |
 | Part of the owner | HILL DIFIDINIT DIRVE BRIDDE | HIL DI7[Din1] DIN01 B#07 B# Input |

L'ultima cosa da configurare è il trigger (Figura 4.16), infatti nei test si utilizzerà un trigger manuale attivato direttamente dal pilota che consentirà di acquisire solo i run interessanti ai fini delle analisi, semplificando così il post-processing.

Rase Processing Eve	gts Inigger Amplifier i	Jalancing		
Event	Event type	Trigger definition	Target	Action
	Signal = 1	Troper 01 Amount1 (DR)	The Definition of the Definiti	Catal (Seco Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop Stat/Stop
		Tripper_02 Amount:1 (Passive) Tripper_03 Amount:1		
		Time M		
Event T No		Trigger definition		
Analog inputs R_UFT R_ORAG L_UFT L_UFT R_UFT	Shot	Name: Trigon_01 Amount: 1 Combination: 0R	8	
NO TIMOTO	B Sires Int Department			

Figura 4.16: Schermata della scheda "Trigger" nel software "imc Devices V2.6"

Una volta terminata la configurazione dei canali e impostato il trigger, la schermata principale del software imc si presenta come in figura 4.17, tutto è pronto per l'azzeramento dei canali e l'inizio delle acquisizioni.

imc Devices - Tassetto_FSAF_carichi_aero	
Settings Measurement Extras Channels Device View ?	
2월 MIE에수상 12월 - 7월 - 12	
octive measurement channe	
ALUTO ALUTO ALUTO ALUTO ALUTO POTAL	
dr	- Lai
/ Start 🖉 🕼 in: Devices - Tassett 🛛 🔿 💁 🖓 🕲 🖓 🖉	10.57

Figura 4.17: Schermata principale a configurazione terminata nel software "imc Devices V2.6"

Tutti i settaggi impostati sono stati salvati nella directory di lavoro "Tassetto_FSAE_carichi_aero".

4.4 Verifica dei segnali di acquisizione

Una volta installate in vettura le celle di carico e l'intero sistema di misura, si è verificato a motore spento il corretto funzionamento del sistema di acquisizione dei segnali. Sono stati applicati dei carichi a mano prima all'ala anteriore e poi all'ala posteriore, in figura 4.18 si possono osservare i segnali acquisiti.

L'elaborazione e l'analisi dei segnali è stata eseguita con il software "imc FAMOS Enterprise 7.3" come sarà spiegato nel capitolo 6.



Figura 4.18: Verifica dei segnali di acquisizione

Per verificare la compatibilità dei segnali con le sollecitazioni applicate si controlla il segno degli outputs e l'ordine di grandezza. Si nota come un carico di lift all'anteriore comporti un trasferimento di carico all'anteriore con i pull anteriori che si caricano e quelli posteriori che si scaricano. Si osserva invece come il carico di lift al posteriore abbia comportato solo un aumento di carico dei pull posteriori, sinonimo di un carico quasi verticale ai i pull considerati. I carichi di drag invece non comportano significativi trasferimenti di carico alle sospensioni.

La presenza di drag all'applicazione di carichi di downforce e viceversa va spiegato con il fatto che caricando a mano è impossibile applicare carichi puramente di lift o di drag.

Va sottolineato come le osservazioni sopra riportate non vogliono avere carattere generale ma semplicemente servire da spiegazione alla correttezza dei segnali di prova acquisiti.

Non vengono riportati i segnali dei potenziometri in quanto la bassa entità dei carichi applicati comporta quasi nulle compressioni delle sospensioni.

Capitolo 5: Test in pista

5.1 Sito dei test

Il sito dove eseguire i test doveva garantire un asfalto con un grip sufficientemente elevato e l'assenza di particolari buche o avvallamenti. Inoltre, era importante avere sufficiente spazio libero per poter eseguire run a velocità costante e mantenere la velocità target almeno per 3 secondi.

Per queste ragioni il test è stato eseguito nel piazzale della Baita al Lago a Cittadella (PD) (Figura 5.1).



Figura 5.1: Visuale aerea del piazzale di Baita al Lago (PD)

Arrivati nel parcheggio, per prima cosa sono stati posizioni i birilli per definire il percorso delle varie prove ed è stata pulita la traiettoria da ghiaia e dalle foglie secche (Figura 5.2).



Figura 5.2: Piazzale pulito e pronto per l'inizio dei test

5.2 Prove in programma

Una volta sistemata la "pista", si è ultimato il setup della macchina, inserito e fissato le batterie delle centraline e montato il muso. Infine si è acceso il motore (Figura 5.3).



Figura 5.3: Vettura MG 13.18 pronta per la sessione di test

Prima di iniziare le prove, la vettura deve eseguire dei run di riscaldamento per portare in temperatura gomme e organi meccanici e per verificare che tutta l'attrezzatura montata in macchina sia fissata a dovere. Terminata questa fase, si è fermata la macchina ed eseguito l'azzeramento di ciascun canale della centralina Cronos e del modulo di espansione Cansas. Questa procedura è stata eseguita a motore spento e con pilota a bordo.

Successivamente è stato eseguito un run di prova a velocità variabile per verificare il corretto funzionamento della strumentazione utilizzata a bordo (Figura 5.4).



Figura 5.4: Run di prova

Una volta fatte queste verifiche, si è dato il via alle prove che hanno seguito il seguente programma:

Prove in rettilineo a velocità costante (Figura 5.5): durante i run si è cercato di mantenere per un intervallo di circa 3 secondi la velocità target che è stata di 30, 50, 70, 90 km/h. Per controllare la ripetibilità delle misure ogni run è stato ripetuto per 3 volte. Al fine di eliminare l'influenza del vento, per ogni velocità l'auto avrebbe dovuto percorrere un run in una direzione e poi un run alla stessa velocità ma in direzione opposta. Ciò è stato possibile solo per le velocità di 30 e 50 km/h in quanto a velocità più alte c'erano ostacoli a fine parcheggio che rendevano pericolosa l'esecuzione del test in una direzione. Infatti, la vettura nei run a 70 e soprattutto a 90 km/h riusciva ad acquisire la velocità solo nel tratto finale del parcheggio.

L'intensità del vento è stata misurata a ogni run con un anemometro a ventola (Figura 5.6).



Figura 5.5: Rettilineo utilizzato per i run a velocità costante



Figura 5.6: Anemometro a ventola

• Tracciato (Figura 5.7): si è riprodotto un semplice tracciato che permetta di studiare il comportamento in transitorio della vettura. Il percorso riproduce le tipiche caratteristiche di un circuito di F.S.A.E. e sono stati eseguiti 12 giri in senso antiorario.



Figura 5.7: Mappa del tracciato

• Prove circolari a velocità costante (Figura 5.8): per riprodurre la prova di skid-pad sono stati posizionati dei birilli (Figura 5.9) che formavano una circonferenza di raggio tipico della prova, cioè con un raggio di 9.125 m, una circonferenza 3 m più grande e una 3 m più piccola.



Figura 5.8: Zona utilizzata per la prova dei run circolari



Figura 5.9: Prova run circolari a velocità costante

In questa prova la vettura percorreva la circonferenza 10 volte alla massima velocità possibile, sia in un senso che nel senso opposto.

5.3 Sessione di test

Di seguito vengono elencati i parametri dell'auto e le condizioni ambientali osservate all'inizio della sessione di test:

•	Data:	19/11/2018
•	Orario:	Dalle 8.00 alle 17.00
•	Condizioni del tracciato:	Non gommato, pulito, con poco grip
•	Temperatura dell'aria:	6°C
•	Temperatura dell'asfalto:	10°C
•	Pressione atmosferica:	101000 Pa
•	Umidità:	40 %
•	Peso totale (Vettura + pilota):	290 kg
•	Rigidezza delle molle anteriori e posteriori:	60 N/mm
•	Pneumatici:	Hoosier 20.5x7.0-13 R25B (usate)
•	Pressione degli pneumatici:	0.5 bar (a freddo)
•	Raggio delle ruote:	250 mm
•	Altezza C.O.G.:	300 mm
•	Passo:	1535 mm

In tabella 5.1 si riporta in dettaglio la sessione di prove eseguita.

Numero	Configurazione	Configurazione	Velocità	Direzione	Orario	Direzione	Velocità
Run	ala anteriore	ala posteriore	nominale[km/h]			del vento	del
							vento
1	Δ	٨	20	N	11.40	Contrario	[KM/N] 1
1	A	A	30	IN C	11.40	Contrano	1
2	A	A	30	S	11.50	Favore	1
3	A	A	50	N	12.00	Contrario	1
4	A	A	50	S	12.02	Favore	1
5	A	A	70	N	12.26	Contrario	2
6	А	А	90	Ν	12.34	Contrario	0.5
7	Μ	А	30	N	12.50	Contrario	1
8	M	A	30	S	12.52	Favore	1
9	M	А	50	N	13.16	Contrario	1
10	M	A	50	S	13.17	Favore	1
11	M	A	70	N	13.30	Contrario	1.5
12	M	A	90	N	13.35	Contrario	0.5
13	M	M	30	N	13.44	Favore	1.5
14	M	M	30	S	13.46	Contrario	1.5
15	M	M	70	N	13.51	Contrario	0.5
16	M	M	90	N	13.59	Favore	1
17	M	M	50	N	14.15	Favore	1.5
18	M	M	50	S	14.17	Contrario	1.5
19	В	В	30	N	14.42	Favore	2
20	В	В	30	S	14.44	Contrario	2
21	В	В	50	S	14.46	Contrario	1.5
22	В	В	50	N	14.47	Favore	2
23	В	В	70	N	14.48	Favore	1
24	В	В	90	N	14.50	Favore	2
25	В	A	30	N	14.59	Favore	1

26	В	А	30	S	15.00	Contrario	1
27	В	А	50	S	15.02	Contrario	1
28	В	А	50	N	15.03	Favore	1
29	В	А	70	N	15.04	Contrario	0.5
30	В	А	90	N	15.06	Contrario	0.5
31	А	А	Variabile	CCW	15.26	-	1-3
32	А	А	26	CCW	16.04	-	0.5
33	А	А	26	CW	16.08	-	1
34	А	А	33	CW	16.10	-	2.5
35	А	А	33	CCW	16.12	-	2
36	А	А	40	CCW	16.16	-	2
37	А	А	40	CW	16.18	-	2

Tabella 5.1: Programma della sessione di prove

Per configurazione "A" si intende "Alto carico", con "M" "Medio carico" e con "B" "Basso carico". Nel capitolo seguente i vari run verranno richiamati facendo riferimento al numero indicato nella tabella sopra riportata.

Capitolo 6: Analisi preliminare dei dati acquisiti

6.1 Analisi dei dati originali

Per spiegare l'analisi effettuata sui dati acquisiti si prendono in considerazione come esempio i due segnali acquisiti dalla cella di carico aerodinamica anteriore destra A.R. durante il run svolto alla velocità di 90 km/h con la configurazione delle ali in "Alto carico", svoltasi alle 12.34.

L'analisi dei dati e la loro successiva elaborazione viene svolta con il software "imc FAMOS Enterprise 7.3", il programma di elaborazione dati abbinato alle centraline utilizzate.

In figura 6.1 si possono vedere i due segnali vergini, cioè senza alcuna operazione di post-processing.



Figura 6.1: Segnali originali acquisiti dalla cella di carico A.R. nel run 6 (in blu il segnale di lift, in rosso quello di drag)

Si nota come i segnali appaiano alquanto rumorosi, nonostante sia già presente un filtro passa basso con frequenza di taglio a 50 Hz impostato nei parametri di configurazione della centralina. In figura 6.2 viene proposto il segnale acquisito nell'intervallo da 9.5 a 11.5 secondi, quello inquadrato in verde nell'immagine precedente, ossia uno degli intervalli in cui si è mantenuta la velocità costante di 90 km/h nel run preso in considerazione.



Figura 6.2: Dettaglio del segnale del run 6 tra 9.5 e 11.5 secondi

L'ampiezza del disturbo risulta di circa 1000 microepsilon per quanto riguarda il segnale di lift e quasi 2000 microepsilon per quello di drag. Passando alle forze attraverso le matrici di taratura queste incertezze equivalgono a circa 150 N per il lift e 80 N per il drag. È evidente come tali disturbi non siano accettabili al fine di ottenere risultati con un'incertezza non eccessivamente elevata.

6.2 Filtraggio dei dati

Al fine di ridurre l'incertezza delle misure, eliminando il rumore del segnale, si è passati alla fase di filtraggio dei dati.

Sempre utilizzando il software Famos si è deciso di utilizzare un ulteriore filtro passa basso in quanto i fenomeni che vogliamo studiare possono ritenersi quasi statici, è quindi possibile ignorare i fenomeni di vibrazioni proprie delle strutture in gioco.

Il filtro utilizzato è di tipo Butterworth, di secondo ordine, con una frequenza di taglio di 1 Hz. È stata scelta questa frequenza in quanto l'obiettivo principale era trovare il valore medio dei carichi agenti sulle ali durante i test a velocità costante.

Nelle seguenti figure 6.3 e 6.4 si possono vedere i blu i segnali di lift e drag vergini e in rosso gli stessi segnali filtrati.



Figura 6.3: Confronto tra il segnale originale (in blu) di lift e il segnale filtrato (in rosso) in riferimento al run 6



Figura 6.4: Confronto tra il segnale originale (in blu) di drag e il segnale filtrato (in rosso) in riferimento al run 6

Appare evidente come i segnali ora siano molto più puliti e di più facile comprensione.

Infine, in figura 6.5 viene proposto il dettaglio dell'intervallo tra 9.5 e 11.5 secondi: in blu e in rosso rispettivamente i segnali di lift e drag non filtrati, in azzurro e viola invece si vedono gli stessi segnali filtrati.



Figura 6.5: Confronto tra segnali non filtrati e filtrati nell'intervallo 9.5-11.5 secondi (in blu il segnale di lift non filtrato, in azzurro quello filtrato, in rosso il segnale di drag non filtrato, in viola quello filtrato)

6.3 Post-processing dei dati

Una volta filtrato ciascun canale acquisito, è necessario combinare i vari canali al fine di ottenere i carichi agenti su ciascun supporto. Per far ciò è sufficiente combinare i segnali attraverso le matrici di taratura come spiegato nel capitolo 3. Una volta ottenute le 8 componenti di forza (una componente di lift e una di drag per ogni supporto), si vanno a sommare tra di loro le 2 componenti di lift anteriore, le 2 di drag anteriore, le 2 di lift posteriore e le 2 di drag posteriore. Si ottengono così 4 segnali, ossia i carichi di lift e drag relativi all'ala anteriore e posteriore.

In figura 6.6 viene mostrato il risultato del post-processing dei dati relativi alla prova a 90 km/h in configurazione "Alto carico".



Figura 6.6: Acquisizione dei carichi aerodinamici del run 6

Nel caso in esame è facile individuare i tre tratti in cui la vettura ha assunto la velocità target, nei periodi intermedi invece il pilota ha guidato a velocità più moderata per riportarsi all'inizio del rettilineo. Questo andamento si ritroverà anche nelle prove a 70 km/h, in quelle a 30 km/h e a 50 km/h invece il pilota ha guidato a velocità maggiori nei tratti non di rettilineo per permettere a posteriori una più facile individuazione delle zone di run a velocità costante.

Oltre ai carichi aerodinamici, sono stati acquisiti anche i segnali relativi alle forze agenti sui pull e i segnali relativi alle corse delle sospensioni. Per essi è stato eseguito lo stesso filtraggio effettuato per i carichi aerodinamici, in figura 6.7 è possibile visionare tali segnali relativi alla stessa prova sopra menzionata.



Figura 6.7: Acquisizione carichi pull e corse sospensioni del run 6

Si nota come i segnali di forza ai pull rispecchino l'andamento dei segnali di corsa delle sospensioni e viceversa. Questi segnali oltre a permettere un'analisi dei trasferimenti di carico della vettura in relazione ai carichi aerodinamici, sono utili indicatori delle manovre del pilota in pista, facilitando l'individuazione dei tratti a velocità costante qualora non risultino immediatamente evidenti dai segnali aerodinamici.

Per quanto riguarda i segni dei segnali si è utilizzata la convenzione di segno positivo per i pull sollecitati a trazione, va precisato che i segnali dei pull sono stati azzerati con macchina spensa e pilota a bordo, presentano già uno stato di pretensione iniziale. Se si vuole risalire al reale stato di tensione del materiale va sommata cambiata di segno la tensione che i pull presentano in una prova di sollevamento con pilota a bordo (Figura 6.8).



Figura 6.8: Segnali dei pull acquisiti nella prova di sollevamento della vettura

A macchina completamente sospesa i segnali misurati dai pull risultano i seguenti:

- Pull anteriore destro: -1582.1 N,
- Pull anteriore sinistro: -1465.4 N,
- Pull posteriore destro: -1969.8 N,
- Pull posteriore sinistro: -2124.7 N.

Si nota come i pull si trovino in uno stato di pretensione, quindi variazioni di carico negati dei segnali acquisiti durante i run non significano che l'elemento vada in stato di comprensione, ma semplicemente scarica parte della sua pretensione.

Per quanto riguarda i segnali dei potenziometri si è assunta la convenzione di utilizzare segnali positivi per indicare la compressione della molla, così da avere segnali concordi anche in segno con gli andamenti dei segnali dei pull. Inoltre, non viene plottata la corsa assoluta della sospensione, ma la sua variazione di corsa rispetto alla posizione di azzeramento dei canali che è sempre la condizione di macchina spenta con pilota a bordo.

Per completezza in appendice C vengono riprodotti gli esatti codici utilizzati per l'elaborazione dati con il software "imc FAMOS Enterprise 7.3".

Capitolo 7: Analisi dei dati e presentazione dei risultati

7.1 Configurazioni aerodinamiche delle ali

Prima di analizzare i dati sperimentali acquisiti si va a spiegare in dettaglio la geometria dei flap delle ali, che sono profili Naca BE 122-155.

Sia l'ala anteriore che quella posteriore presentano 2 flap regolabili (Figura 7.1) con continuità da un angolo minimo a uno massimo, variando l'angolo di attacco si può variare la configurazione aerodinamica dell'ala. I flap sono attaccati agli "endplates" con due viti M3 per lato, la vite più indietro è usata come pivot per cambiare l'angolo di attacco dei profili (Figura 7.2).



Figura 1.10: Esempio di rotazione del flap [3]



Figura 7.2: "Endplate" dell'ala anteriore (a sinistra) e posteriore (a destra) con evidenziati gli inserti per l'attacco e la regolazione dei flap

In figura 7.3 viene mostrata una riproduzione dell'ala anteriore, per regolamento i flap laterali (in blu) devono restare fissi, quindi per variare l'assetto dell'ala si andrà ad agire solo sui flap centrali (in rosso). L'ala posteriore invece presenta due unici lunghi flap regolabili che occupano tutta la larghezza dell'ala.



Figura 7.3: Ala anteriore con evidenziati i flap regolabili centrali (rosso) e quelli fissi laterali (blu) [3]

In figura 7.1 viene mostrato il profilo dell'ala anteriore, per agire sull'aerodinamica si va a variare l'angolo dei flap centrali definito come l'angolo assoluto tra la direzione orizzontare e la direzione della retta passante per il bordo d'ingresso del profilo e quello di uscita. Con F1 si indica l'angolo del flap 1 e con F2 l'angolo del flap 2.



Figura 7.4: Profilo dell'ala anteriore con quotati gli angoli di posizionamento dei flap

In figura 7.5 viene invece mostrato il profilo dell'ala posteriore, allo stesso modo vengono quotati i flap regolabili che permettono di variare la configurazione aerodinamica dell'ala. Al posteriore i flap sono lunghi tutta la larghezza dell'ala.



Figura 7.5: Profilo dell'ala posteriore con quotati gli angoli di posizionamento dei flap

In tabella 7.1 si riportano le configurazioni di alto, medio e basso carico prese in esame (Figura 7.6), che corrispondono alle tipiche configurazioni usate agli eventi dal team.

Nei prossimi paragrafi si parlerà di configurazione di "Alto carico" per indicare sia l'ala anteriore che quella posteriore settata con i flap nelle posizioni indicate dalla seguente tabella nel caso di alto carico, allo stesso modo si parlerà di "Medio carico" o di "Basso carico". Quando le due ali avranno configurazioni diverse verrà specificato parlando della configurazione dell'ala anteriore e di quella dell'ala posteriore.

	FLAP 1 (F1) [°]	FLAP 2 (F2) [°]	CARICO
ALA POSTERIORE	40	70	Alto
	30	52.5	Medio
	20	35	Basso
ALA ANTERIORE	30	50	Alto
	22.5	37.5	Medio
	15	25	Basso

Tabella 7.1: Angoli assoluti di inclinazione dei flap nelle varie configurazioni



Figura 7.6: Configurazioni aerodinamiche [3]

7.2 Prove in rettilineo a velocità costante

7.2.1 Run in configurazione "Alto carico"

La prima sessione di run in rettilineo è stata eseguita con entrambe le ali in configurazione di "Alto carico".

La velocità della vettura è misurata alle ruote con l'utilizzo di 4 sensori ad effetto Hall, ciascuno montato su una ruota. La velocità misurata viene poi corretta con l'intensità rilevata del vento.

Run a 30 km/h

Si inizia con l'analizzare i run eseguiti a 30 km/h, in figura 7.7 si possono vedere i segnali acquisiti nel run eseguito in direzione Nord.

L'ultima finestra dell'immagine (quella più in basso) mostra il dato sulla velocità, così da facilitare l'individuazione degli intervalli in cui si è mantenuta la velocità costante.



Figura 7.7: Segnali acquisiti nel run 1



In figura 7.8 invece sono raffigurati i dati acquisiti nel run eseguito in direzione opposta, cioè verso Sud.

Figura 7.8: Segnali acquisiti nel run 2

Ogni riquadro verde evidenzia il tratto in cui si è mantenuta la velocità costante, si è calcolata la media dei segnali aerodinamici e la media della velocità alle ruote mantenuta in ciascun intervallo. Poi si è trovato il valor medio della velocità del run e dei carichi aerodinamici.

Il vento non ha variato considerevolmente la sua intensità nei due run considerati, quindi facendo la media tra i carichi nei run in una direzione e quelli nella direzione opposta si ottengono dei valori disaccoppiati dall'influenza del vento.

I risultati ottenuti in questo run fanno riferimento a una velocità di 31.2 km/h e sono:

- Downforce ala anteriore= 46.2 N
- Drag ala anteriore= 7.7 N
- Downforce ala posteriore= 31.5 N
- Drag ala posteriore= 18.8 N

<u>Run a 50 km/h</u>

In figura 7.9 e 7.10 si riportano rispettivamente i segnali acquisiti nel run a 50 km/h in un senso e nel senso opposto.



Figura 7.9: Segnali acquisiti nel run 3



Figura 7.10: Segnali acquisiti nel run 4

Si nota come sia in un senso che in quello opposto la vettura abbia fatto fatica a mantenere una velocità costante di 50 km/h, descrivendo rampe che vanno da 45 km/h a circa 55 km/h. Questi run sono stati ripetuti più volte

ma il problema, legato alla mappa del motore utilizzata per mantenere la velocità costante, non sono stati risolti completamente.

Si considera dunque una velocità media di 48.2 km/h, considerando sia i run in una direzione che in quella opposta per eliminare l'influenza del vento, che durante i due run si è mantenuto costante. I risultati ottenuti in questo run sono:

- Downforce ala anteriore= 116.1 N
- Drag ala anteriore= 20.0 N
- Downforce ala posteriore= 79.2 N
- Drag ala posteriore= 42.3 N

<u>Run a 70 km/h</u>

In figura 7.11 vengono mostrati i segnali acquisiti nel run a 70 km/h. Questa prova è stata eseguita solo in un senso in quanto risultava pericoloso raggiungere la velocità di 70 km/h alla fine del rettilineo nel senso opposto. È quindi necessario tenere in considerazione l'influenza del vento che farà percepire all'ala una velocità diversa da quella nominale di 70 km/h.



Figura 7.11: Segnali acquisiti nel run 5

Durante il run la velocità del vento era in media di 2 km/h in direzione opposta a quella del rettilineo mentre la velocità media dei run misurata alle ruote della vettura era di 69.6 km/h, quindi la velocità da considerare per la prova è di 71.6 km/h.

I risultati ottenuti in questo run sono:

- Downforce ala anteriore= 279.2 N
- Drag ala anteriore= 44.1 N
- Downforce ala posteriore= 170.1 N
- Drag ala posteriore= 89.2 N

<u>Run a 90 km/h</u>

In figura 7.12 vengono mostrati i segnali acquisiti nel run a 90 km/h. Anche questa prova è stata eseguita in un'unica direzione per gli stessi motivi spiegati in precedenza.



Figura 7.12: Segnali acquisiti nel run 6

Durante il run la velocità del vento era in media di 0.5 km/h in senso opposto alla direzione del rettilineo, la velocità media alle ruote è stata di 90.1 km/h, quindi la velocità da considerare per la prova è di 90.6 km/h. I risultati ottenuti in questo run sono:

•	Downforce ala anteriore=	478.9 N
•	Drag ala anteriore=	72.1 N
•	Downforce ala posteriore=	287.6 N

- Drag ala posteriore= 150.5 N
- Drag and posteriore 150.51

Risultati dei run in configurazione di "Alto carico"

In tabella 7.2 vengono riassunti i risultati ottenuti durante i run a velocità costante con le ali in configurazione di "Alto carico".

CONFIGURAZIONE	VELOCITÀ	ANT LIFT	ANT DRAG	POST LIFT	POST DRAG
	[KM/H]	[N]	[N]	[N]	[N]
	31.2	46.2	7.7	31.5	18.8
A A	48.2	116.1	20.0	79.2	42.3
A.A.	71.6	279.2	44.1	170.1	89.2
	90.6	478.9	72.1	287.6	150.5

Tabella 7.2: Riassunto risultati ottenuti nei run a velocità costante in configurazione "Alto carico"



In figura 7.13 vengono mostrati gli andamenti dei risultati ottenuti.

Figura 7.13: Andamento dei carichi in funzione della velocità nei run eseguiti in configurazione di "Alto carico"

7.2.2 Run in configurazione "Medio carico"

Run a 30 km/h

In figura 7.14 sono mostrati i segnali acquisiti nel run eseguito in direzione Nord e nei riquadri in verde gli intervalli in cui si è mantenuta la velocità costante.



Figura 7.14: Segnali acquisiti nel run 13

In figura 7.15 sono raffigurati i dati acquisiti nel run eseguito in direzione opposta, cioè verso Sud.



Figura 7.15: Segnali acquisiti nel run 14

Il procedimento eseguito per l'analisi dei dati è lo stesso svolto in precedenza.

La velocità media osservata nei rettilinei è di 31.1 km/h, mentre l'intensità del vento si può ritenere costante tra run in una direzione e quelli nella direzione opposta.

I risultati ottenuti in questo run sono:

- Downforce ala anteriore= 44.1 N
- Drag ala anteriore= 6.3 N
- Downforce ala posteriore= 29.0 N
- Drag ala posteriore= 12.1 N
<u>Run a 50 km/h</u>

In figura 7.16 e 7.17 si riportano rispettivamente i segnali acquisiti nel run a 50 km/h in un senso e nel senso opposto.



Figura 7.16: Segnali acquisiti nel run 17



Figura 7.17: Segnali acquisiti nel run 18

Anche qui compare il problema di mantenere la velocità costante a 50 km/h, si assume una velocità media di 51.0 km/h osservando che tra run di andata e di ritorno la velocità del vento si mantiene pressoché costante.

	-	
•	Downforce ala anteriore=	111.9 N

- Drag ala anteriore= 16.1 N
 Downforce ala posteriore= 77.2 N
- Drag ala posteriore= 33.3 N

<u>Run a 70 km/h</u>





Figura 7.18: Segnali acquisiti nel run 15

Durante il run la velocità del vento era in media di 0.5 km/h in direzione opposta al moto, quindi la velocità da considerare per la prova è di 70.5 km/h.

•	Downforce ala anteriore=	212.3 N
•	Drag ala anteriore=	30.7 N

- Downforce ala posteriore= 157.7 N
- Drag ala posteriore= 68.2 N

<u>Run a 90 km/h</u>

In figura 7.19 vengono mostrati i segnali acquisiti nel run a 90 km/h. In questa prova i run eseguiti a velocità costante sono stati 5 per calibrare alcuni parametri del limitatore di velocità.



Figura 7.19: Segnali acquisiti nel run 16

Durante il run la velocità del vento era in media di 1 km/h nello stesso senso di percorrenza del rettilineo, quindi la velocità da considerare per la prova è di 88.8 km/h, visto che la velocità alle ruote media è stata misurata in 89.8 km/h.

- Downforce ala anteriore= 347.8 N
 Drag ala anteriore= 48.5 N
- Downforce ala posteriore= 238.6 N
- Drag ala posteriore= 102.8 N

Risultati dei run in configurazione di "Medio carico"

In tabella 7.3 vengono riassunti i risultati ottenuti durante i run a velocità costante con le ali in configurazione di "Medio carico".

CONFIGURAZIONE	VELOCITÀ	ANT LIFT ANT DRAG		POST LIFT	POST DRAG
	[KM/H]	[N]	[N]	[N]	[N]
	31.1	44.1	6.3	29.0	12.1
ММ	51.0	111.9	16.1	77.2	33.3
IVI.IVI.	70.5	212.3	30.7	157.7	68.2
	88.8	347.8	48.5	238.6	102.8

Tabella 7.3: Riassunto risultati ottenuti nei run a velocità costante in configurazione "Medio carico"



In figura 7.20 vengono mostrati gli andamenti dei risultati ottenuti.

Figura 7.20: Andamento dei carichi in funzione della velocità nei run eseguiti in configurazione di "Medio carico"

7.2.3 Run in configurazione "Basso carico"

Run a 30 km/h

In figura 7.21 sono mostrati i segnali acquisiti nel run eseguito in direzione Nord e nei riquadri in verde gli intervalli in cui si è mantenuta la velocità costante.



Figura 7.21: Segnali acquisiti nel run 19



In figura 7.22 sono raffigurati i dati acquisiti nel run eseguito in direzione opposta, cioè verso Sud.

Figura 7.22: Segnali acquisiti nel run 120

La velocità media mantenuta dalla vettura nei rettilinei è stata di 32.1 km/h, la velocità del vento si è mantenuta pressochè costante tra i run in una direzione e quelli nella direzione opposta per cui si ritiene eliminata l'influenza del vento.

- Downforce ala anteriore= 29.6 N
- Drag ala anteriore= 3.9 N
- Downforce ala posteriore= 13.6 N
- Drag ala posteriore= 7.5 N

<u>Run a 50 km/h</u>

In figura 7.23 e 7.24 si riportano rispettivamente i segnali acquisiti nel run a 50 km/h in un senso e nel senso opposto.



Figura 7.23: Segnali acquisiti nel run 22



Figura 7.24: Segnali acquisiti nel run 21

Anche in questi run si nota la difficoltà della vettura a mantenere la velocità costante di 50 km/h, eseguendo la media si può considerare una velocità del run di 48.6 km/h, trascurando l'influenza del vento che si è mantenuto costante nell'esecuzione dei run.

- Downforce ala anteriore= 62.0 N
- Drag ala anteriore= 11.2 N
- Downforce ala posteriore= 31.6 N
- Drag ala posteriore= 20.1 N

<u>Run a 70 km/h</u>





Figura 7.25: Segnali acquisiti nel run 23

Durante il run la velocità del vento era in media di 1 km/h a favore di rettilineo, quindi la velocità da considerare per la prova è di 68.9 km/h, in quanto la velocità media registrata alle ruote è stata di 69.9 km/h.

I risultati ottenuti in questo run sono:

•	Downforce ala anteriore=	140.8 N
•	Drag ala anteriore=	18.4 N
•	Downforce ala posteriore=	61.8 N

• Drag ala posteriore= 33.6 N

<u>Run a 90 km/h</u> In figura 7.26 vengono mostrati i segnali acquisiti nel run a 90 km/h.



Figura 7.26: Segnali acquisiti nel run 24

Durante il run la velocità del vento era in media di 2 km/h nello stesso senso di percorrenza del rettilineo, quindi la velocità da considerare per la prova è di 87.0 km/h, in quanto la velocità media misurata alle ruote era di 89.0 km/h.

- Lift ala anteriore= 242.8 N
- Drag ala anteriore= 26.2 N
- Lift ala posteriore= 98.7 N
- Drag ala posteriore= 50.8 N

Risultati dei run in configurazione di "Basso carico"

In tabella 7.4 vengono riassunti i risultati ottenuti durante i run a velocità costante con le ali in configurazione di "Basso carico".

CONFIGURAZIONE	VELOCITÀ	ANT LIFT	ANT DRAG	POST LIFT	POST DRAG
	[KM/H]	[N]	[N]	[N]	[N]
	32.1	29.6	3.9	15.6	7.5
ממ	48.6	62.0	11.2	31.6	20.1
D.D.	68.9	140.8	18.4	61.8	33.6
	87.0	242.8	26.2	98.7	50.8

Tabella 7.4: Riassunto risultati ottenuti nei run a velocità costante in configurazione "Basso carico"



In figura 7.27 vengono mostrati gli andamenti dei risultati ottenuti.

Figura 7.27: Andamento dei carichi in funzione della velocità nei run eseguiti in configurazione di "Basso carico"

7.2.4 Run in configurazione "Basso carico" all'anteriore e di "Alto carico" al posteriore

Si va ora ad analizzare le configurazioni miste, partiamo da quella che prevede l'ala anteriore scarica e quella posteriore carica.

<u>Run a 30 km/h</u>

In figura 7.28 sono mostrati i segnali acquisiti nel run eseguito in direzione Nord e nei riquadri in verde gli intervalli in cui si è mantenuta la velocità costante.



Figura 7.28: Segnali acquisiti nel run 25



In figura 7.29 sono raffigurati i dati acquisiti nel run eseguito in direzione opposta, cioè verso Sud.



La velocità media mantenuta dalla vettura nei rettilinei è stata di 32.8 km/h, la velocità del vento si è mantenuta pressochè costante tra i run in una direzione e quelli nella direzione opposta per cui si ritiene eliminata l'influenza del vento.

- Downforce ala anteriore= 27.8 N
- Drag ala anteriore= 3.4 N
- Downforce ala posteriore= 42.8 N
- Drag ala posteriore= 19.6 N

<u>Run a 50 km/h</u>

In figura 7.30 e 7.31 si riportano rispettivamente i segnali acquisiti nel run a 50 km/h in un senso e nel senso opposto.



Figura 7.30: Segnali acquisiti nel run 28



Figura 7.31: Segnali acquisiti nel run 27

Si nota la difficoltà della vettura a mantenere la velocità costante di 50 km/h, eseguendo la media si può considerare una velocità del run di 47.4 km/h, trascurando l'influenza del vento che si è mantenuto costante nell'esecuzione dei run.

- Downforce ala anteriore= 59.8 N ٠
- ٠ Drag ala anteriore= 8.5 N Downforce ala posteriore= 86.6 N
- Drag ala posteriore= 42.2 N

<u>Run a 70 km/h</u>





Figura 7.32: Segnali acquisiti nel run 29

Durante il run la velocità del vento era in media di 0.5 km/h in direzione contraria al rettilineo, quindi la velocità da considerare per la prova è di 70.3 km/h, in quanto la velocità media registrata alle ruote è stata di 69.8 km/h. I risultati ottenuti in questo run sono:

•	Downforce ala anteriore=	128.3 N
•	Drag ala anteriore=	15.9 N
•	Downforce ala posteriore=	174.1 N

• Drag ala posteriore= 91.8 N

<u>Run a 90 km/h</u> In figura 7.33 vengono mostrati i segnali acquisiti nel run a 90 km/h.



Figura 7.33: Segnali acquisiti nel run 30

Durante il run la velocità del vento era in media di 0.5 km/h in direzione contraria al verso di percorrenza del rettilineo, quindi la velocità da considerare per la prova è di 90.0 km/h, in quanto la velocità media misurata alle ruote era di 88.5 km/h.

- Lift ala anteriore= 225.3 N
- Drag ala anteriore= 26.5 N
- Lift ala posteriore= 290.7 N
- Drag ala posteriore= 160.2 N

Risultati dei run in configurazione di "Basso carico" all'anteriore e "Alto carico" al posteriore

In tabella 7.5 vengono riassunti i risultati ottenuti durante i run a velocità costante con l'ala anteriore in configurazione di "Basso carico" e quella posteriore in configurazione di "Alto carico".

CONFIGURAZIONE	VELOCITÀ	ANT LIFT	ANT DRAG	POST LIFT	POST DRAG
	[KM/H]	[N]	[N]	[N]	[N]
	32.8	27.8	3.4	42.8	19.6
D A	47.4	59.8	8.5	86.6	42.2
D.A.	70.3	128.3	15.9	174.1	91.8
	90.0	225.3	26.5	290.7	160.2

Tabella 7.5: Riassunto risultati ottenuti nei run a velocità costante in configurazione "Basso carico" all'anteriore e di "Alto carico" al posteriore

In figura 7.34 vengono mostrati gli andamenti dei risultati ottenuti.



Figura 7.34: Andamento dei carichi in funzione della velocità nei run eseguiti in configurazione di "Basso carico" all'anteriore e di "Alto carico" al posteriore

7.2.5 Run in configurazione "Medio carico" all'anteriore e di "Alto carico" al posteriore

Run a 30 km/h

In figura 7.35 sono mostrati i segnali acquisiti nel run eseguito in direzione Nord e nei riquadri in verde gli intervalli in cui si è mantenuta la velocità costante.



Figura 7.35: Segnali acquisiti nel run 7

In figura 7.36 sono raffigurati i dati acquisiti nel run eseguito in direzione opposta, cioè verso Sud.



Figura 7.36: Segnali acquisiti nel run 8

La velocità media mantenuta dalla vettura nei rettilinei è stata di 32.1 km/h, la velocità del vento si è mantenuta pressochè costante tra i run in una direzione e quelli nella direzione opposta per cui si ritiene eliminata l'influenza del vento.

- Downforce ala anteriore= 39.5 N
- Drag ala anteriore= 6.3 N
- Downforce ala posteriore= 36.8 N
- Drag ala posteriore= 21.4 N

<u>Run a 50 km/h</u>

In figura 7.37 e 7.38 si riportano rispettivamente i segnali acquisiti nel run a 50 km/h in un senso e nel senso opposto.



Figura 7.37: Segnali acquisiti nel run 9



Figura 7.38: Segnali acquisiti nel run 10

Anche in questi run si nota la difficoltà della vettura a mantenere la velocità costante di 50 km/h, la velocità media considerata per il run è di 48.4 km/h, mantenendosi la velocità del vento costante in entrambi i run. I risultati ottenuti in questo run sono:

- Downforce ala anteriore= 89.2 N
- Drag ala anteriore= 15.4 N
- Downforce ala posteriore= 83.9 N
- Drag ala posteriore= 48.4 N

Run a 70 km/h In figura 7.39 vengono mostrati i segnali acquisiti nel run a 70 km/h.



Figura 7.39: Segnali acquisiti nel run 11

Durante il run la velocità del vento era in media di 1.5 km/h in direzione opposta al rettilineo, quindi la velocità da considerare per la prova è di 71.8 km/h, in quanto la velocità media registrata alle ruote è stata di 70.3 km/h. I risultati ottenuti in questo run sono:

•	Downforce ala anteriore=	208.5 N			
•	Drag ala anteriore=	32.9 N			
		100 031			

- Downforce ala posteriore= 192.8 N
- Drag ala posteriore= 110.6 N

<u>Run a 90 km/h</u> In figura 7.40 vengono mostrati i segnali acquisiti nel run a 90 km/h.



Figura 7.40: Segnali acquisiti nel run 12

Durante il run la velocità del vento era pressochè zero, quindi la velocità da considerare per la prova è la stessa della velocità misurata alle ruote ed è di 88.5 km/h. I risultati ottenuti in questo run sono:

- Lift ala anteriore= •
- 330.6 N Drag ala anteriore= 43.3 N •
- Lift ala posteriore= 286.6 N •
- Drag ala posteriore= 154.2 N •

Risultati dei run in configurazione di "Medio carico" all'anteriore e di "Alto carico" al posteriore

In tabella 7.6 vengono riassunti i risultati ottenuti durante i run a velocità costante con le ali in configurazione di "Medio carico" all'anteriore e di "Alto carico" al posteriore.

CONFIGURAZIONE	VELOCITÀ	ANT LIFT	ANT DRAG	POST LIFT	POST DRAG
	[KM/H]	[N]	[N]	[N]	[N]
	32.1	39.5	6.3	36.8	21.4
МА	48.4	89.2	15.4	83.9	48.4
MI.A.	71.8	208.5	32.9	192.8	110.6
	88.5	330.6	43.3	286.6	154.2

Tabella 7.6: Riassunto risultati ottenuti nei run a velocità costante in configurazione "Medio carico" all'anteriore e di "Alto carico" al posteriore

In figura 7.41 vengono mostrati gli andamenti dei risultati ottenuti.



Figura 7.41: Andamento dei carichi in funzione della velocità nei run eseguiti in configurazione di "Medio carico" all'anteriore e di "Alto carico" al posteriore

7.3 Tracciato

Dopo aver eseguito i run in rettilineo sono stati sistemati i birilli per formare il circuito rappresentato in figura 7.42, di cui viene quotata la planimetria e vengono numerati i settori.



Figura 7.42: Planimetria circuito (in giallo la numerazione con cui sono stati indicati i settori della pista)

La pista è stata divisa in settori nel seguente modo:

- Settore 1: Rettilineo 1, lunghezza di circa 50 metri;
- Settore 2: slalom su 2 coni, 3 cambi di direzione;
- Settore 3: Curva a sinistra di raggio 6 metri;
- Settore 4: Rettilineo 2, lunghezza di circa 30 metri;
- Settore 5: Curva a sinistra di raggio 10 metri;
- Settore 6: Rettilineo 3, lunghezza di circa 50 metri;
- Settore 7: Curva a sinistra di raggio 8 metri;
- Settore 8: Curva a destra di raggio 3 metri;
- Settore 9: Curva a sinistra di raggio 5 metri.

In figura 7.43 si possono vedere i segnali acquisiti durante l'intero run in pista in cui sono evidenziati i 12 giri eseguiti. Tutto il run è stato eseguito con la configurazione di "Alto carico" sia all'anteriore che al posteriore.



Figura 7.43: Segnali acquisiti nel tracciato (in verde sono delimitati i giri)

I tempi fatti registrare sono i seguenti:

- Giro 1: 20.6 secondi;
- Giro 2: 20.2 secondi;
- Giro 3: 20.6 secondi;
- Giro 4: 20.4 secondi;
- Giro 5: 20.5 secondi;
- Giro 6: 20.8 secondi;
- Giro 7: 20.8 secondi;
- Giro 8: 20.2 secondi;
- Giro 9: 20.2 secondi;
- Giro 10: 20.1 secondi;
- Giro 11: 20.8 secondi;
- Giro 12: 20.2 secondi.

Si va ora ad analizzare in dettaglio il decimo giro eseguito, quello più veloce. In figura 7.44 si possono vedere i segnali acquisite con le centraline IMC, mentre in figura 7.45 si possono vedere i segnali di velocità, accelerazione longitudinale e laterale acquisiti con la centralina di bordo della vettura.



Figura 7.44: Segnali acquisiti nel giro 10 (con indicazione dei settori)



Figura 7.45: Segnali di velocità, accelerazione longitudinale e laterale nel giro 10 (con delimitazione dei settori)

Nel giro si è registrata una velocità media di 44.6 km/h con il picco nel rettilineo del settore 6 di 83.6 km/h. L'accelerazione longitudinale massima registrata è di 0.87 g in accelerazione e di -1.27 g in frenata, mentre quella laterale è stata al massimo di 1.9 g.

Si va ora a descrivere nel dettaglio l'interpretazione dei segnali per ogni settore:

- Settore 1: nel rettilineo iniziale si vede come l'accelerazione positiva, di circa 0.8 g, comporti un aumento della velocità, accompagnata da un aumento dei carichi aerodinamici;
- Settore 2: a fine rettilineo si frena per poter impostare alla giusta velocità lo slalom che prevede 3 cambi di direzione. Si nota come l'accelerazione laterale si cambi segno proprio 3 volte assumendo valori massimi di circa 1.1 g. Questi cambi di direzione sono accompagnati da trasferimenti di carico laterali come si nota sui segnali dei pull e su quelli dei potenziometri;
- Settore 3: nella curva verso sinistra per immettersi nel secondo rettilineo si osserva un'accelerazione laterale di circa 1 g e si vede come si vadano a caricare i pull anteriore e posteriore destro e si vadano a scaricare gli altri due, lo stesso comportamento si nota nei segnali dei potenziometri;
- Settore 4: nel secondo rettilineo si vede un'accelerazione positiva accompagnata da aumento di velocità e dei carichi aerodinamici;
- Settore 5: tra il rettilineo 2 e 3 c'è una curva verso sinistra di ampio raggio che consente di mantenere una velocità sostenuta di circa 60 km/h;
- Settore 6: il terzo rettilineo permette di raggiungere le velocità maggiori nel giro in quanto si parte dalla velocità del curvone precedente e si raggiungono velocità superiori agli 80 km/h. Qui si

raggiungono i carichi aerodinamici maggiori: circa 35 kg di downforce all'anteriore e 25 kg di downforce al posteriore;

- Settore 7: alla fine del terzo rettilineo si frena con un'accelerazione negativa di circa 1.2 g per impostare la curva verso sinistra. Si notano i trasferimenti di carico laterali che accompagnano l'accelerazione laterale di 1 g;
- Settore 8: segue una curva verso destra di accelerazione laterale di circa 1 g, si osserva come i pull anteriore e posteriore sinistri si vadano a caricare e, viceversa, quelli destri si scarichino;
- Settore 9: infine una curva verso sinistra riporta al rettilineo di partenza. L'accelerazione laterale è di circa 1 g e i trasferimenti di carico laterali evidenziano il fenomeno.

La presenza di segnali di drag negativi si spiega col fatto che in fase di decelerazione si generano forze di inerzia che sollecitano le celle di carico in direzione opposta a quella dei carichi di drag aerodinamici.

7.4 Prove circolari a velocità costante

7.4.1 Run sulla circonferenza di 6.125 metri

Infine, sono stati eseguiti dei run su circonferenze di raggio costante alla massima velocità percorribile. In figura 7.46 si possono vedere i dati acquisiti nel run eseguito sulla circonferenza di 6.125 metri in senso orario. In questa prova, come nelle altre di questo genere, i flap dell'ala anteriore e di quella posteriore erano in configurazione di "Alto carico".



Figura 7.46: Dati acquisiti nel run 32

La velocità media mantenuta nel run è di 25.6 km/h e l'accelerazione laterale di 0.76 g. I valori medi dei carichi aerodinamici registrati sono:

- Downforce dell'ala anteriore: 31.0 N,
- Drag dell'ala anteriore: -12.4 N,
- Downforce dell'ala posteriore: 22.0 N,
- Drag dell'ala posteriore: 12.4 N.

La presenza di un segnale di drag negativo all'anteriore non trova giustificazione in quanto la velocità era pressoché costante e non vi erano quindi forze di inerzia dirette nel verso opposto del moto. Questo risultato suggerisce di approfondire il comportamento delle celle se sollecitate a carichi laterali in quanto è probabile che i tiranti a "X" di supporto laterale inducano delle tensioni aggiuntive che vanno ad invalidare le matrici di taratura ottenute per puri carichi di downforce e di drag.

Per quanto riguarda i segnali dei pull e dei potenziometri si osserva il coerente trasferimento di carico alle ruote esterne.



In figura 7.47 vengono mostrati i dati relativi alla stessa prova eseguita in senso antiorario.

Figura 7.47: Dati acquisiti nel run 33

La velocità media mantenuta in questo run è di 26.0 km/h e l'accelerazione laterale di 0.79 g. I valori medi dei carichi aerodinamici registrati sono:

- Downforce dell'ala anteriore: 30.1 N, •
- Drag dell'ala anteriore: -8.2 N,
- Downforce dell'ala posteriore: 30.0 N,
- Drag dell'ala posteriore: 17.4 N.

La mancanza di simmetria tra i risultati dei run eseguiti in senso orario e di quelli eseguiti in senso antiorario non trova spiegazione e va motivata col fatto che va approfondito il comportamento delle celle di carico sottoposte a carichi laterali.

I comportamenti evidenziati nei run sulle circonferenze di 6.125 metri si ripetono anche per le circonferenze di 9.125 e 12.125 metri, per le quali valgono le stesse osservazioni qui esposte.

7.4.2 Run sulla circonferenza di 9.125 metri

Si passa successivamente a circonferenze di raggio 9.125 metri. In figura 7.48 vengono mostrati i dati relativi alla prova eseguita in senso orario.



Figura 7.48: Dati acquisiti nel run 34

La velocità media mantenuta nel run è di 32.9 km/h e l'accelerazione laterale di 0.96 g. I valori medi dei carichi aerodinamici registrati sono:

- Downforce dell'ala anteriore: 54.5 N,
- Drag dell'ala anteriore: -3.8 N,
- Downforce dell'ala posteriore: 36.4 N,
- Drag dell'ala posteriore: 24.0 N.

In figura 7.49 vengono mostrati i dati relativi alla stessa prova eseguita in senso antiorario.



Figura 7.49: Dati acquisiti nel run 35

La velocità media mantenuta nel run è di 34.0 km/h e l'accelerazione laterale di 0.98 g. I valori medi dei carichi aerodinamici registrati sono:

- Downforce dell'ala anteriore: 58.5 N,
- Drag dell'ala anteriore: 1.1 N,
- Downforce dell'ala posteriore: 51.9 N,
- Drag dell'ala posteriore: 33.1 N.

7.4.3 Run sulla circonferenza di 12.125 metri

Si passa infine a circonferenze di raggio 12.125 metri. In figura 7.50 vengono mostrati i dati relativi alla prova eseguita in senso orario.



Figura 7.50: Dati acquisiti nel run 37

La velocità media mantenuta nel run è di 39.9 km/h e l'accelerazione laterale di 1.04 g. I valori medi dei carichi aerodinamici registrati sono:

- Downforce dell'ala anteriore: 54.5 N,
- Drag dell'ala anteriore: -3.8 N,
- Downforce dell'ala posteriore: 36.4 N,
- Drag dell'ala posteriore: 24.0 N.

In figura 7.51 vengono mostrati i dati relativi alla stessa prova eseguita in senso antiorario.



Figura 7.51: Dati acquisiti nel run 37

La velocità media mantenuta nel run è di 39.7 km/h e l'accelerazione laterale di 1.01 g. I valori medi dei carichi aerodinamici registrati sono:

- Downforce dell'ala anteriore: 58.5 N,
- Drag dell'ala anteriore: 1.1 N,
- Downforce dell'ala posteriore: 51.9 N,
- Drag dell'ala posteriore: 33.1 N.

7.5 Discussione dei risultati ottenuti

Si inizia con il considerare i risultati ottenuti dalle prove condotte in rettilineo. L'obiettivo di queste prove era mantenere per almeno 3-5 secondi una velocità costante al fine di instaurare delle condizioni di stazionarietà ed ottenere misurazioni più accurate. Ciò non è stato possibile a causa del vento che cambiava repentinamente la sua intensità e di cui si è riuscito solo a stimare un valore medio rappresentativo di ciascun run, a causa della mappa del motore che doveva mantenere la velocità della vettura costante ma non sempre risultava efficace (in particolare nei run a 50 km/h) e a causa del limitato spazio disponibile che consentiva di mantenere le velocità di 70 e in particolare di 90 km/h solo per poco tempo. Questi fattori, combinati con la presenza di un asfalto non perfettamente pianeggiante, hanno impedito di poter osservare segnali sufficientemente costanti negli intervalli di interesse. In particolare, i segnali dei pull e delle corse sospensioni, che dovevano fornire una misura indiretta dei carichi aerodinamici, sono risultati eccessivamente influenzati dai trasferimenti di carico della vettura per dare riferimenti numericamente significativi.

Per quanto riguarda i segnali aerodinamici, per ciascun intervallo in cui la velocità era nominalmente costante, si sono calcolati i valori medi e poi si è calcolata la media dei tra valori medi calcolati per ogni run. Tenendo presente i fattori di disturbo sopra esposti e osservando il comportamento dei segnali negli intervalli di interesse, si è deciso in base a considerazioni ingegneristiche di associare ai risultati ottenuti un'incertezza di misura del 5%.

Per poter confrontare direttamente i risultati sperimentali ottenuti con quelli derivanti da analisi C.F.D., per ogni configurazione analizzata si sono interpolati i dati con funzioni paraboliche e successivamente si è calcolato il carico corrispondente alla velocità esatta di 50 km/h, in analogia ai dati delle simulazioni numeriche a disposizione. I risultati di tale interpolazioni sono mostrati in tabella 7.7, insieme al confronto con i dati C.F.D. già esposti nel capitolo 1.

	DOWNFORCE ANTERIORE [N]		DRAG ANTERIORE [N]		DOWNFORCE POSTERIORE [N]			DRAG POSTERIORE [N]				
CONFIG.	Sper.	Num.	Diff [%]	Sper.	Num.	Diff [%]	Sper.	Num.	Diff [%]	Sper.	Num.	Diff [%]
A.A.	127.6	105.1	21.4	21.1	19.7	7.0	83.1	87.0	-4.5	44.8	45.1	-0.6
M.M.	107.7	98.0	9.9	15.5	16.7	-7.2	76.8	86.6	-11.3	33.2	37.9	-12.4
B.B.	69.7	86.9	-19.8	10.5	12.3	-14.4	33.0	71.8	-54.0	19.2	29.4	-34.7
B.A.	65.1	90.4	-28.0	8.6	12.7	-32.5	93.2	89.5	4.2	46.1	45.1	2.2
M.A.	97.3	101.3	-3.9	16.4	17.5	-6.1	91.4	89.5	2.1	53.1	46.8	13.5

Tabella 7.7: Confronto tra risultati sperimentali e numerici alla velocità di 50 km/h

L'analisi degli errori percentuali tra i risultati sperimentali e quelli numerici mostra per la maggior parte dei dati delle differenze inferiori al 15 %. I dati maggiormente in disaccordo risultano quelli relativi alla configurazione di "Basso carico" in cui si raggiungono differenze del 54 e 34.7 % rispettivamente per la downforce e il drag dell'ala posteriore.

Le prove eseguite in tracciato hanno mostrato qualitativamente come le celle di carico permettano anche un'analisi dinamica delle sollecitazioni alle ali, qui risultano interessanti anche i segnali dei pull e delle corse delle sospensioni, dalle cui analisi si possono osservare chiaramente i trasferimenti di carico e ricostruire le dinamiche eseguite in pista dalla vettura.

Infine, i test eseguiti su circonferenze di raggio costante hanno evidenziato come siano necessarie ulteriori analisi relative al comportamento delle celle di carico sottoposte a carichi laterali, infatti la presenza del segnale di drag dell'ala anteriore negativo non trova spiegazione, così come la mancanza di simmetria tra i risultati in un senso di percorrenza della circonferenza e quelli nel senso opposto.
Capitolo 8: Conclusioni

Il lavoro di tesi ha avuto come obiettivo la progettazione di celle di carico per acquisire i carichi aerodinamici dell'ala anteriore e dell'ala posteriore della vettura Formula S.A.E. MG 13.18 del Race Up Team dell'Università di Padova.

A tal fine sono stati riprogettati i supporti ala anteriori e posteriori in modo da renderli sensibili alla forza di downforce e a quella di drag. Dopo la progettazione con l'utilizzo di strumenti C.A.D. e F.E.M., i supporti sono stati realizzati a partire da una lastra di Ergal tagliata ad acqua, sono stati applicati gli estensimetri per realizzare un ponte intero di Wheatstone per ogni canale e infine è stata eseguita una taratura statica per creare una corrispondenza tra deformazioni misurate e carichi applicati.

Infine, è stata organizzata una giornata di test per convalidare gli strumenti di misura realizzati e per acquisire dei dati sperimentali relativi all'aerodinamica della vettura.

In tabella 8.1 sono riassunti i risultati ottenuti.

CONFIGURAZIONE	VELOCITÀ	DOWNFORCE ANTERIORE [N]	DRAG ANTERIORE [N]	DOWNFORCE POSTERIORE [N]	DRAG POSTERIORE [N]
A.A.	31.2	46.2	7.7	31.5	18.8
A.A.	48.2	116.1	20	79.2	42.3
A.A.	71.6	279.2	44.1	170.1	89.2
A.A.	90.6	478.9	72.1	287.6	150.5
M.M.	31.1	44.1	6.3	29	12.1
M.M.	51	111.9	16.1	77.2	33.3
M.M.	70.5	212.3	30.7	157.7	68.2
M.M.	88.8	347.8	48.5	238.6	102.8
B.B.	32.1	29.6	3.9	13.6	7.5
B.B.	48.6	62	11.2	31.6	20.1
B.B.	68.9	140.8	18.4	61.8	33.6
B.B.	87	242.8	26.2	98.7	50.8
B.A.	32.8	27.75	3.4	42.8	19.6
B.A.	47.4	59.8	8.45	86.6	42.15
B.A.	70.3	128.3	15.9	174.1	91.8
B.A.	90	225.3	26.5	290.7	160.2
M.A.	32.1	39.45	6.25	36.8	21.4
M.A.	48.4	89.15	15.35	83.85	48.35
M.A.	71.8	208.5	32.9	192.8	110.6
M.A.	88.5	330.6	43.3	286.6	154.2

Tabella 8.1: Risultati ottenuti

I dati acquisiti consentiranno al reparto "Aerodinamica" del Race Up team di verificare la bontà delle simulazioni C.F.D. effettuate durante la fase di progettazione.

Il confronto tra dati sperimentali e numerici permette di correggere eventuali errori presenti nel modello matematico. È di fondamentale importanza avere a disposizione un modello matematico che dia risultati quanto più aderenti alla realtà, infatti condurre una campagna di acquisizione dati come quella presente in questo lavoro è troppo oneroso in termini di tempo e risorse per essere ripetibile con una certa frequenza. È molto più conveniente avere un solido modello numerico che, dopo validazione sperimentale, consenta in maniera rapida e poco costosa di ottenere i risultati voluti.

Avere a disposizione i carichi aerodinamici corretti è di vitale importanza al fine di prevedere il comportamento dinamico della vettura e di conseguenza poter progettare nella maniera più opportuna i componenti meccanici. Approfondendo il comportamento aerodinamico della vettura è possibile migliorare l'handling e in generale le prestazioni della vettura nelle prove dinamiche presenti agli eventi del circuito Formula S.A.E.

Un possibile sviluppo futuro del presente lavoro è eseguire un'altra campagna di acquisizione dati in un luogo più spazioso, che consenta di mantenere le velocità di 70 e 90 km/h per almeno 3-5 secondi al fine di avere risultati meno incerti. Un altro sviluppo possibile è l'investigazione del comportamento delle celle di carico

quando sono applicati carichi laterali, prima mediante acquisizioni statiche in laboratorio e poi con altri test tipo run circolari in pista.

Bibliografia

- [1] [2] Sito internet: https://it.wikipedia.org/wiki/Formula SAE
- De Lazzari M., Acquisizione e analisi dei carichi aerodinamici all'ala posteriore della vettura Formula S.A.E. MG 10.15, Università degli Studi di Padova, 2016
- Milan R., Development of a surrogate model of F.S.A.E. car, Università degli Studi di Padova, 2018 [3]
- [4] Kubit A., Kluz R., Trzepiecinski T., Wydrzynski D., Bochnowski W., Analysis of the mechanical properties and of micrographs of refill friction stir spot welded 7075-T6 aluminium sheets, University of Rzeszow, 2017
- [5] Petrone N., Strain gauge theroy application, Università degli Studi di Padova, dispensa del corso di Sport engineering and rehabilitation devices
- [6] [7] Kyowa, Strain gauge installation manual
- Chiarin R., Acquisizione dei carichi in pista e sviluppo di un banco per prove a fatica multiassiali su gruppi sospensione per vetture F.S.A.E., Università degli Studi di Padova, 2018
- [8] Meneghetti G., Saraceni M., Utilizzo della centralina IMC-CRONOS PL2, Università degli Studi di Padova, 2014
- [9] Meneghetti G., Zoccarato R., Utilizzo del modulo di espansione IMC-CANSAS abbinato alla centralina IMC-CRONOS PL2, Università degli Studi di Padova, 2016

Appendice A: Centro di pressione

Il centro di pressione (Center of pressure C.O.P.) di un profilo aerodinamico è il punto di applicazione della risultante di tutte le forze aerodinamiche applicate al profilo stesso, lì per definizione è nullo il momento risultante.

Il momento di una forza è definito come:

$$\vec{M} = \vec{r} \ x \ \vec{F} \tag{A.1}$$

Dove r è la distanza tra il punto di applicazione della forza e il polo attorno cui si calcola il momento e F è la forza risultante.

La precedente equazione può essere sviluppata scomponendo i vettori nelle loro componenti spaziali:

$$\begin{cases}
M_x = -F_y z + F_z y \\
M_y = F_x z - F_z x \\
M_z = -F_x y + F_y x
\end{cases}$$
(A.2)

Le analisi C.F.D. dei profili aerodinamici di una vettura consentono l'introduzione di ipotesi semplificative del problema in esame, ossia:

- Risultate F_y nulla (assenza di carichi laterali),
- Momenti M_x e M_z nulli.

Abbiamo a che fare a tutti gli effetti con un sistema piano di forze, le equazioni (A.2) quindi si semplificano nelle seguenti:

$$\begin{cases} M_y = F_x z - F_z x\\ y = 0 \end{cases}$$
(A.3)

In cui F_x è il drag e F_z è la downforce. Si nota come il sistema ha infinite soluzioni e il luogo dei punti descritto è una retta appartenente al piano di simmetria e orientata come la forza risultante.

Ecco spiegato perché, nonostante in letteratura si parli sempre di centro di pressione, è lecito parlare di retta luogo dei punti dei centri di pressione in quanto stiamo parlando di un caso piano di sistema di forze. Una volta nota tale retta, si può scegliere ad arbitrio un punto al fine di risolvere l'equilibrio della struttura ed eseguire ulteriori analisi.

Al fine di trovare la posizione del C.O.P. dalle simulazioni C.F.D., si calcola la forza risultante e il momento attorno l'asse y, poi si suppongono due valori di x del C.O.P. e si calcolano i rispettivi valori z, per tali punti passa la retta dei centri di pressione.

Nel caso della vettura MG 13.18, considerando una velocità di 100 km/h e la configurazione delle ali in alto carico, la retta dei centri di pressione dell'ala anteriore ha equazione y = 5.89x - 14386 e viene mostrata in figura A.1 con il sistema di riferimento considerato. Per quanto riguarda l'ala posteriore il luogo dei punti ha equazione y = 2.50x - 491 ed è mostrato in figura A.2.

Le equazioni di queste rette cambiano in funzione della velocità dell'aria e della configurazione dell'ala, le rette considerate per il dimensionamento sono quelle più gravose perché corrispondono a una velocità maggiore di quella massima prevista per i test (90 km/h) e a una configurazione di alto carico.

Una volta ottenute le linee dei C.O.P. è sufficiente scegliere arbitrariamente un punto su di esse da considerarsi come punto di applicazione delle forze risultanti e procedere con il dimensionamento.



Figura A.1: Linea dei C.O.P. dell'ala anteriore



Figura A.2: Linea dei C.O.P. dell'ala posteriore

Appendice B: Dati numerici della fase di taratura

Nelle seguenti tabelle vengono riportati i valori numerici ottenuti ed elaborati nella fase di taratura delle celle di carico. La colonna m indica la massa in kg applicata e la colonna P indica il corrispondente carico in N. Le ultime due colonne indicano la media dei segnali misurati negli intervalli in cui il carico era costante.

m[kg]	P[N]	L[με]	D[με]
0	0	0	0
0.528	5.180	32.439	-0.634
1.060	10.399	65.675	-1.695
1.592	15.618	98.682	-2.678
2.589	25.398	160.727	-4.345
4.589	45.018	284.705	-8.301
6.589	64.638	408.616	-12.265
9.942	97.531	616.610	-18.603
14.942	146.581	926.865	-27.031
18.305	179.572	1135.071	-31.989
20.307	199.212	1259.368	-34.261
21.304	208.992	1322.264	-35.307
22.302	218.783	1384.909	-35.818
21.304	208.992	1324.175	-34.528
20.307	199.212	1262.788	-33.502
18.305	179.572	1138.749	-31.168
14.942	146.581	930.311	-26.708
9.942	97.531	620.200	-19.149
6.589	64.638	412.184	-13.370
4.589	45.018	287.856	-9.491
2.589	25.398	163.247	-5.535
1.592	15.618	101.177	-3.781
1.060	10.399	67.840	-2.463
0.528	5.180	34.392	-1.236
0	0	0.501	0.006

Tabella B.1: Dati numerici relativi alla taratura a lift della cella di carico A.R.

m[kg]	P[N]	L[με]	D[με]
0	0	0	0
0.200	1.962	-0.024	48.628
0.728	7.142	0.002	177.897
1.260	12.361	-0.042	307.574
2.257	22.141	-0.030	550.405
3.254	31.922	-0.045	793.321
4.252	41.712	0.061	1036.390
4.784	46.931	0.081	1166.030
4.984	48.893	0.100	1214.526
4.784	46.931	0.184	1166.165
4.252	41.712	-0.060	1036.584

3.254	31.922	-0.210	793.833
2.257	22.141	-0.289	550.800
1.260	12.361	-0.462	307.774
0.728	7.142	-0.559	178.209
0.200	1.962	-0.618	49.273
0	0	0.002	0.901

Tabella B.2: Dati numerici relativi alla taratura a drag della cella di carico A.R.

m[kg]	P[N]	L[με]	D[με]
0	0	0	0
0.528	5.180	33.360	-0.793
1.060	10.399	67.070	-1.127
1.592	15.618	101.215	-1.599
2.589	25.398	164.950	-2.226
4.589	45.018	292.564	-3.391
6.589	64.638	419.950	-4.743
9.942	97.531	633.911	-6.360
14.942	146.581	952.132	-7.540
18.305	179.572	1165.700	-7.391
20.307	199.212	1293.408	-6.952
21.304	208.992	1357.154	-6.756
22.302	218.783	1420.970	-6.278
21.304	208.992	1357.365	-6.589
20.307	199.212	1294.134	-6.652
18.305	179.572	1166.760	-7.160
14.942	146.581	953.607	-7.125
9.942	97.531	635.401	-6.101
6.589	64.638	421.377	-4.250
4.589	45.018	294.195	-2.945
2.589	25.398	166.581	-1.633
1.592	15.618	102.941	-0.893
1.060	10.399	68.996	-0.490
0.528	5.180	34.961	-0.168
0	0	1.022	0.052

Tabella B.3: Dati numerici relativi alla taratura a lift della cella di carico A.L.

m[kg]	P[N]	L[με]	D[με]
0	0	0	0
0.200	1.962	0.046	51.034
0.728	7.142	-0.086	186.254
1.260	12.361	-0.476	322.699
2.257	22.141	-0.824	578.066
3.254	31.922	-1.200	833.428
4.252	41.712	-1.385	1088.878
4.784	46.931	-1.629	1225.260
4.984	48.893	-1.555	1276.360

4.784	46.931	-1.496	1225.476
4.252	41.712	-1.369	1089.482
3.254	31.922	-1.134	834.142
2.257	22.141	-0.814	579.043
1.260	12.361	-0.394	323.529
0.728	7.142	-0.037	187.138
0.200	1.962	0.113	51.703
0	0	0.002	0.563

|--|

m[kg]	P[N]	L[µɛ]	D[με]
0	0	0	0
0.997	9.781	72.866	0.655
2.997	29.401	218.897	3.085
4.997	49.021	364.971	5.074
8.350	81.914	610.159	9.275
13.350	130.964	974.989	17.974
16.713	163.955	1219.680	24.815
18.713	183.575	1365.549	29.036
19.710	193.355	1438.564	31.141
18.713	183.575	1365.790	29.584
16.713	163.955	1220.099	26.082
13.350	130.964	975.455	20.230
8.350	81.914	610.472	11.454
4.997	49.021	365.379	6.364
2.997	29.401	219.251	3.625
0.997	9.781	73.321	1.415
0	0	0.952	0.022

Tabella B.5: Dati numerici relativi alla taratura a lift della cella di carico P.R.

m[kg]	P[N]	L[με]	D[με]
0	0	0	0
0.528	5.180	-0.071	71.242
1.525	14.960	-0.557	203.255
2.522	24.741	-1.241	336.962
4.522	44.361	-2.398	605.125
6.522	63.981	-3.109	872.059
8.524	83.620	-3.556	1139.403
9.522	93.411	-3.995	1273.061
10.054	98.630	-4.469	1344.339
9.522	93.411	-4.471	1273.698
8.524	83.620	-3.691	1139.833
6.522	63.981	-2.750	871.757
4.522	44.361	-1.874	604.354
2.522	24.741	-0.832	336.393
1.525	14.960	-0.363	202.675

0.528	5.180	0.487	69.171
0	0	0.010	0.650

Tabella B.6: Dati numerici relativi alla taratura a drag della cella di carico P.R.

m[kg]	P[N]	L[με]	D[με]
0	0	0	0
0.997	9.781	77.350	-0.351
2.997	29.401	232.215	0.185
4.997	49.021	386.772	1.820
8.350	81.914	646.064	5.264
13.350	130.964	1032.657	13.927
16.713	163.955	1292.645	21.655
18.713	183.575	1447.405	27.138
19.710	193.355	1524.762	30.146
18.713	183.575	1447.789	28.613
16.713	163.955	1293.018	24.635
13.350	130.964	1033.316	18.050
8.350	81.914	646.814	9.497
4.997	49.021	387.727	4.783
2.997	29.401	233.139	2.678
0.997	9.781	78.290	0.981
0	0	0.701	-0.054

Tabella B.7: Dati numerici relativi alla taratura a lift della cella di carico P.L.

m[kg]	P[N]	L[με]	D[με]
0	0	0	0
0.528	5.180	-0.235	68.603
1.525	14.960	-0.262	196.445
2.522	24.741	-0.647	324.333
4.522	44.361	-3.497	580.906
6.522	63.981	-5.034	836.631
8.524	83.620	-6.274	1091.783
9.522	93.411	-7.203	1219.898
10.054	98.630	-7.529	1288.329
9.522	93.411	-6.781	1219.917
8.524	83.620	-5.789	1091.915
6.522	63.981	-4.154	835.639
4.522	44.361	-2.804	579.838
2.522	24.741	-1.273	323.735
1.525	14.960	-0.451	195.612
0.528	5.180	0.266	67.384
0	0	0.005	0.557

Tabella B.8: Dati numerici relativi alla taratura a drag della cella di carico P.L.

Appendice C: Codici Famos

Si riporta il codice utilizzato nel software "imc FAMOS Enterprise 7.3" per elaborare i segnali acquisiti con le centraline.

;Filtraggio dei dati con filtro passa basso di secondo grado, frequenza di taglio 1 Hz freq_taglio=1

grado=2 filter AR LIFT=FiltLP(AR LIFT,0,0,grado,freq taglio) filter AR DRAG=FiltLP(AR DRAG,0,0,grado,freq taglio) filter AL LIFT=FiltLP(AL LIFT,0,0,grado,freq taglio) filter AL DRAG=FiltLP(AL DRAG,0,0,grado,freq taglio) filter PR LIFT=FiltLP(PR LIFT,0,0,grado,freq taglio) filter_PR_DRAG=FiltLP(PR_DRAG,0,0,grado,freq_taglio) filter PL LIFT=FiltLP(PL LIFT,0,0,grado,freq taglio) filter_PL_DRAG=FiltLP(PL_DRAG,0,0,grado,freq_taglio) filter_PULL_AR=FiltLP(PULL_AR,0,0,grado,freq_taglio) filter PULL AL=FiltLP(PULL AL,0,0,grado,freq taglio) filter PULL PR=FiltLP(PULL PR,0,0,grado,freq taglio) filter PULL PL=FiltLP(PULL PL,0,0,grado,freq taglio) corsa POT AR=FiltLP(POT AR,0,0,grado,freq taglio) corsa_POT_AL=FiltLP(POT_AL,0,0,grado,freq_taglio) corsa_POT_PR=FiltLP(POT_PR,0,0,grado,freq_taglio) corsa POT PL=FiltLP(POT PL,0,0,grado,freq taglio)

;Applicazione delle matrici di taratura ai segnali delle celle di carico aerodinamiche forza_AR_LIFT=0.001*(157.9*FILTER_AR_LIFT+0.00445*FILTER_AR_DRAG) forza_AR_DRAG=0.001*(1.102*FILTER_AR_LIFT+40.24*FILTER_AR_DRAG) forza_AL_LIFT=0.001*(153.92*FILTER_AL_LIFT+0.197*FILTER_AL_DRAG) forza_AL_DRAG=0.001*(0.23*FILTER_AL_LIFT+38.3*FILTER_AL_DRAG) forza_PR_LIFT=0.001*(134.36*FILTER_PR_LIFT+0.442*FILTER_PR_DRAG) forza_PR_DRAG=0.001*(-1.509*FILTER_PR_LIFT+73.37*FILTER_PR_DRAG) forza_PL_LIFT=0.001*(126.78*FILTER_PL_LIFT+0.703*FILTER_PL_DRAG) forza_PL_DRAG=0.001*(-1.342*FILTER_PL_LIFT+76.54*FILTER_PL_DRAG)

;Calcolo della forza aerodinamica risultante alle ali forza_ANT_LIFT=(forza_AR_LIFT+forza_AL_LIFT) forza_ANT_DRAG=(forza_AR_DRAG+forza_AL_DRAG) forza_POST_LIFT=(forza_PR_LIFT+forza_PL_LIFT) forza_POST_DRAG=(forza_PR_DRAG+forza_PL_DRAG)

;Applicazione del fattore di taratura ai segnali dei pull forza_PULL_AR=5.9241*filter_PULL_AR forza_PULL_AL=5.9241*filter_PULL_AL forza_PULL_PR=5.9241*filter_PULL_PR forza_PULL_PL=5.9241*filter_PULL_PL

;Eliminazione delle variabili inutili delete AR_LIFT delete AR_DRAG delete AL_LIFT delete AL_DRAG delete PR_LIFT delete PR_DRAG delete PL_LIFT delete PL_LIFT delete PULL_AR delete PULL_AL delete PULL_PR delete PULL_PL delete POT_AR delete POT_AL delete POT_PR delete POT_PL

;Azzeramento canali

c=0 forza_ANT_LIFT=forza_ANT_LIFT-Value(forza_ANT_LIFT,c) forza_ANT_DRAG=forza_ANT_DRAG-Value(forza_ANT_DRAG,c) forza_POST_LIFT=forza_POST_LIFT-Value(forza_POST_DRAG,c) forza_POST_DRAG=forza_POST_DRAG-Value(forza_POST_DRAG,c) forza_PULL_AR=forza_PULL_AR-Value(forza_PULL_AR,c) forza_PULL_AL=forza_PULL_AL-Value(forza_PULL_AL,c) forza_PULL_PR=forza_PULL_PR-Value(forza_PULL_PR,c) forza_PULL_PL=forza_PULL_PL-Value(forza_PULL_PL,c) corsa_POT_AR=corsa_POT_AR-Value(corsa_POT_AR,c) corsa_POT_AL=corsa_POT_AL-Value(corsa_POT_AL,c) corsa_POT_PR=corsa_POT_PR-Value(corsa_POT_PR,c) corsa_POT_PL=corsa_POT_PL-Value(corsa_POT_PL,c)